

N-Ch 40V Fast Switching MOSFETs



AEC-Q101 Qualified

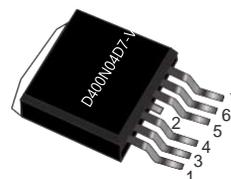
Features:

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- High Current Capability
- Green Device Available
- AEC-Q101 qualified and PPAP capable

Applications:

- SMPS Synchronous Rectification
- DC/DC Converters
- Or-ing

TO-263-7L Pin Configuration

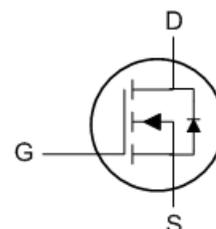


Product Summary

BVDSS	RDSON	ID
40V	1.1mΩ	400A

Pin Definition:

1. Gate
2. Drain
- 3/4/5/6/7. Source



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V (Silicon Limited)	400	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, V_{GS} @ 10V (Silicon Limited)	280	A
$I_D@T_C = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V (Wire Bond Limited)	240	A
I_{DM}	Pulsed Drain Current ²	1610	A
EAS	Single Pulse Avalanche Energy ³	673	mJ
I_{AS}	Avalanche Current	116	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	380	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	40	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.4	°C/W

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=195A$	---	0.9	1.1	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	2.8	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=32V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	μA
		$V_{DS}=32V, V_{GS}=0V, T_J=55^\circ C$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	1.2	---	Ω
Q_g	Total Gate Charge	$V_{DS}=20V, V_{GS}=10V, I_D=20A$	---	108	---	nC
Q_{gs}	Gate-Source Charge		---	25.4	---	
Q_{gd}	Gate-Drain Charge		---	26.8	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=20V, V_{GS}=10V, R_G=1.5\Omega, I_D=20A$	---	20	---	ns
T_r	Rise Time		---	145	---	
$T_{d(off)}$	Turn-Off Delay Time		---	55	---	
T_f	Fall Time		---	18	---	
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, f=1MHz$	---	6601	---	pF
C_{oss}	Output Capacitance		---	2073	---	
C_{rSS}	Reverse Transfer Capacitance		---	248	---	
Diode Characteristics						
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V, \text{Force Current}$	---	---	240	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=195A, T_J=25^\circ C$	---	---	1.2	V

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=240A$
- The power dissipation is limited by 175 $^\circ\text{C}$ junction temperature
- Package limitation current is 240 A.

Typical Characteristics

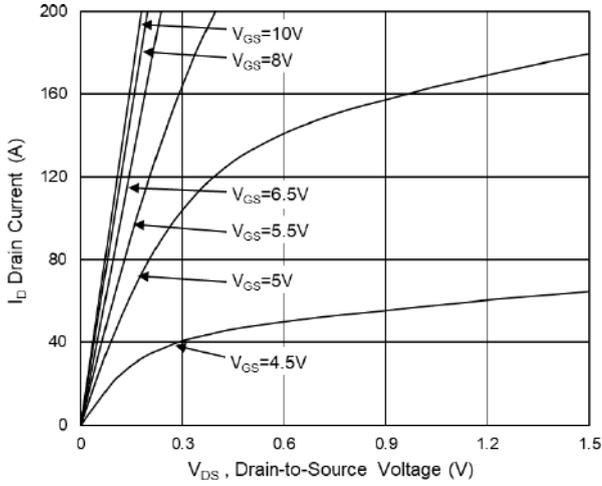


Fig.1 Typical Output Characteristics

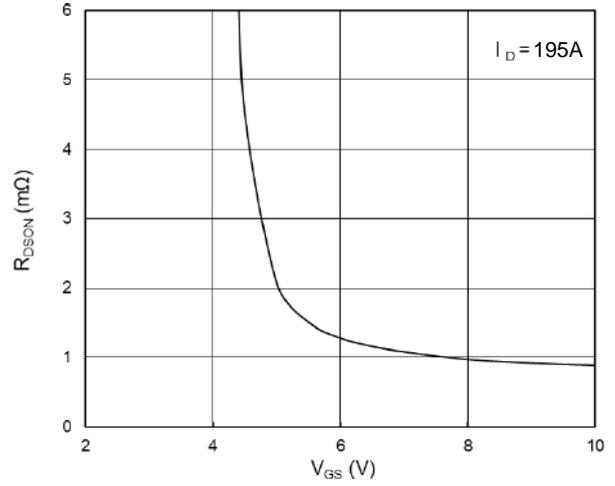


Fig.2 On-Resistance vs G-S Voltage

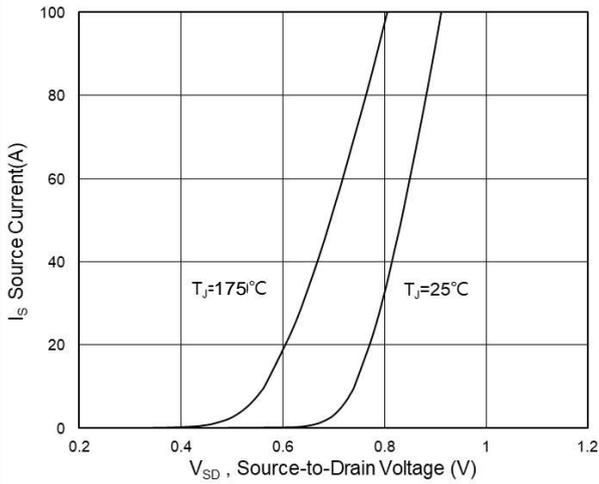


Fig.3 Source Drain Forward Characteristics

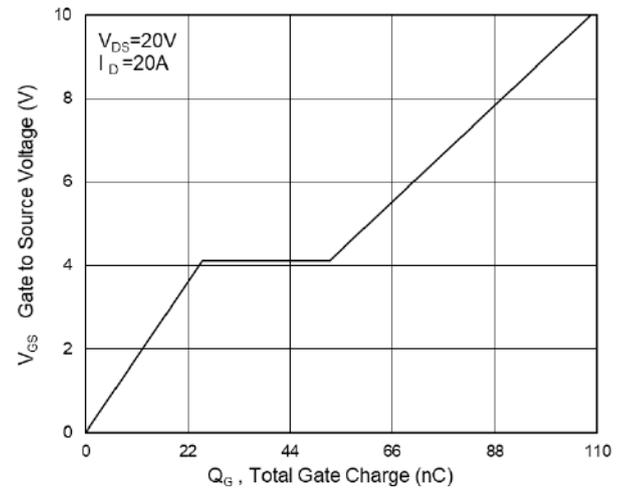


Fig.4 Gate-Charge Characteristics

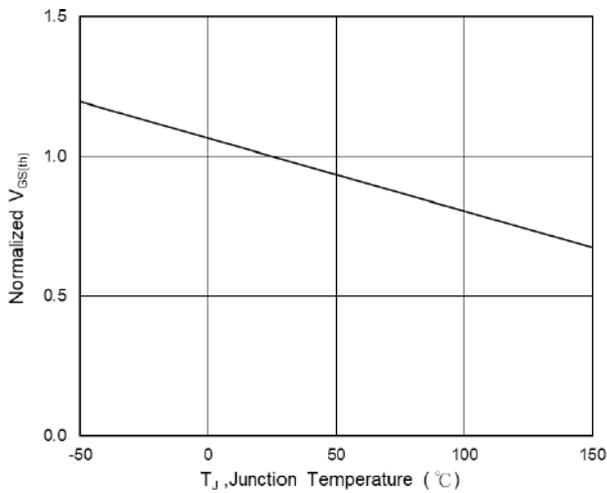


Fig.5 Normalized $V_{GS(th)}$ vs T_J

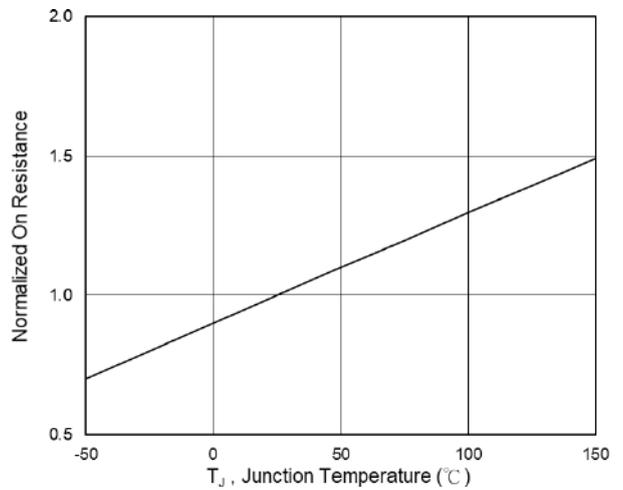


Fig.6 Normalized $R_{DS(on)}$ vs T_J

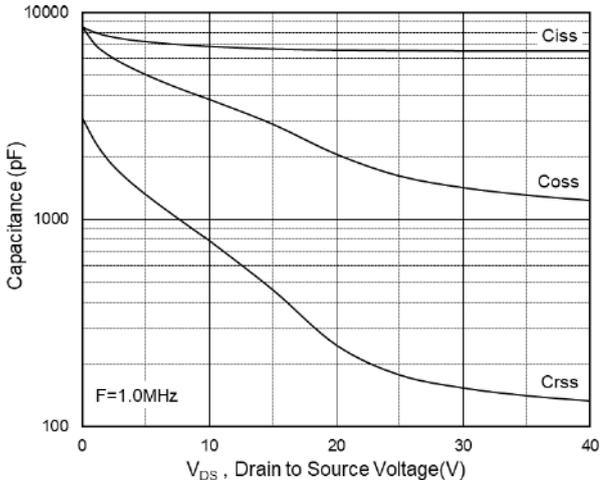


Fig.7 Capacitance

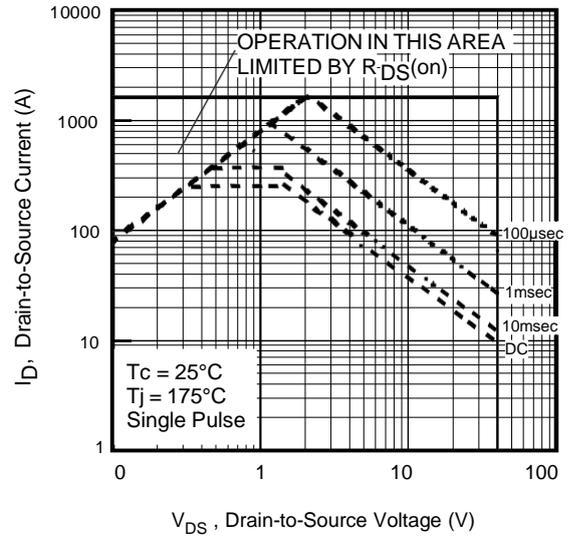


Fig.8 Safe Operating Area

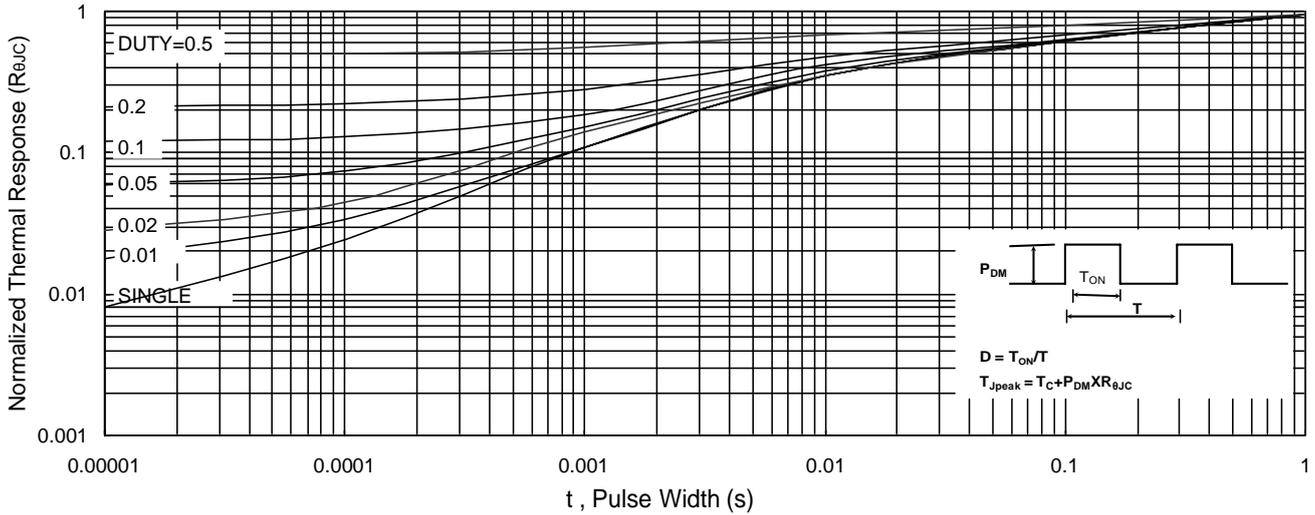


Fig.9 Normalized Maximum Transient Thermal Impedance

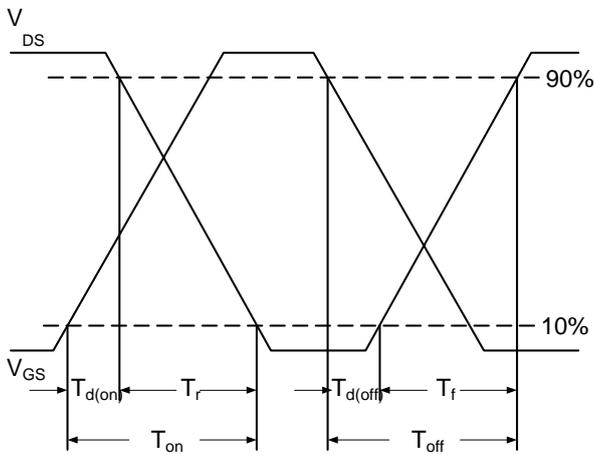


Fig.10 Switching Time Waveform

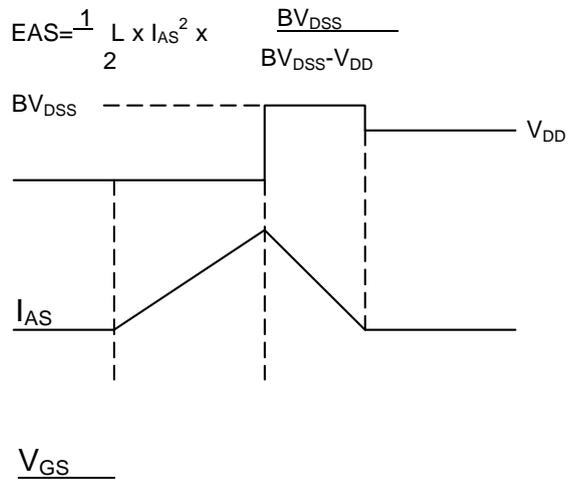
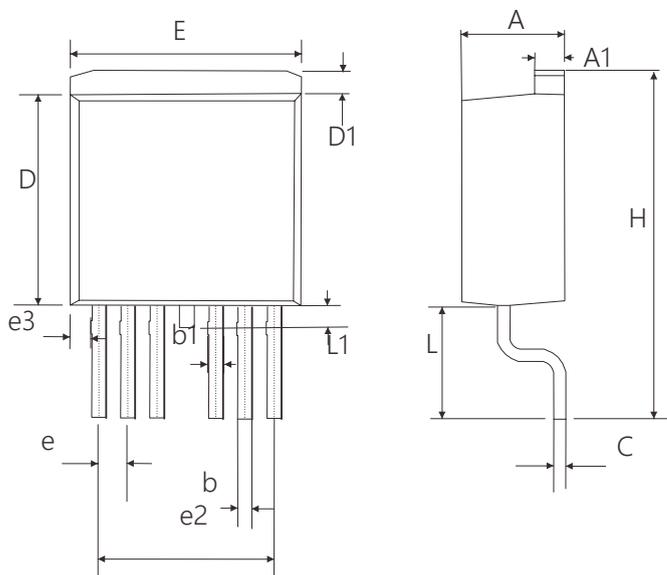


Fig.11 Unclamped Inductive Switching Waveform

TO-263-7L Outline



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.25	4.75	0.167	0.187
A1	1.2	1.4	0.047	0.055
b	0.5	0.7	0.020	0.028
b1	0.5	0.9	0.020	0.035
C	0.4	0.6	0.016	0.024
D	9.05	9.45	0.356	0.372
D1	0.7	1.3	0.028	0.051
E	9.8	10.2	0.386	0.402
e	1.07	1.47	0.042	0.058
e2	7.32	7.92	0.288	0.312
e3	0.64	1.04	0.025	0.041
H	14.65	15.65	0.577	0.616
L	4.47	5.47	0.176	0.215
L1	0.90	1.50	0.035	0.059