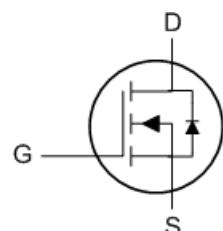
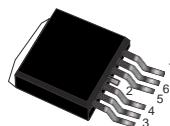


## N-Ch 100 V Fast Switching MOSFETs

### Features :

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Fast Switching Speed
- Green Device Available

### TO263-7 Pin Configuration



Pin Definition:

1. Gate
2. Drain

3/4/5/6/7. Source

### Applications :

- Power Tools.
- Motor Control.
- UPS
- Synchronous Rectification in SMPS

### Product Summary

BVDSS	RDS(on)	ID
100V	2.2mΩ	308A

Table1 Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ , unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current <sup>1,6</sup>	308	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current <sup>1,6</sup>	218	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	550	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	1012.5	mJ
$I_{AS}$	Avalanche Current	45	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation <sup>4</sup>	429	W
$T_{STG}$	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 175	$^\circ\text{C}$

Table 2.Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	60	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	0.35	$^\circ\text{C}/\text{W}$

Table 3. Electrical Characteristics ( $T_J=25^\circ C$ , unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=30A$	---	1.8	2.2	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	---	4	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=80V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	$\mu A$
		$V_{DS}=80V, V_{GS}=0V, T_J=100^\circ C$	---	---	100	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=20A$	---	75	---	S
$Q_g$	Total Gate Charge	$V_{DS}=50V, V_{GS}=10V, I_D=20A$	---	200	---	nC
$Q_{gs}$	Gate-Source Charge		---	53.3	---	
$Q_{gd}$	Gate-Drain Charge		---	49	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V, V_{GS}=10V, R_G=3\Omega, I_D=20A$	---	47	---	ns
$T_r$	Rise Time		---	28	---	
$T_{d(off)}$	Turn-Off Delay Time		---	79	---	
$T_f$	Fall Time		---	18	---	
$C_{iss}$	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, f=1MHz$	---	13362	---	pF
$C_{oss}$	Output Capacitance		---	1917	---	
$C_{rss}$	Reverse Transfer Capacitance		---	387	---	

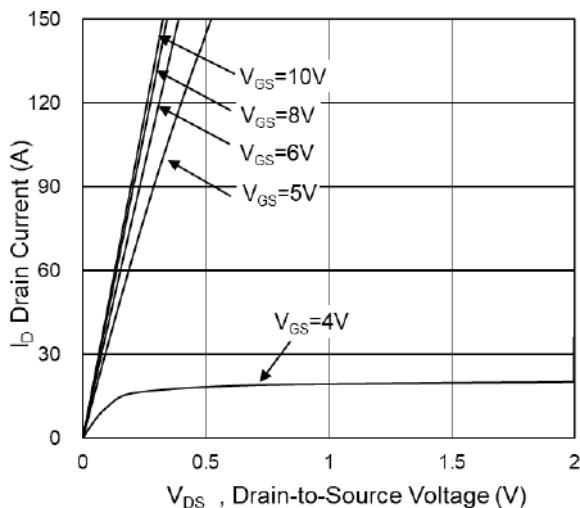
#### Diode Characteristics

$I_s$	Continuous Source Current <sup>1,5,6</sup>	$V_G=V_D=0V$ , Force Current	---	---	80	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_s=1A, T_J=25^\circ C$	---	---	1.1	V
$t_{rr}$	Reverse Recovery Time	$I_F=20A, di/dt=100A/\mu s, T_J=25^\circ C$	---	70	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	580	---	nC

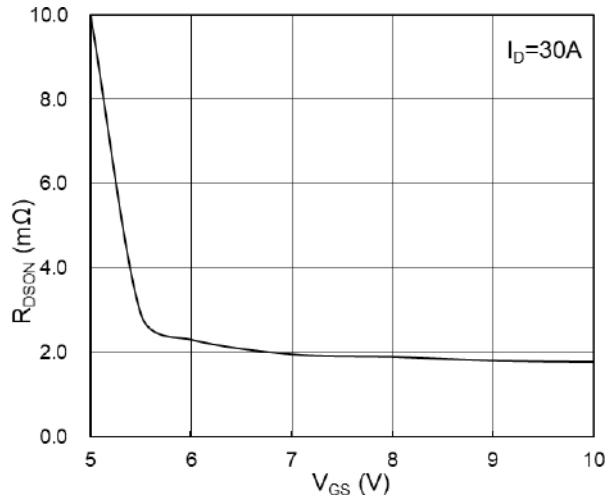
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=50V, V_{GS}=10V, L=1.0mH, I_{AS}=45A$
- 4.The power dissipation is limited by  $175^\circ C$  junction temperature
- 5.The data is theoretically the same as  $I_b$  and  $I_s$  , in real applications , should be limited by total power dissipation.
6. Bonding wire limitation current is 120A.

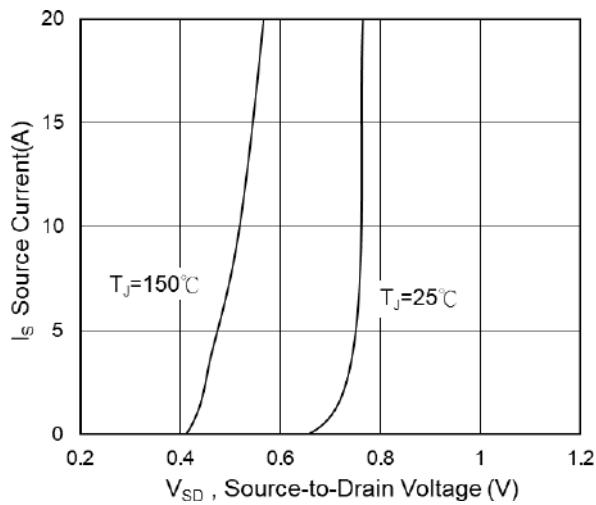
### Typical Characteristics



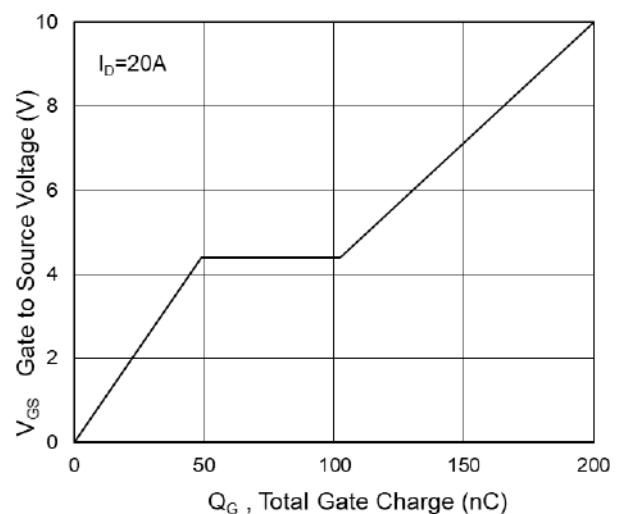
**Fig.1 Typical Output Characteristics**



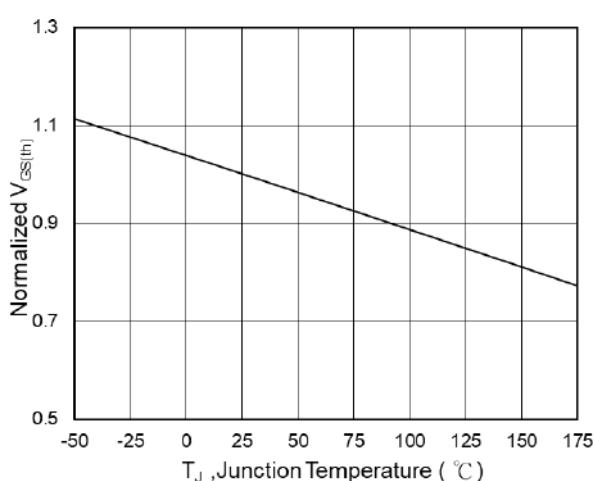
**Fig.2 On-Resistance vs G-S Voltage**



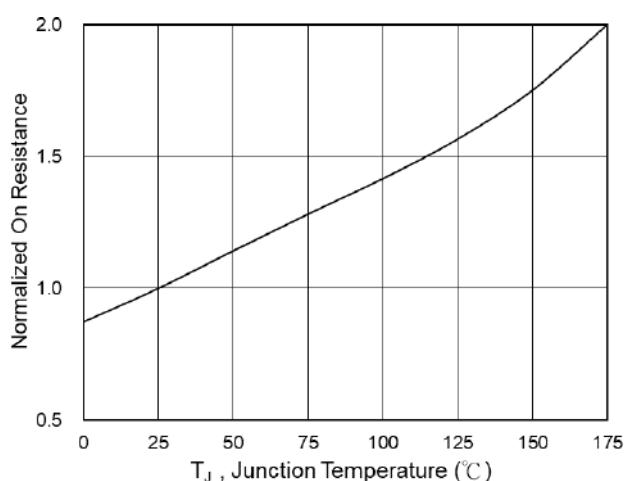
**Fig.3 Source-Drain Forward Characteristics**



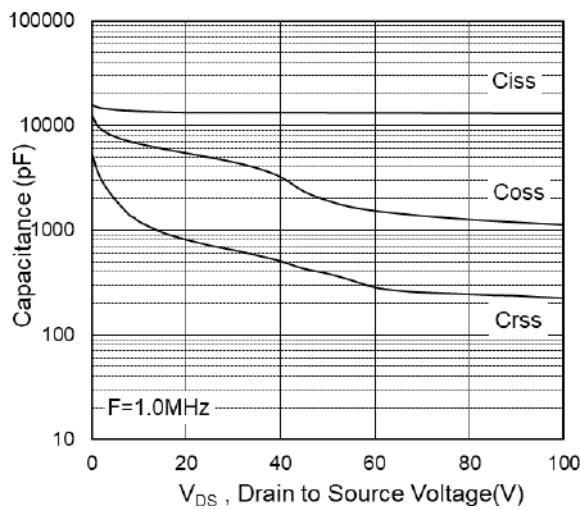
**Fig.4 Gate-Charge Characteristics**



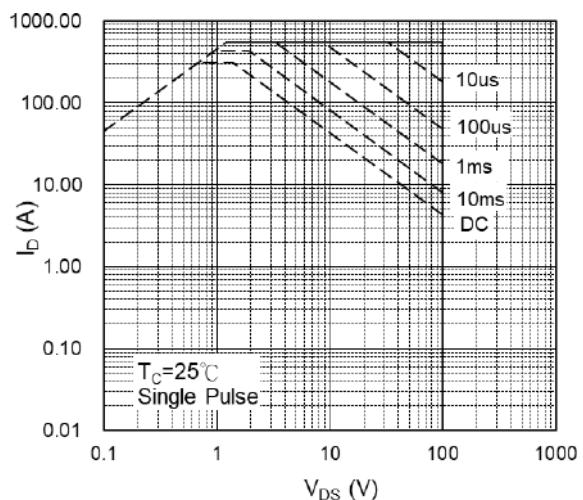
**Fig.5 Normalized V<sub>GS(th)</sub> vs T<sub>J</sub>**



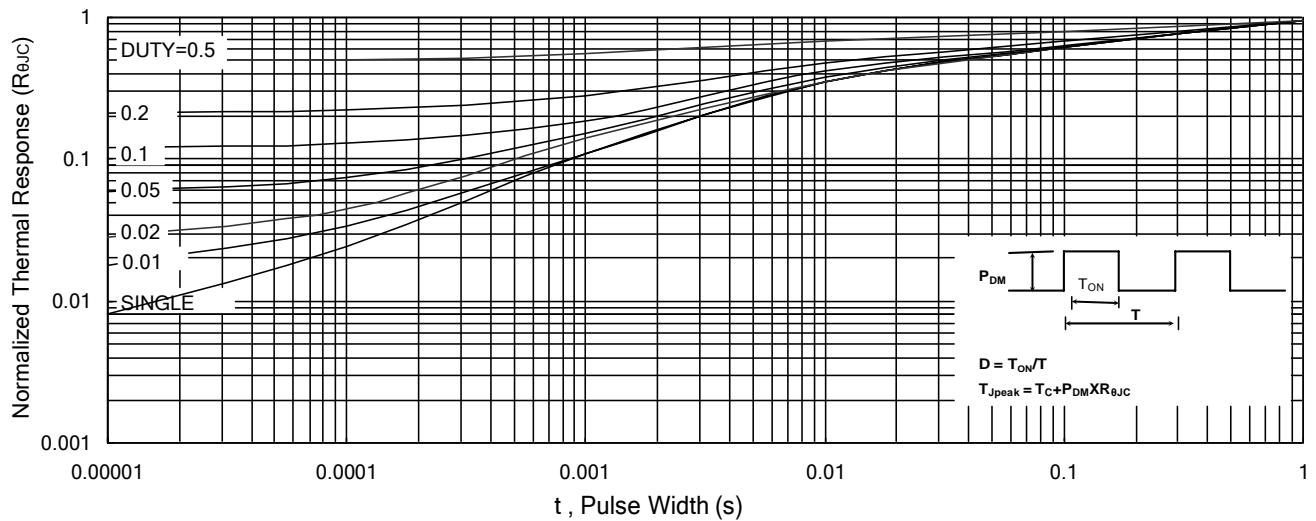
**Fig.6 Normalized R<sub>DS(on)</sub> vs T<sub>J</sub>**



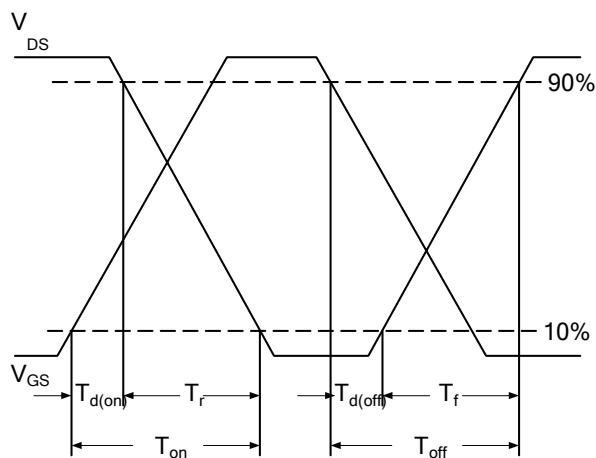
**Fig.7 Capacitance**



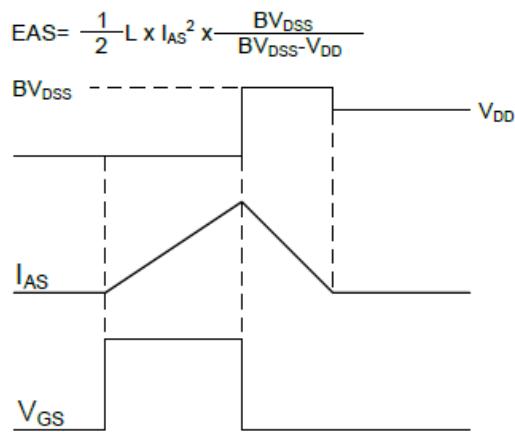
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



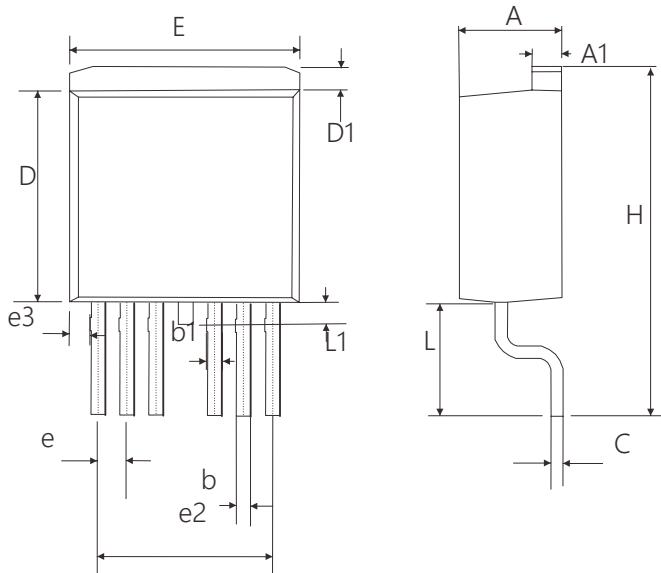
**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

## Dimensions

### TO-263-7L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.25	4.75	0.167	0.187
A1	1.2	1.4	0.047	0.055
b	0.5	0.7	0.020	0.028
b1	0.5	0.9	0.020	0.035
C	0.4	0.6	0.016	0.024
D	9.05	9.45	0.356	0.372
D1	0.7	1.3	0.028	0.051
E	9.8	10.2	0.386	0.402
e	1.07	1.47	0.042	0.058
e2	7.32	7.92	0.288	0.312
e3	0.64	1.04	0.025	0.041
H	14.65	15.65	0.577	0.616
L	4.47	5.47	0.176	0.215
L1	0.90	1.50	0.035	0.059