

# SILICON CARBIDE SCHOTTKY DIODE

**Reverse Voltage - 1200 Volts**

**Forward Current - 6Amperes**

## Description

SiC Schottky Diode has no switching loss, provides improved system efficiency against Si diodes by utilizing new semiconductor material-Silicon Carbide, enables higher operating frequency, and helps increasing power density and reduction of system size /cost. Its high reliability ensures robust operation during surge or over\_voltage conditions.

## Features

- Max Junction Temperature 175° C
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery

## Mechanical Data

- Case: JEDEC TO-220AC/ITO-220AC/TO-263/TO-252
- Molding compound meets UL94V-0 flammability rating
- Terminals: Lead solderable per J-STD-002 and JESD22-B102
- Polarity: As marked
- Mounting Torque: 10 in-lbs maximum

## Typical Applications

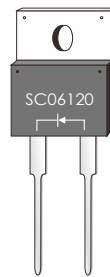
- General Purpose
- SMPS, Solar inverter, UPS
- Power Switching Circuits

## Key Performance And Package Parameters

Type	V <sub>DC</sub>	I <sub>F</sub>	Q <sub>c</sub>	T <sub>J,max</sub>	Package
KWSC06120	1200V	6A	19nC	175°C	TO-220AC
KWSC06120F	1200V	6A	19nC	175°C	ITO-220AC
KWSC06120D2	1200V	6A	19nC	175°C	TO-263
KWSC06120M2	1200V	6A	19nC	175°C	TO-252

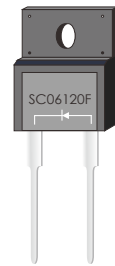
**TO-220AC**

**KWSC06120**



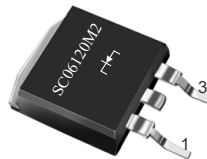
**ITO-220AC**

**KWSC06120F**



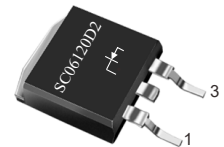
**TO-252**

**KWSC06120M2**



**TO-263**

**KWSC06120D2**



## Maximum Ratings

(Ratings at 25° C ambient temperature unless otherwise specified )

Parameter	Symbol	Value	Unit
Maximum repetitive peak reverse voltage	$V_{RRM}$	1200	V
Continuous Forward Current for $R_{th(j-c)}$	$I_F$	20 ( $T_c \leq 25^\circ C$ TO -220/TO-263) 14 ( $T_c \leq 25^\circ C$ TO-252/ITO-220) 6 ( $T_c \leq 156^\circ C$ TO-220/TO-263) 6 ( $T_c \leq 135^\circ C$ TO-252/ITO-220)	A
Non-Repetitive Forward Surge Current (Half-Sine Pulse, $t_p=8.3ms$ )	$I_{FSM}$	60 ( $25^\circ C$ ) 52 ( $150^\circ C$ )	A
$I^2t$ value	$\int i^2t$	15 ( $25^\circ C$ ) 11.2 ( $150^\circ C$ )	A <sup>2</sup> S
Diode dv/dt ruggedness( $V_R=0...650V$ )	dv/dt	80	V/nS
Power dissipation for $R_{th(j-c,max)}$ ( $T_c=25^\circ C$ )	$P_{tot}$	125(TO-220/TO-263) 60(TO-252/ITO-220)	W
Operating junction temperature range	$T_j$	-55...175	°C
Storage temperature range	$T_{stg}$	-55...175	°C

## Thermal Characteristics

Parameter	Symbol	ITO-220AC	TO-220AC	TO-263	TO-252	Unit
Diode thermal resistance junction-case	$R_{th(j-c)}$	2.5	1.2	1.2	2.5	K/W

Electrical Characteristics (  $T_A=25^{\circ}\text{C}$  Unless otherwise noted)

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
DC blocking voltage	$V_{DC}$	$T_j=25\dots175^{\circ}\text{C}$	1200			V
Diode forward voltage	$V_F$	$I_F=6\text{A } T_j=25^{\circ}\text{C}$ $I_F=6\text{A } T_j=125^{\circ}\text{C}$ $I_F=6\text{A } T_j=175^{\circ}\text{C}$		1.5 1.7 2.0	1.8 2.0 2.3	V
Reverse current	$I_R$	$V_R=1200\text{V } T_j=25^{\circ}\text{C}$ $V_R=1200\text{V } T_j=125^{\circ}\text{C}$ $V_R=1200\text{V } T_j=175^{\circ}\text{C}$			20 100 200	$\mu\text{A}$

Dynamic Characteristics (  $T_j=25^{\circ}\text{C}$ , unless otherwise specified)

Parameter	Symbol	conditions	Value			Unit
			min	typ	max	
Total capacitive charge	$Q_C$	$V_R=1200\text{V}, I_F=6\text{A}$ $di/dt=200\text{A}/\mu\text{s}$ $T_j=25^{\circ}\text{C}$		19		nC
Total capacitance	$C$	$V_R=0\text{V}, f=1\text{MHz}$ $V_R=400\text{V}, f=1\text{MHz}$ $V_R=800\text{V}, f=1\text{MHz}$ $T_j=25^{\circ}\text{C}$		385 28 22		pF

FIG.1-FORWARD CURRENT DERATING CURVE

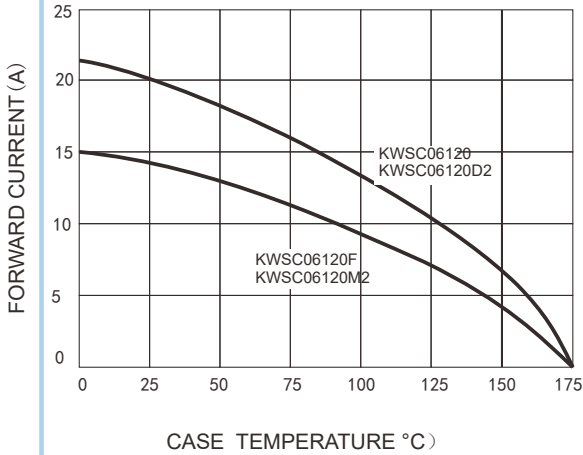


FIG.2-TYPICAL JUNCTION CAPACITANCE

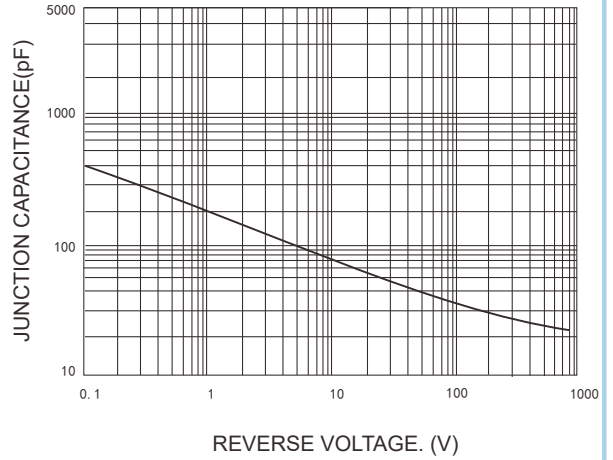


FIG.3-FORWARD CHARACTERISTICS

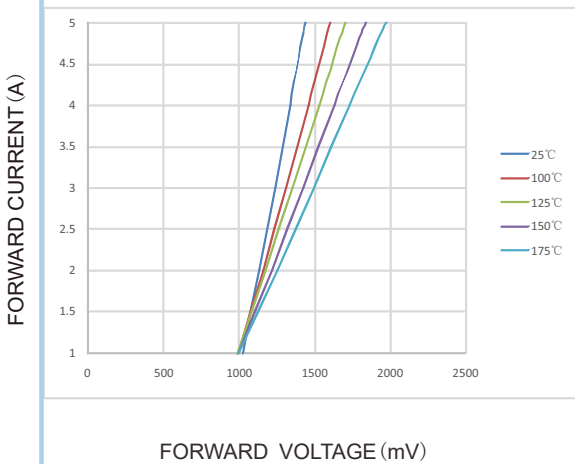


FIG.4-REVERSE CHARACTERISTICS

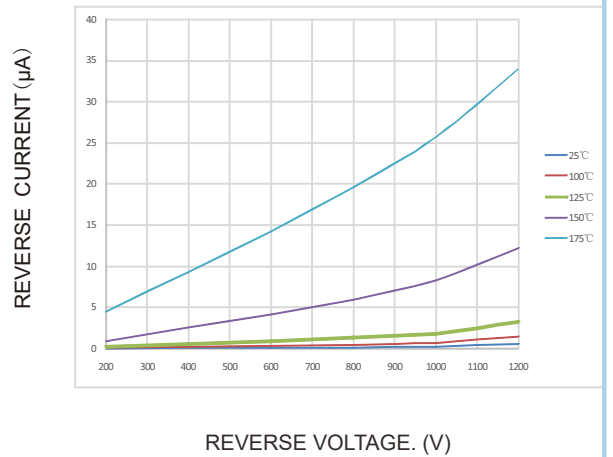
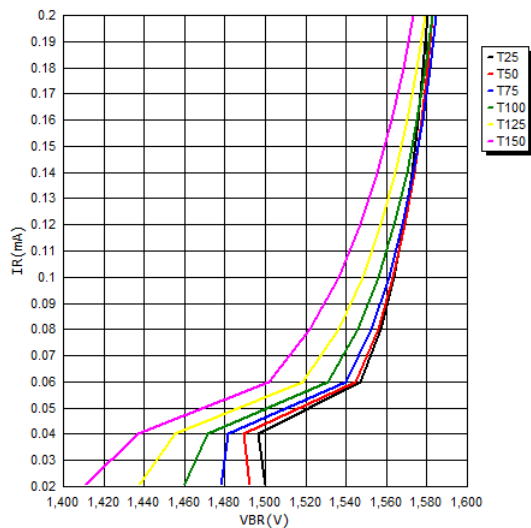
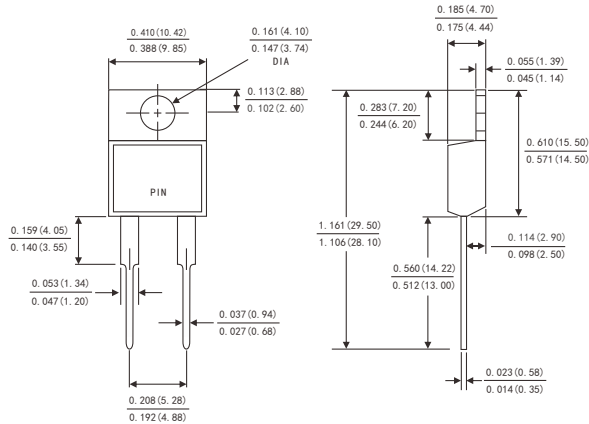


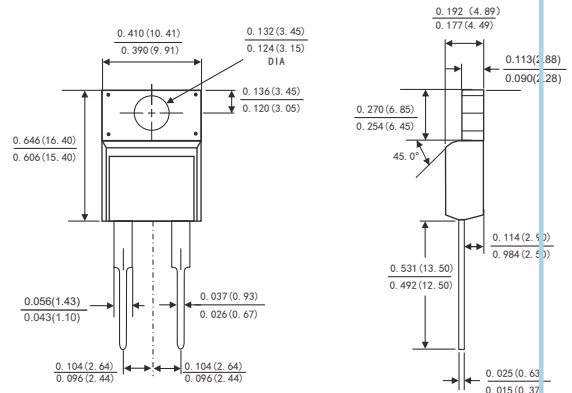
FIG.5-REVERSE CHARACTERISTICS (IR:0.02-0.2mA)



**TO-220AC**



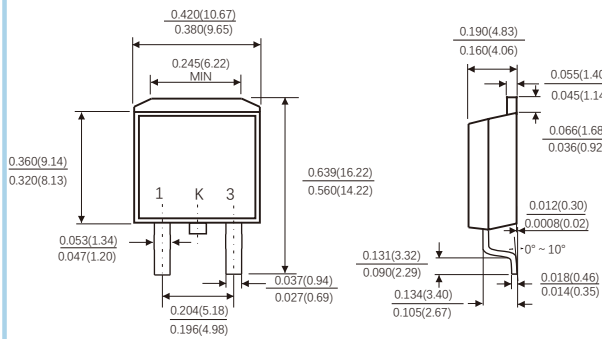
**ITO-220AC**



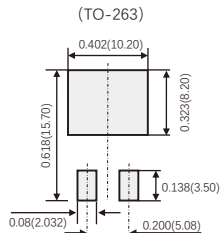
Dimensions in inches and (millimeters)

Dimensions in inches and (millimeters)

**TO-263**

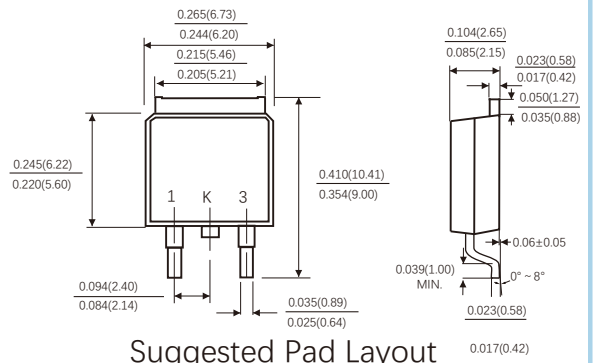


**Suggested Pad Layout**

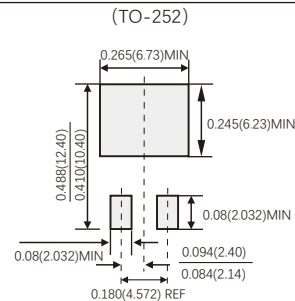


(Designers can refer to the recommended values according to the manufacturing process requirements to determine the appropriate pad size)

**TO-252**



**Suggested Pad Layout**

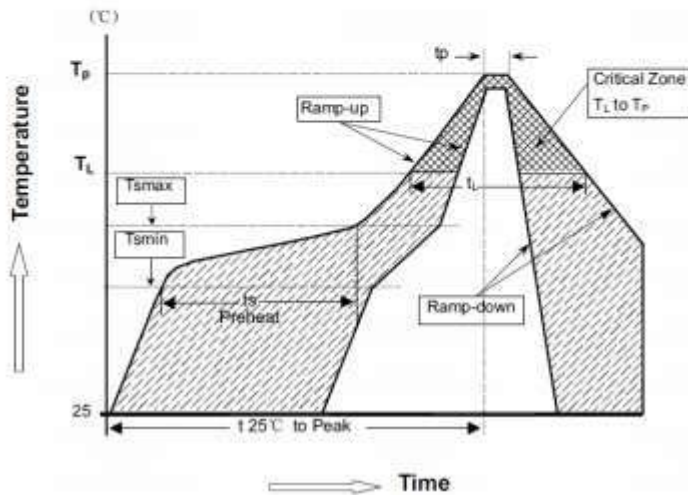


(Designers can refer to the recommended values according to the manufacturing process requirements to determine the appropriate pad size)

Recommended of soldering condition.

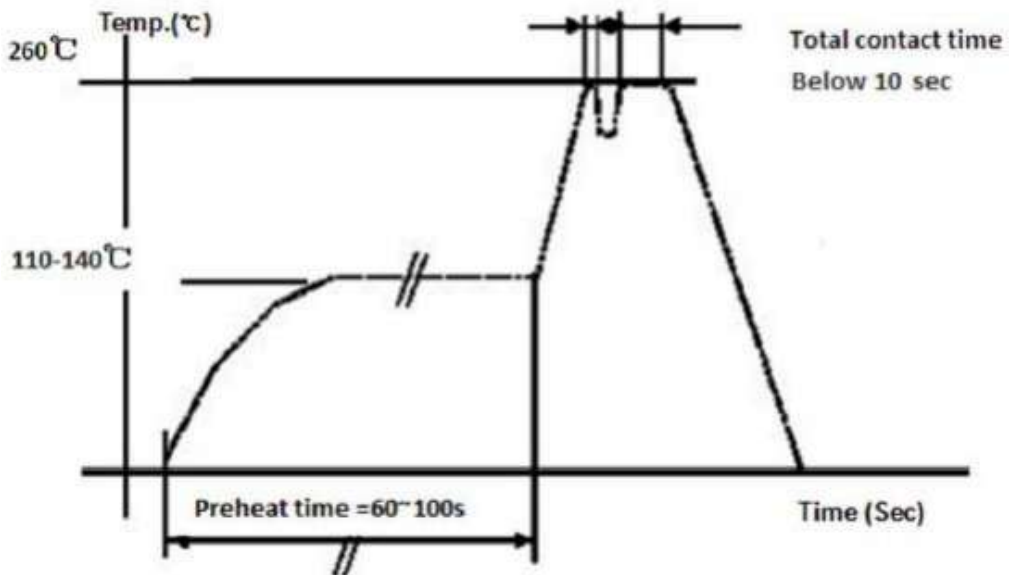
1 Reflow soldering condition.

Profile Feature	Pb-Free Assembly
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.
Preheat Temperature Min ( $T_{smin}$ ) Temperature Max ( $T_{smax}$ ) Time (min to max) (ts)	150°C 200°C 60-180 seconds
Time maintained above: Temperature ( $T_L$ ) Time (tL)	217°C 60-150 seconds
Peak Temperature ( $T_p$ )	260±0°C
Time within 5 °C of actual Peak Temperature (tp)	20-40 seconds
Ramp-down Rate	6 °C/second max.
Time 25°C to Peak Temperature	8 minutes max.



Recommended of soldering condition.

2 Wave soldering condition.



3 Hand soldering condition.

Temperature: 370°C max

Time: 3 sec. max