

TO-252-2 Silicon Carbide Schottky Diode

Features

- ✓ Zero forward recovery voltage
- ✓ Zero reverse recovery current
- ✓ Excellent surge current capability
- ✓ Temperature independent switching
- ✓ Positive temperature coefficient on V_F
- ✓ High frequency operation

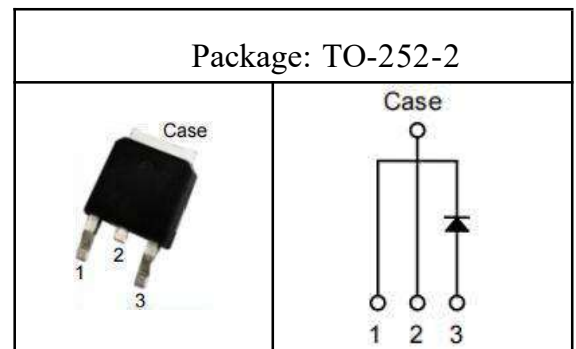
Benefits

- ✓ Increased Power Density
- ✓ Essentially no Switching Losses
- ✓ Reduction of Heat Sink Requirements
- ✓ Higher Efficiency
- ✓ Reduced EMI

Applications

- ✓ Uninterruptible power supplies
- ✓ Switch mode power supplies (SMPS)
- ✓ Power Factor Correction
- ✓ Motor Drivers

Part NO.	KJSC65C6T2
V_{RRM}	= 650 V
$I_F(T_C=160^\circ\text{C})$	= 6A
Q_C	= 25nC



Maximum ratings (T_j=25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Value	Unit
V _{RRM}	Repetitive peak reverse voltage		650	V
I _F	Continuous forward current	T _c =25°C T _c =160°C	26 6	A
I _{FSM}	Non-Repetitive forward surge current	T _c =25°C, t _p =10ms, Half Sine Wave	60	A
∫i ² dt	i ² t value	T _c =25°C, t _p =10ms	18	A ² S
P _{tot}	Power dissipation	T _c =25°C T _c =110°C	103 44	W
T _j	Operating junction temperature		-55~175	°C
T _{stg}	Storage temperature		-55~175	°C

Electrical Characteristics

Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
V _{DC}	DC blocking voltage	T _j =25°C	650			V
V _F	Diode forward voltage	I _F =6A, T _j =25°C I _F =6A, T _j =135°C I _F =6A, T _j =175°C		1.25 1.32 1.38	1.48 1.74 1.90	V
I _R	Reverse current	V _R =650V, T _j =25°C V _R =650V, T _j =175°C		0.5 15	50 200	μA

AC Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
Q _C	Total capacitive charge	V _R =400V , T _j =25°C $Q_C = \int_0^V R C(V)dV$		25		nC
C	Total capacitance	V _R =1V f=1MHz V _R =300V f=1MHz V _R =600V f=1MHz		350 42 36		pF
E _C	Capacitance stored energy	V _R =400V		3.8		μJ

Thermal Characteristics

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
R _{th(jc)}	Thermal Resistance from Junction to Case		1.45		° C/W

Typical Performance

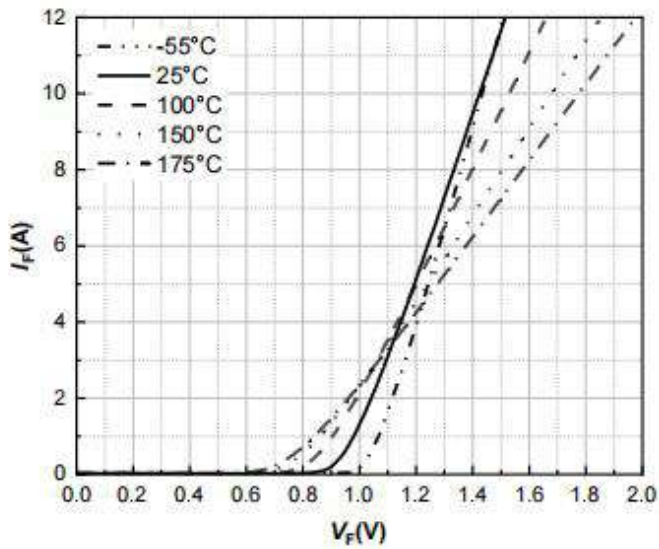


Figure 1. Typical forward characteristics

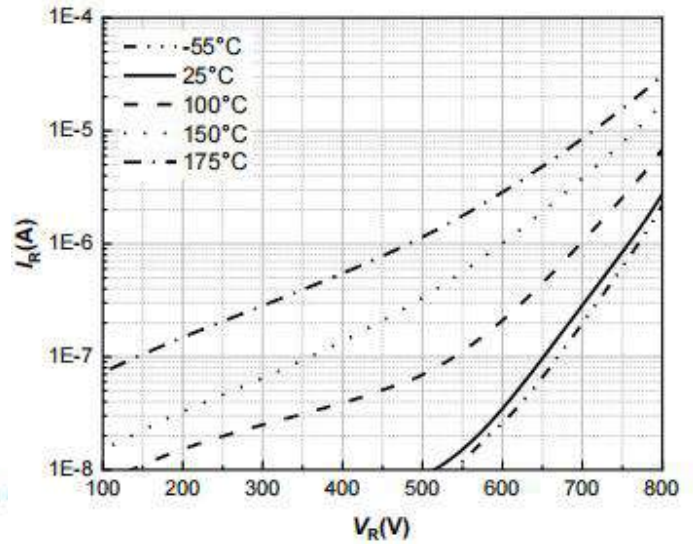


Figure 2. Typical reverse current as function of reverse voltage

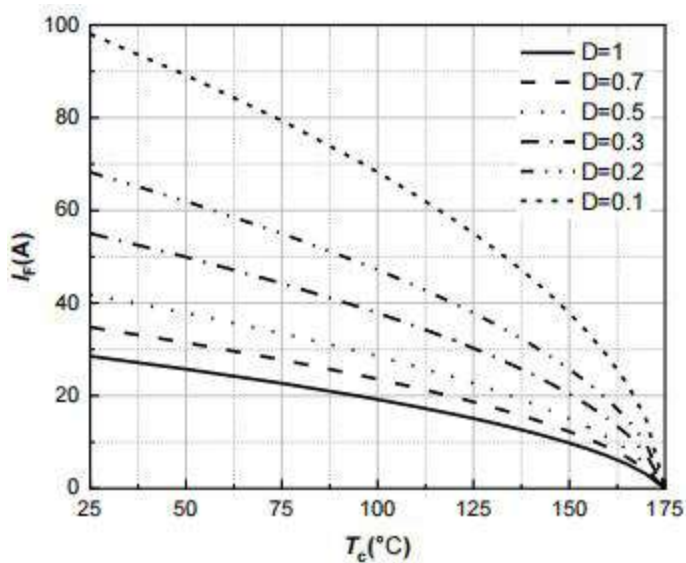


Figure 3. Diode forward current as function of temperature, D=duty cycle

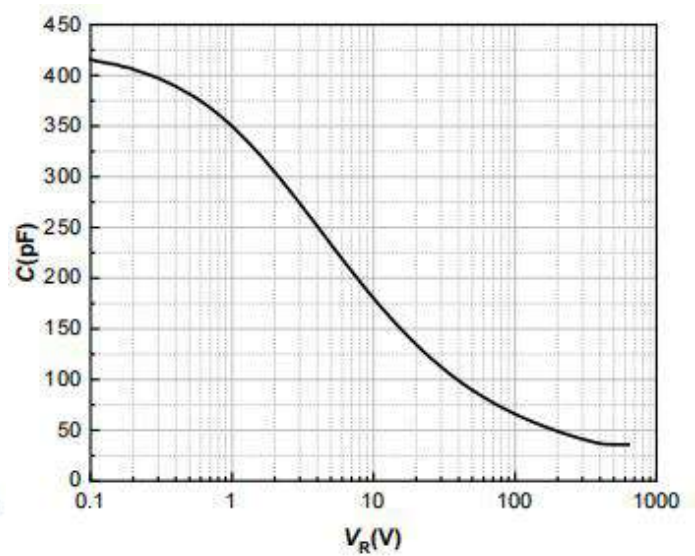


Figure 4. Typical capacitance as function of reverse voltage, $C=f(V_R)$; $T_j=25^\circ\text{C}$; $f=1\text{ MHz}$

Typical Characteristics

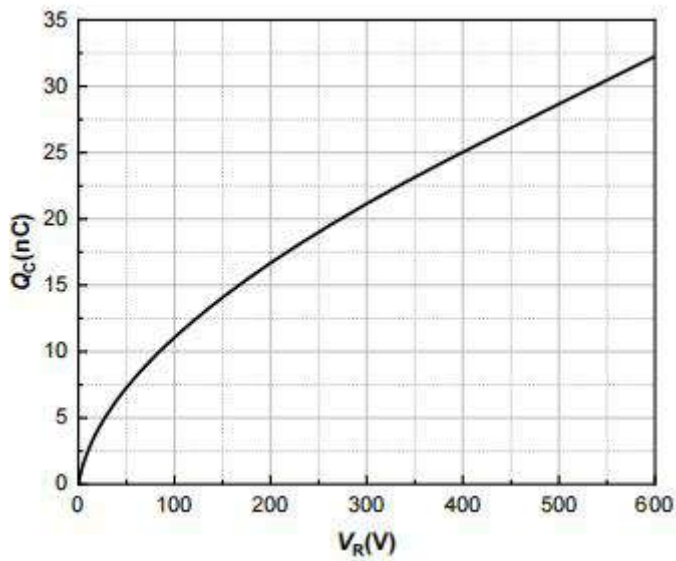


Figure 5. Typical reverse charge as function of reverse voltage

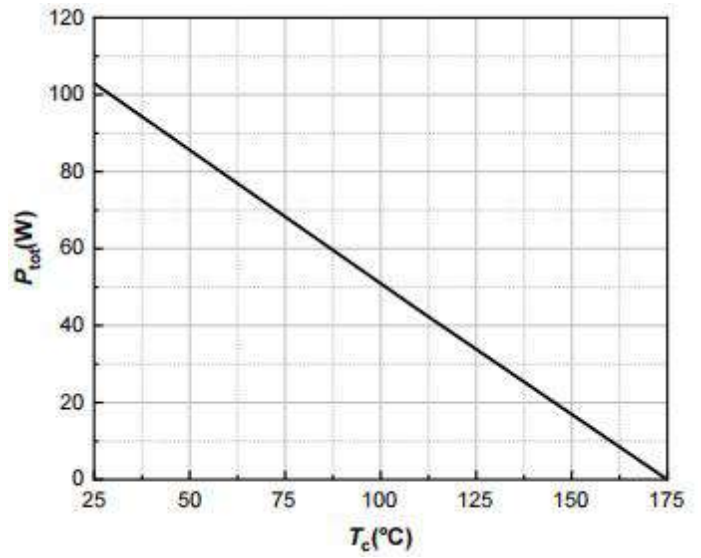


Figure 6. Power dissipation as function of case temperature

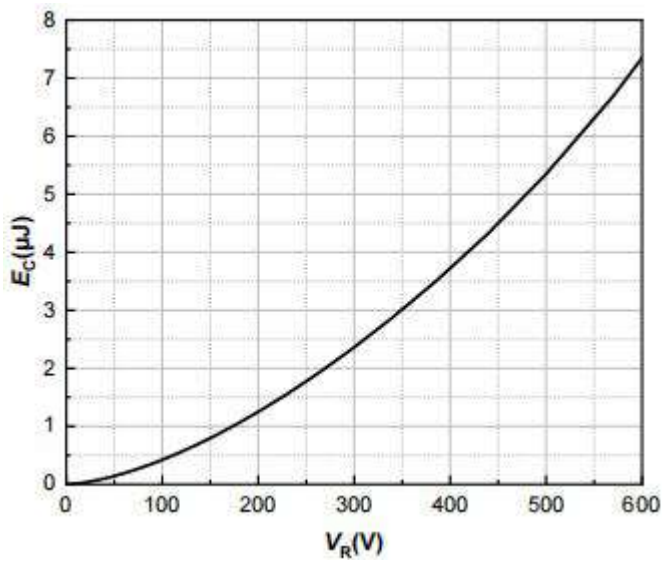


Figure 7. Capacitance stored energy

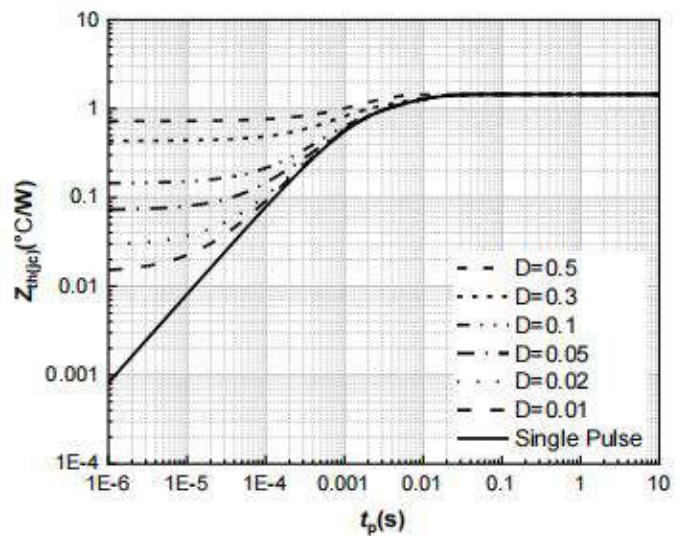


Figure 8. Max. transient thermal impedance, $Z_{th(jc)} = f(t_p)$, parameter: $D = t_p/T$

Package Outline Dimensions

Package outlines

