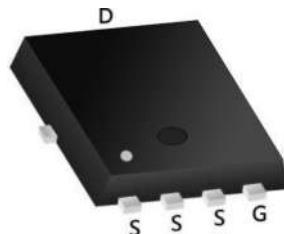


N-Ch 150V Fast Switching MOSFETs

Features:

- Low $R_{DS(ON)}$
- Green Device Available
- 100% EAS Tested
- Advanced Trench MOS Technology



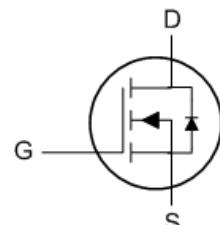
PRPAK5X6 Pin Configuration

Applications:

- Power Management in TV Converter.
- DC/DC Converter.

Product Summary

BVDSS	RDS(on)	ID
150V	56mΩ	23A



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current ¹	23	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current ¹	16	A
$I_D @ T_A=25^\circ C$	Continuous Drain Current ¹	4.5	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current ¹	3.8	A
I_{DM}	Pulsed Drain Current ²	60	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation ³	72.6	W
$P_D @ T_A=25^\circ C$	Total Power Dissipation ³	2.7	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	55	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.0	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	150	---	---	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{GS}=10\text{V}$, $I_D=10\text{A}$	---	47	56	$\text{m}\Omega$
	Static Drain-Source On-Resistance ²	$V_{GS}=4.5\text{V}$, $I_D=10\text{A}$	---	53	68	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=120\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=120\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=10\text{A}$	---	25	---	S
Q_g	Total Gate Charge	$V_{DS}=75\text{V}$, $V_{GS}=10\text{V}$, $I_D=10\text{A}$	---	19	---	nC
Q_{gs}	Gate-Source Charge		---	4.5	---	
Q_{gd}	Gate-Drain Charge		---	2.6	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=75\text{V}$, $V_{GS}=10\text{V}$, $R_G=3.3\Omega$ $I_D=10\text{A}$	---	18	---	ns
T_r	Rise Time		---	5.8	---	
$T_{d(off)}$	Turn-Off Delay Time		---	26.5	---	
T_f	Fall Time		---	4.5	---	
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	1090	---	pF
C_{oss}	Output Capacitance		---	93	---	
C_{rss}	Reverse Transfer Capacitance		---	6	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,4}	$V_G=V_D=0\text{V}$, Force Current	---	---	20	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	45	---	nS
Q_{rr}	Reverse Recovery Charge		---	138	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

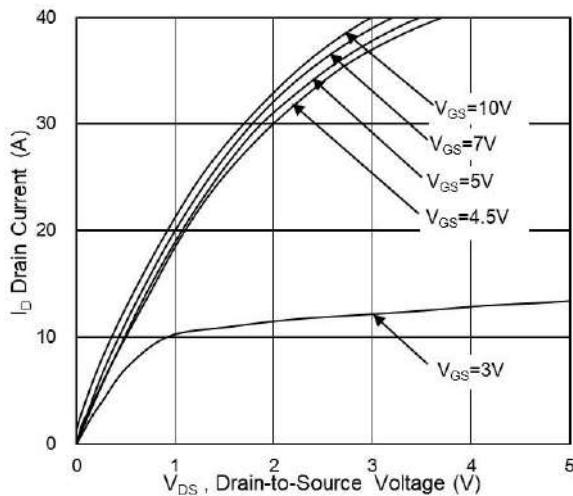


Fig.1 Typical Output Characteristics

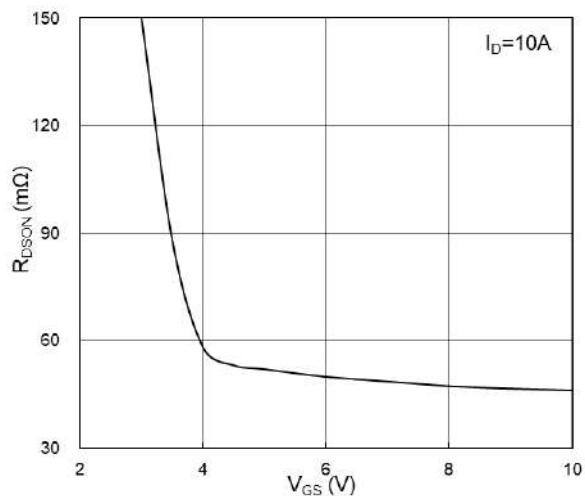


Fig.2 On-Resistance vs G-S Voltage

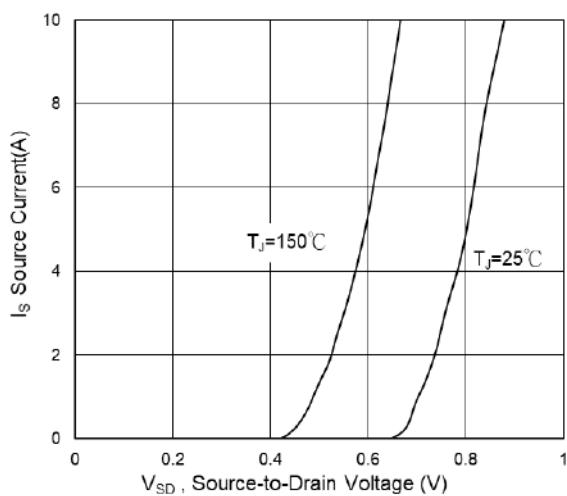


Fig.3 Source Drain Forward Characteristics

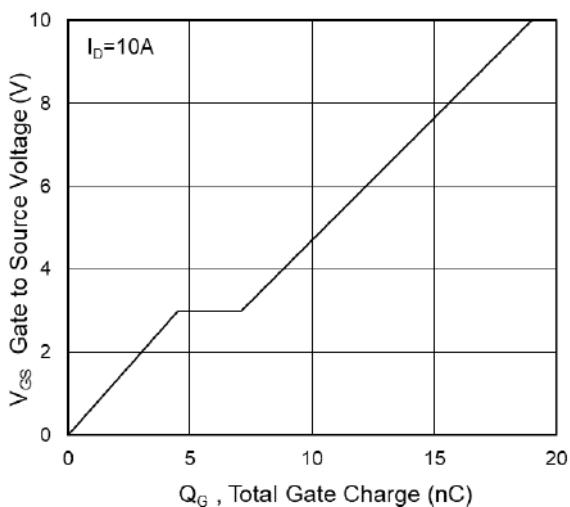


Fig.4 Gate-Charge Characteristics

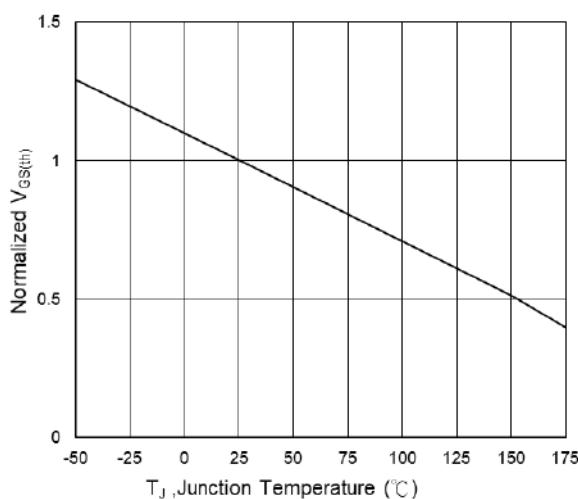


Fig.5 Normalized $V_{GS(th)}$ vs T_J

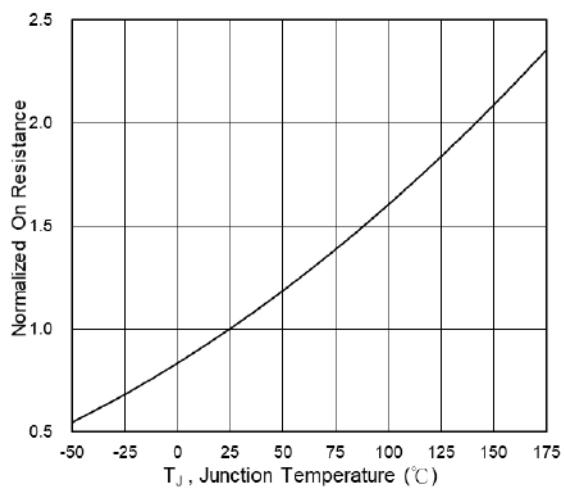


Fig.6 Normalized $R_{DS(on)}$ vs T_J

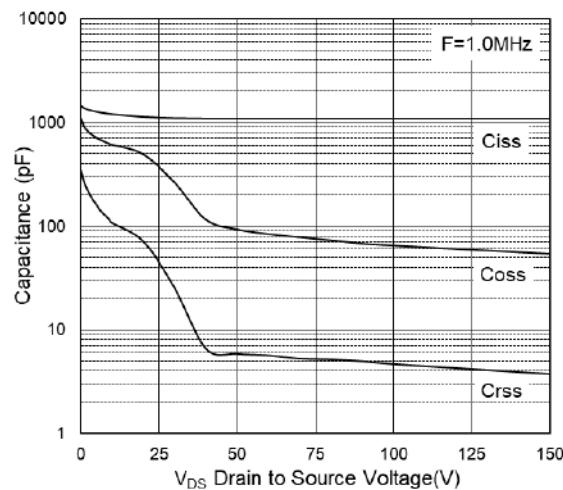


Fig.7 Capacitance

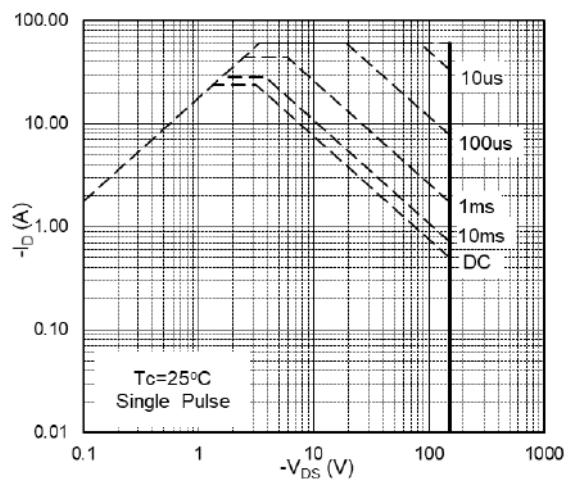


Fig.8 Safe Operating Area

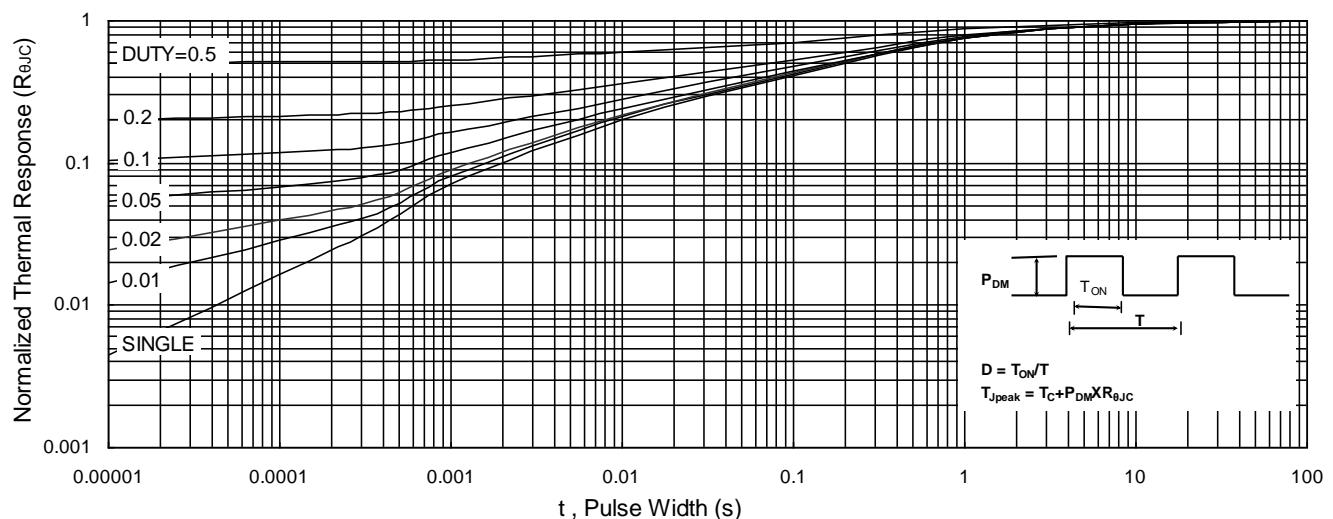


Fig.9 Normalized Maximum Transient Thermal Impedance

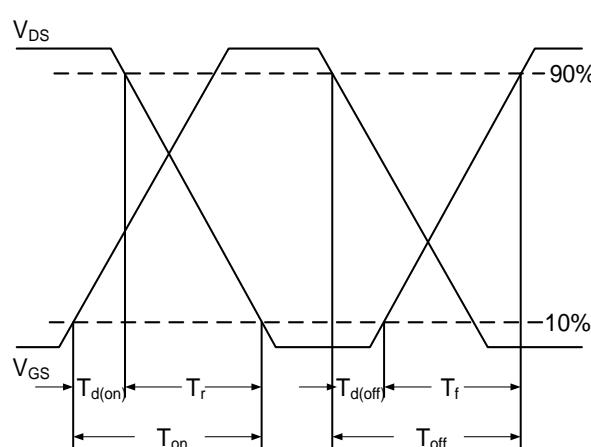


Fig.10 Switching Time Waveform

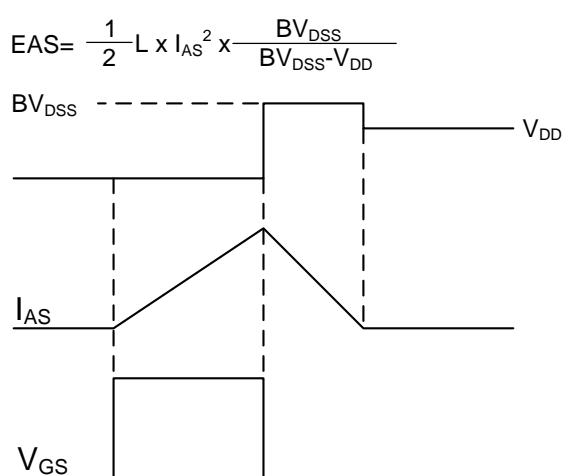


Fig.11 Unclamped Inductive Switching Waveform