

## N-Ch 100V Fast Switching MOSFETs

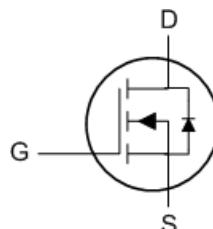
### General Description

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Fast Switching Speed
- Green Device Available

### Applications

- High Frequency Switching and Synchronous Rectification.
- DC/DC Converters.

### TO220 Pin Configuration



### Product Summary

BVDSS	RDS(on)	ID
100V	8mΩ	77A

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	100	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	77	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	49	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	250	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	186	mJ
I <sub>AS</sub>	Avalanche Current	61	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	83	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>	---	50	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	1.5	°C/W

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=250\mu\text{A}$	100	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=20\text{A}$	---	6.8	8	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$ , $\text{I}_D=20\text{A}$	---	8.5	10	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_D=250\mu\text{A}$	1.2	1.7	2.3	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=80\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=25^{\circ}\text{C}$	---	---	1	$\text{uA}$
		$\text{V}_{\text{DS}}=80\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=55^{\circ}\text{C}$	---	---	5	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$\text{g}_{\text{fs}}$	Forward Transconductance	$\text{V}_{\text{DS}}=5\text{V}$ , $\text{I}_D=20\text{A}$	---	---	---	S
$\text{R}_{\text{g}}$	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1.5	---	$\Omega$
$\text{Q}_{\text{g}}$	Total Gate Charge	$\text{V}_{\text{DS}}=50\text{V}$ , $\text{V}_{\text{GS}}=4.5\text{V}$ , $\text{I}_D=20\text{A}$	---	23.8	---	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	6.5	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	10.7	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=50\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{R}_{\text{G}}=3\Omega$ , $\text{I}_D=20\text{A}$	---	8.5	---	ns
$\text{T}_{\text{r}}$	Rise Time		---	5	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	30	---	
$\text{T}_{\text{f}}$	Fall Time		---	4.5	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{DS}}=50\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	2377	---	pF
$\text{C}_{\text{oss}}$	Output Capacitance		---	409	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	5	---	

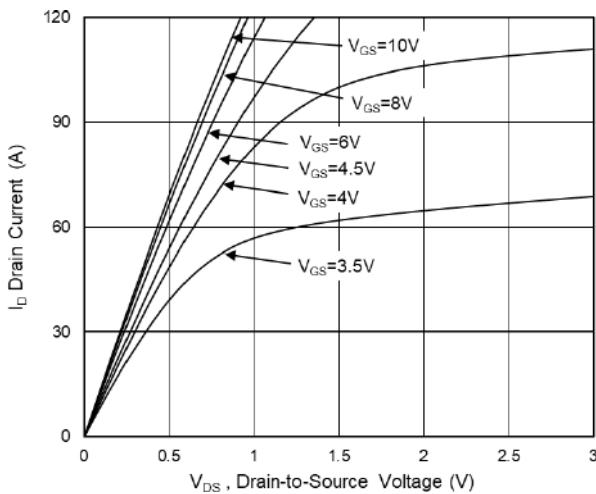
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_{\text{s}}$	Continuous Source Current <sup>1</sup>	$\text{V}_{\text{G}}=\text{V}_{\text{D}}=0\text{V}$ , Force Current	---	---	77	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_{\text{s}}=1\text{A}$ , $\text{T}_J=25^{\circ}\text{C}$	---	---	1.2	V

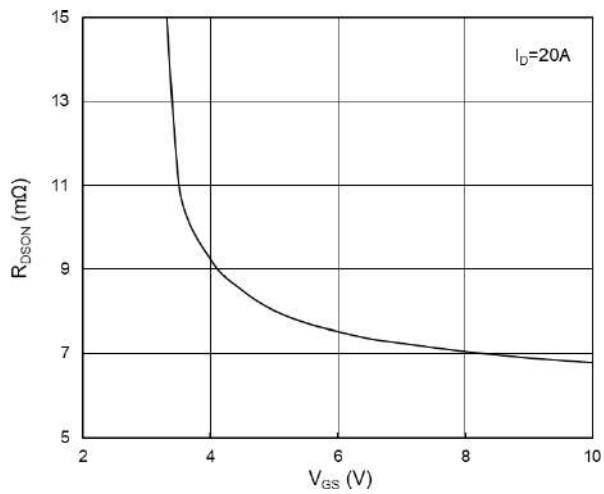
Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is  $\text{V}_{\text{DD}}=25\text{V}$ ,  $\text{V}_{\text{GS}}=10\text{V}$ ,  $\text{L}=0.1\text{mH}$ ,  $\text{I}_{\text{AS}}=61\text{A}$
4. The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
5. The data is theoretically the same as  $\text{I}_{\text{D}}$  and  $\text{I}_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

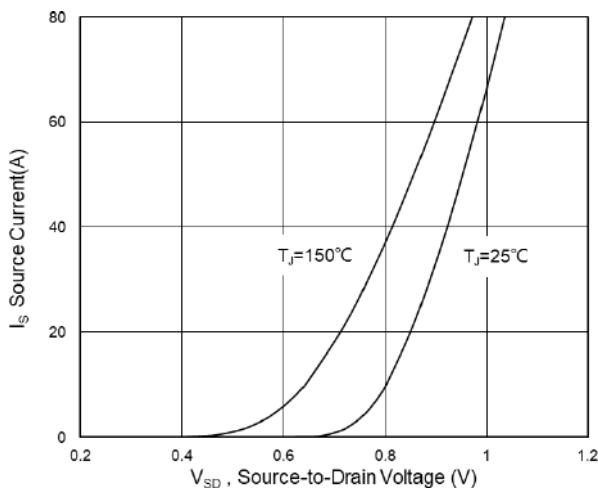
### Typical Characteristics



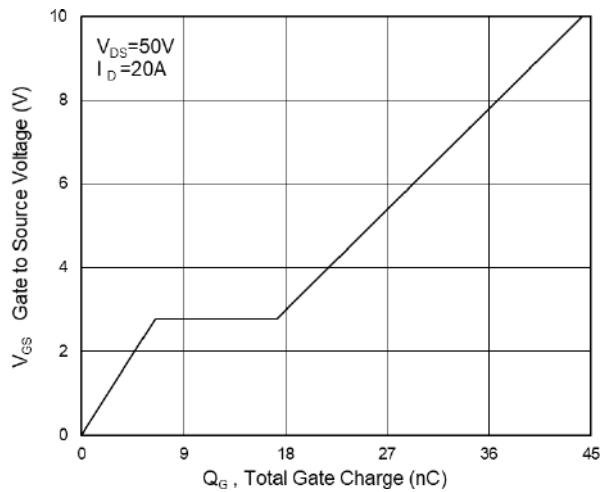
**Fig.1 Typical Output Characteristics**



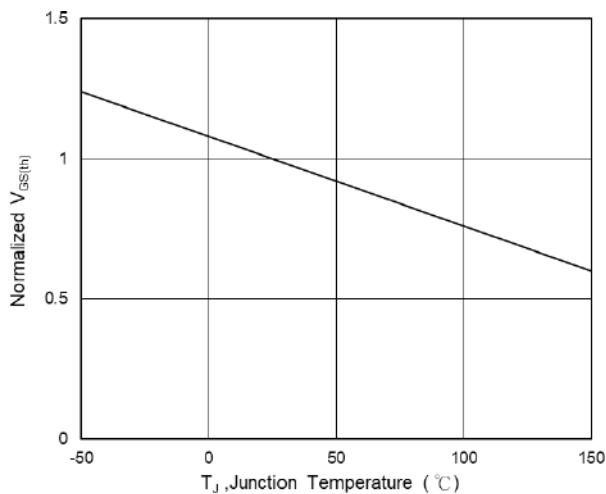
**Fig.2 On-Resistance vs G-S Voltage**



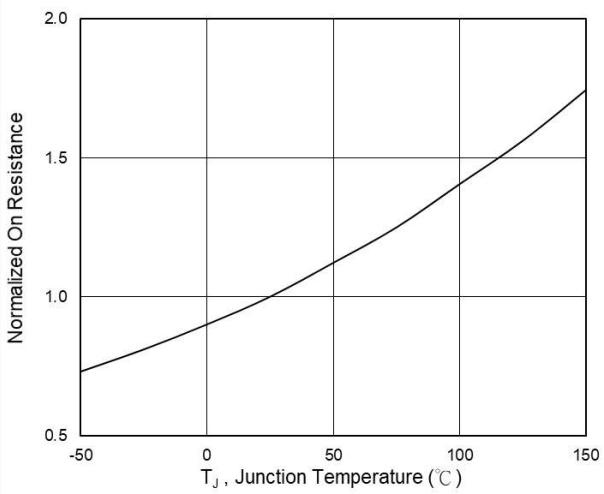
**Fig.3 Source Drain Forward Characteristics**



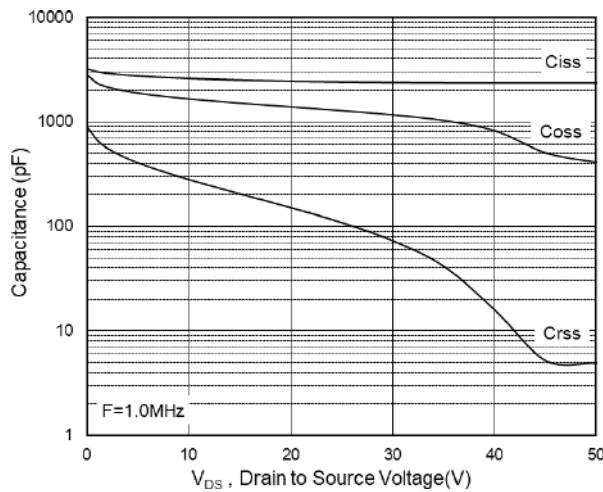
**Fig.4 Gate-Charge Characteristics**



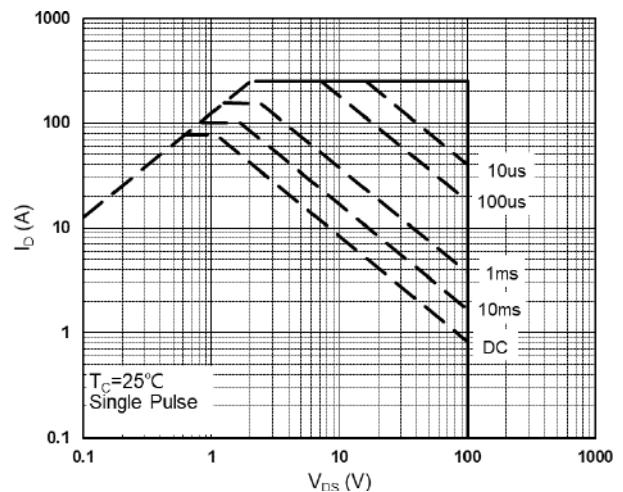
**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



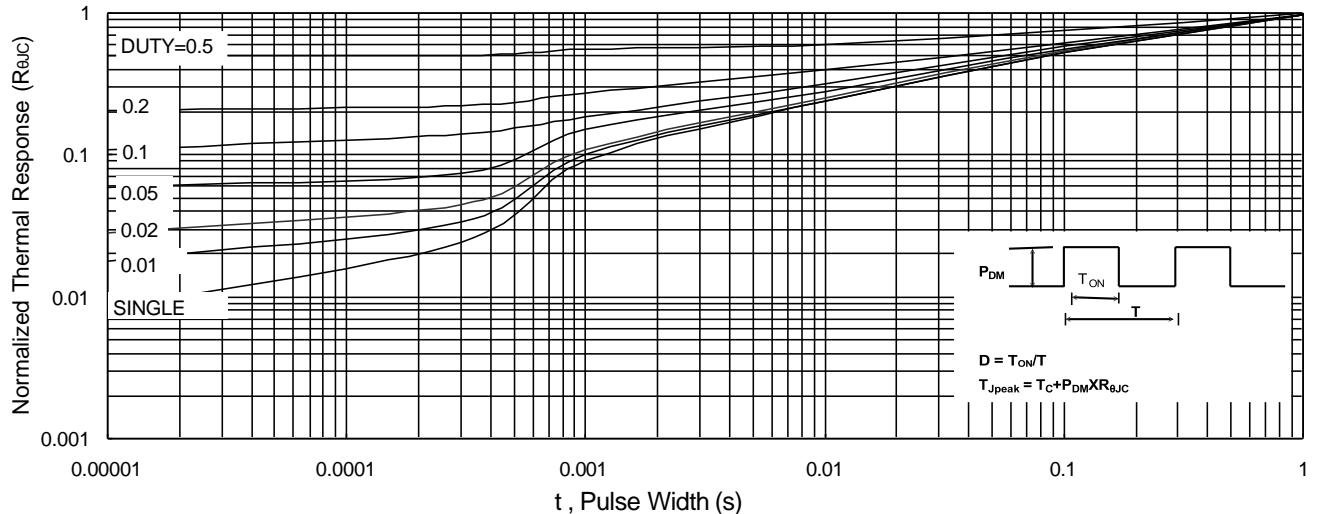
**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**



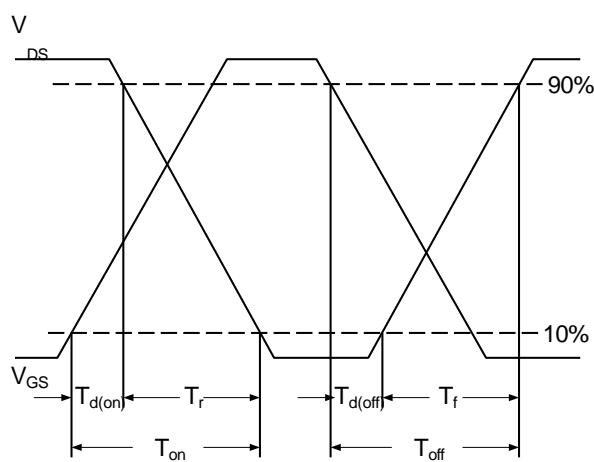
**Fig.7 Capacitance**



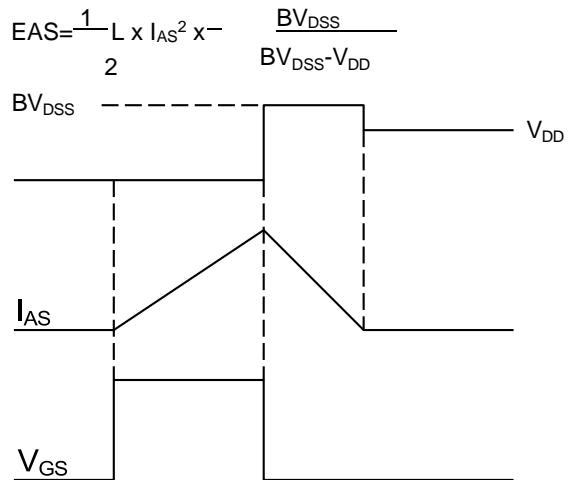
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**