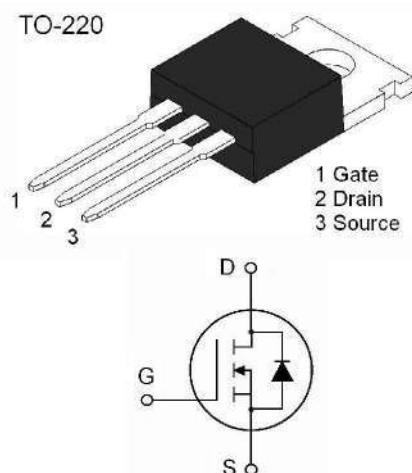


TO-220 Plastic-Encapsulate MOSFETS

N-Channel mode Power MOSFET

Features

- V_{DS} 200V
- I_D 9A
- $R_{DS(ON)}$ ($V_{GS} = 10V$) $< 0.4\Omega$
- High switching speed
- Improved dv/dt capability



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Parameter		Symbol	Ratings	Units
Gate-Drain Voltage		V_{DSS}	200	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	9	A
	Pulsed (Note 1)	I_{DM}	36	
Avalanche Current (Note 1)		I_{AR}	9	A
Single Pulsed Avalanche Energy (Note 2)		E_{AS}	160	mJ
Repetitive Avalanche Energy (Note 1)		E_{AR}	7.2	mJ
dv/dt Peak Diode Recovery dv/dt (Note 3)		dv/dt	5.5	V/ns
Power Dissipation		P_D	72	W
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55~155	$^\circ\text{C}$

Thermal Characteristic

Parameter	Symbol	Value	Units
Thermal Resistance, Junction-Case	$R_{\theta JC}$	1.74	$^\circ\text{C/W}$
Thermal Resistance, Junction-Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

Typical Characteristics

Electrical Characteristics ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Typ	Max.	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250μA	200			V
Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} /ΔT _J	I _D = 1mA, Referenced to 25 °C		0.2		V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200V, V _{GS} = 0V			10	μA
		V _{DS} =160V, V _{GS} = 0V T _C = 125 °C			100	
Gate Leakage Current	I _{GSS}	V _{DS} =0V,V _{GS} =±30V			±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2.0		4.0	V
Drain-source On-Resistance	R _{DS(ON)}	V _{GS} = 10V,I _D = 4.5A			0.18	Ω
Forward Transconductance(Note4)	g _{FS}	V _{DS} =40V ,I _D =4.5A		7.05		S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = 25V,V _{GS} = 0V, f = 1.0MHz		550		pF
Output Capacitance	C _{oss}			85		
Reverse Transfer Capacitance	C _{rss}			22		
Switching Characteristics)						
Turn-On Delay Time	t _{d(on)}	V _{DD} = 100V, I _D = 9A R _G = 25Ω (Note4, 5)		11		ns
Turn-on Rise Time	t _r			70		
Turn-Off elay Time	t _{d(off)}			60		
Turn-Off Fall	t _f			65		
Total Gate Charge	Q _g	V _{DS} = 160V, I _D = 9A V _{GS} = 10V (Note 4, 5)		22		nC
Gate-Source Charge	Q _{gs}			3.6		
Gate-Drain Charge	Q _{gd}			10.2		
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =9A			1.5	V
Diode Forward Current	I _S	-			9	A
Pulsed Diode Forward Current	I _{SM}	-			36	A
Reverse Recovery Time	t _{rr}	I _S = 9A, V _{GS} = 0V		140		ns
Reverse Recovery Charge	Q _{rr}	dl _F /dt =100A/μs (Note 4)		0.87		μC

Note: 1. Repetitive Rating : Pulse width limited by maximum junction temperature

2. $L = 13mH, I_{AS} = 9A, V_{DD} \leq BV_{DSS}$, Starting $T_j = 25^\circ\text{C}$

3. $I_{SD} \leq 9A, di/dt \leq 300A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_j = 25^\circ\text{C}$

4. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

5. Essentially independent of operating temperature

Typical Characteristics

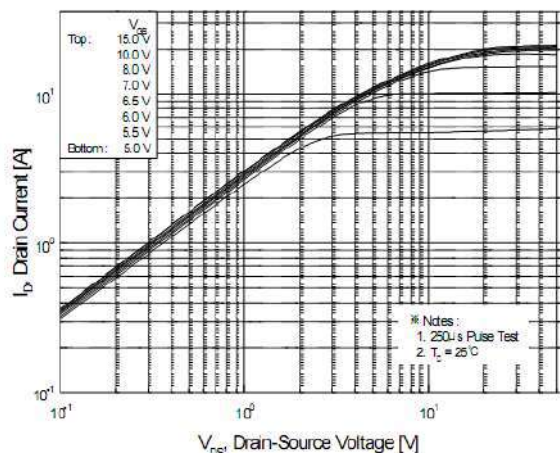


Figure 1. Typical Output Characteristics

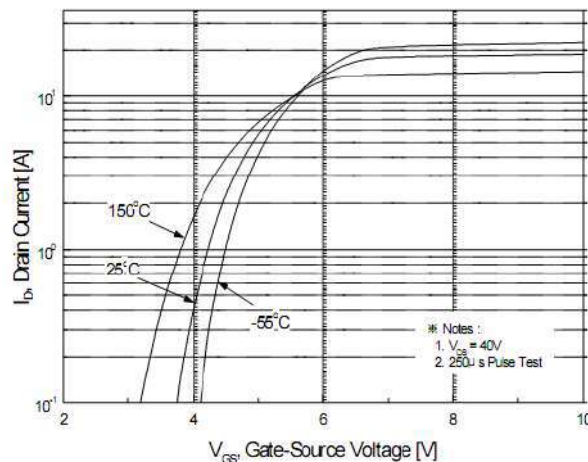


Figure 2. Transfer Characteristics

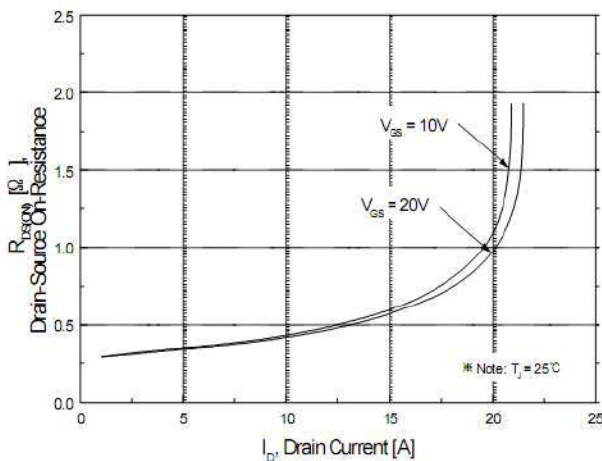


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

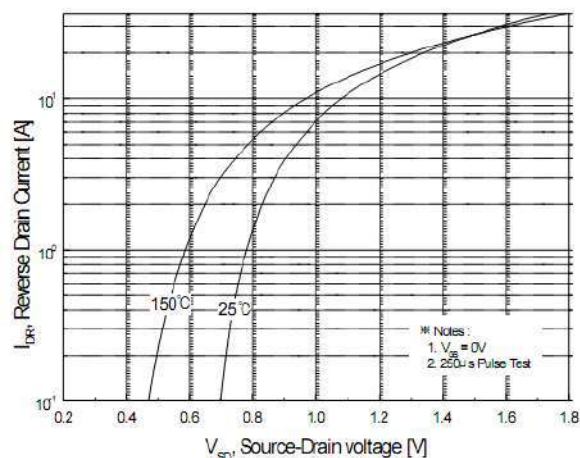


Figure 4. Body Diode Characteristics

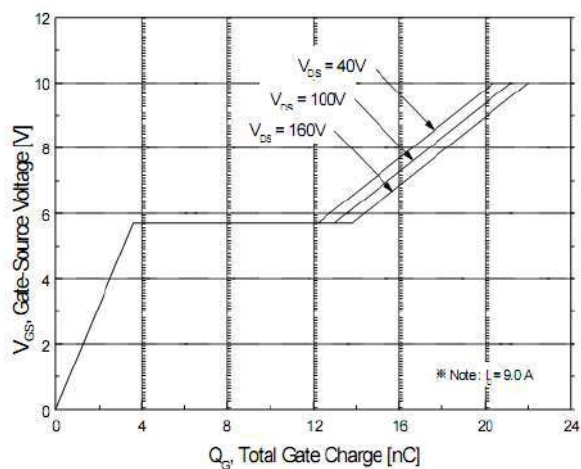


Figure 5. Drain to Source ON Resistance vs Drain Current

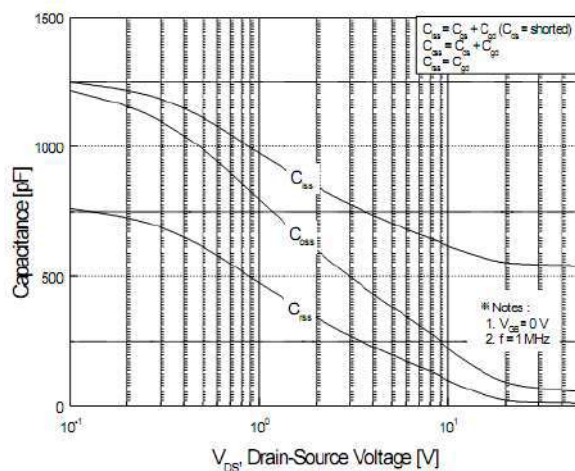


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

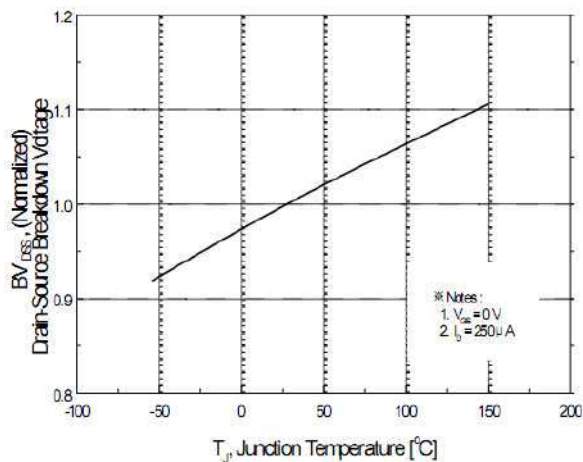


Figure 7. Breakdown Voltage Variation vs Temperature

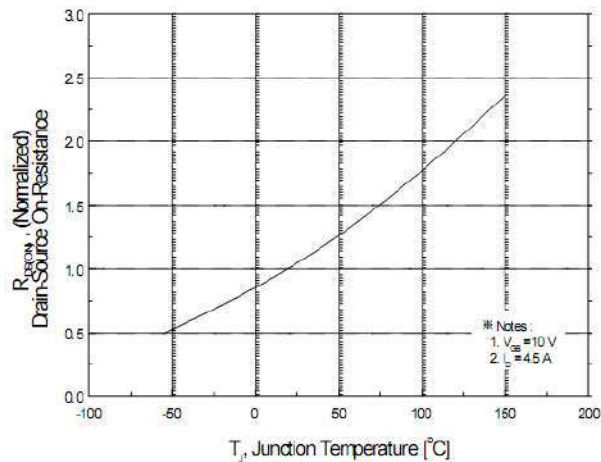


Figure 8. On-Resistance Variation vs Temperature

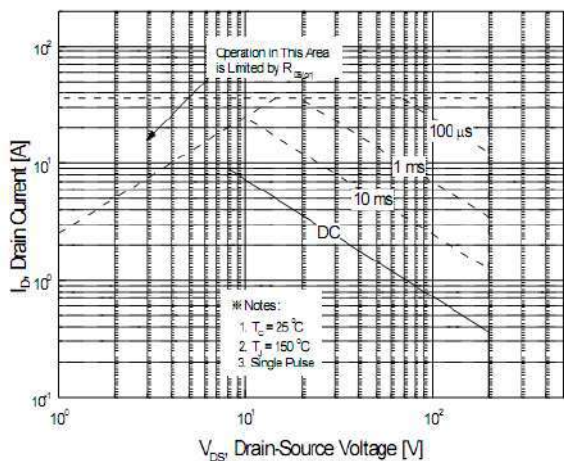


Figure 9. Safe Operating Area

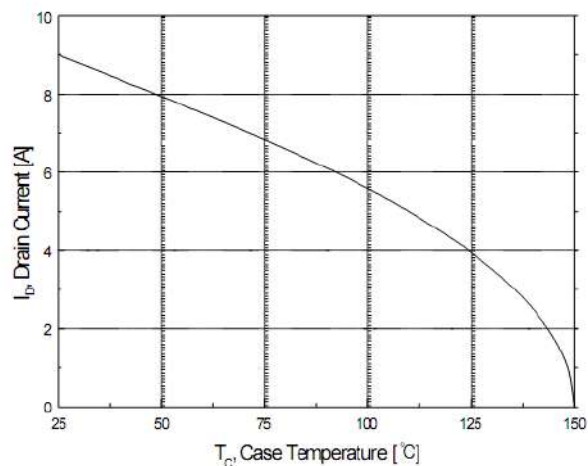


Figure 10. Maximum Drain Current vs Case Temperature

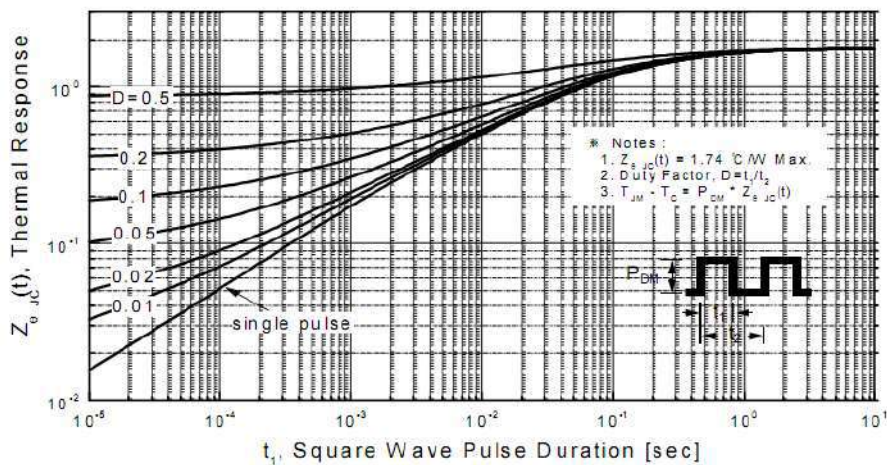


Figure 11. Transient Thermal Response Curve

Test Circuits and waveforms

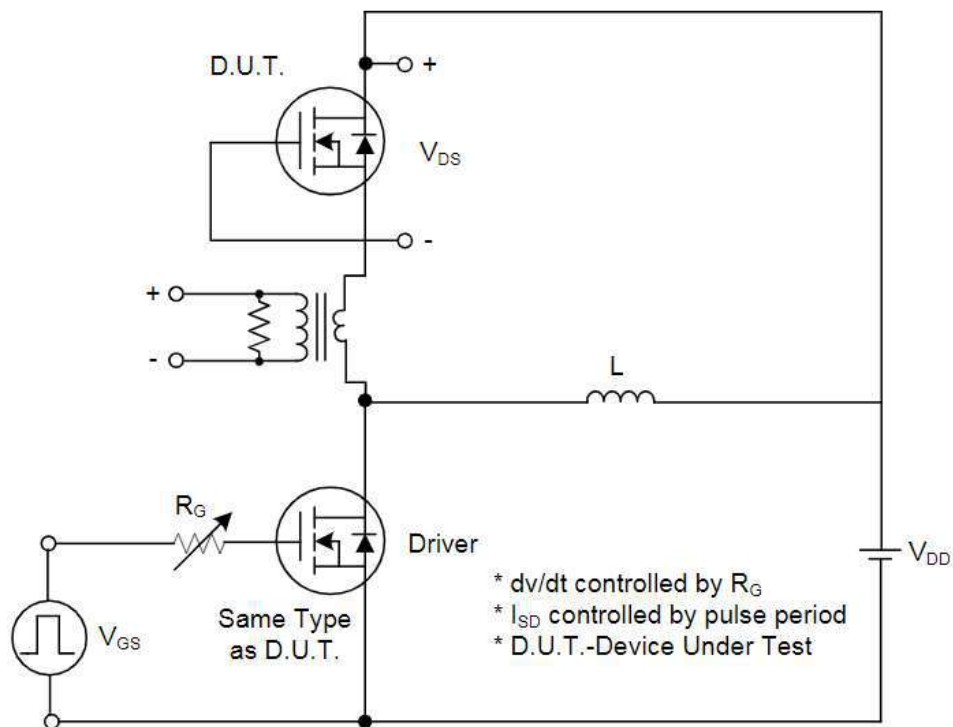


Figure 12. Peak Diode Recovery dv/dt Test Circuit

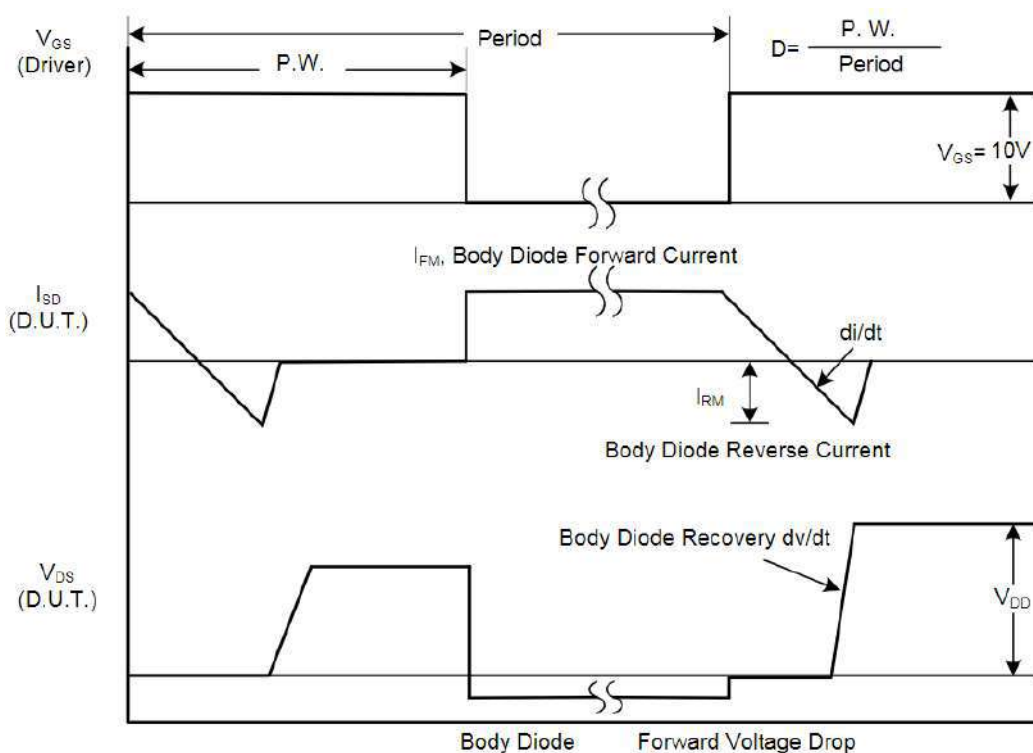


Figure 13. Peak Diode Recovery dv/dt Waveforms

Test Circuits and waveforms

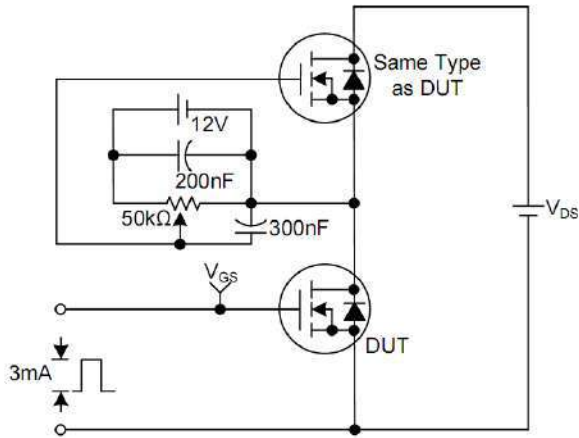


Figure 14. Gate Charge Test Circuit

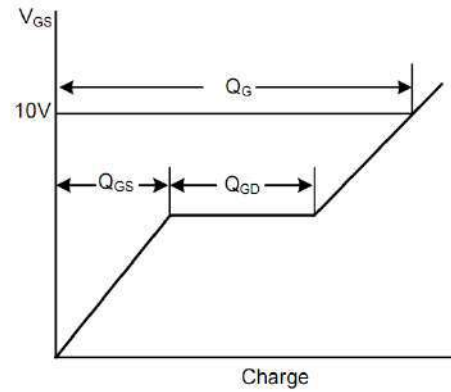


Figure 15. Gate Charge Waveforms

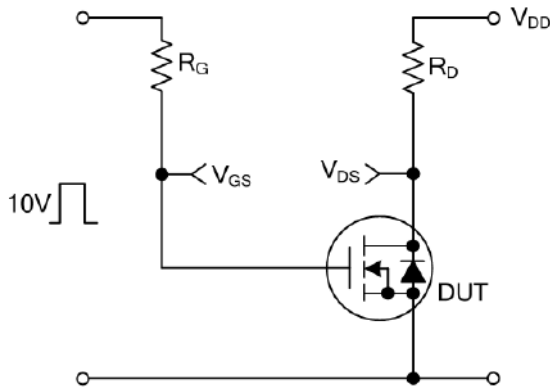


Figure 16. Resistive Switching Circuit

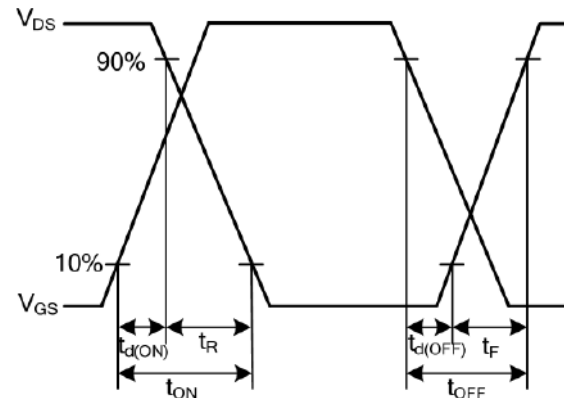


Figure 17. Resistive Switching Waveforms

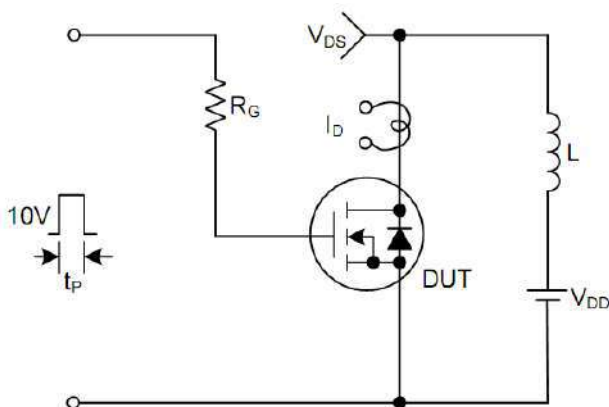


Figure 18. Unclamped Inductive Switching Test Circuit

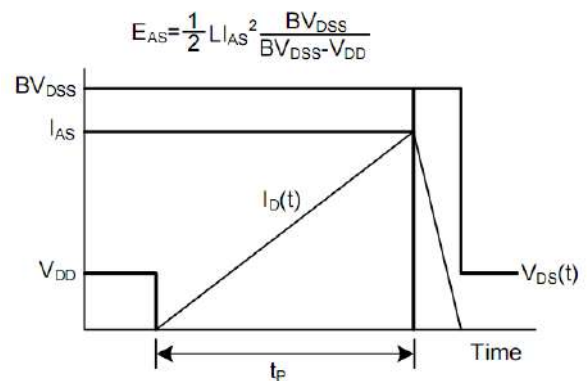


Figure 19. Unclamped Inductive Switching Waveforms

TO-220 Package Outline Dimensions

Package Dimensions

Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.34	4.67	0.171	0.184
A1	2.52	2.82	0.099	0.111
b	0.71	0.91	0.028	0.036
b1	1.17	1.37	0.046	0.054
c	0.30	0.50	0.012	0.020
c1	1.17	1.37	0.046	0.054
D	9.90	10.20	0.390	0.402
E	8.50	8.90	0.335	0.350
E1	12.00	12.50	0.472	0.492
e	2.44	2.64	0.096	0.104
e1	4.88	5.28	0.192	0.208
F	2.60	2.80	0.102	0.110
L	13.20	13.80	0.520	0.543
L1	3.80	4.20	0.150	0.165
Φ	3.60	3.96	0.142	0.156