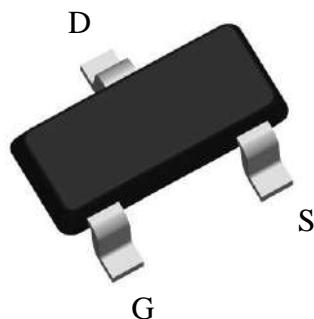


## 60V N-Channel Enhancement Mode MOSFET

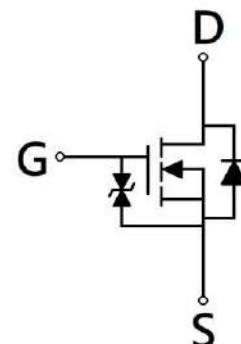
### Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic
- ESD protected gate

SOT-23



BV <sub>DSS</sub>	60V
I <sub>D</sub> @V <sub>GS</sub> =10V, T <sub>A</sub> =25°C	1.9A
R <sub>D(S)</sub> (ON) typ. @ V <sub>GS</sub> =10V, I <sub>D</sub> =1A	80mΩ
R <sub>D(S)</sub> (ON) typ. @ V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A	136mΩ



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KWB100N06KRS	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current @ $V_{GS}=10\text{V}$ , $T_A=25^\circ\text{C}$	$I_D$	1.9	A
Continuous Drain Current @ $V_{GS}=10\text{V}$ , $T_A=70^\circ\text{C}$		1.5	
Pulsed Drain Current	$I_{DM}$	7.2	A
Continuous Body Diode Forward Current @ $T_A=25^\circ\text{C}$	$I_S$	1.9	
Total Power Dissipation	$P_D$	0.7	W
$T_A=70^\circ\text{C}$		0.45	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	°C

### Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	176	°C/W

Note:

- \*a. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR -4 board with 2 oz. copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The power dissipation  $P_D$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*b. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^\circ\text{C}$ .

**Electrical Characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	60	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1	-	2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	1.9	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =3A
I <sub>GSS</sub>	-	-	±10		V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V
R <sub>DSS(ON)</sub>	-	80	110		V <sub>GS</sub> =10V, I <sub>D</sub> =1A
	-	136	198		V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A
<b>Dynamic</b>					
C <sub>iss</sub>	-	121	-	pF	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	40	-		
C <sub>rss</sub>	-	19	-		
R <sub>g</sub>	-	10	-	Ω	f=1MHz
Q <sub>g</sub> *1, 2	-	3.1	-	nC	V <sub>DS</sub> =30V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V
Q <sub>gs</sub> *1, 2	-	0.6	-		
Q <sub>gd</sub> *1, 2	-	0.7	-		
t <sub>d(ON)</sub> *1, 2	-	3.8	-	ns	V <sub>DS</sub> =30V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>GS</sub> =1Ω
t <sub>r</sub> *1, 2	-	16	-		
t <sub>d(OFF)</sub> *1, 2	-	10	-		
t <sub>f</sub> *1, 2	-	4	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *1	-	0.87	1.2	V	I <sub>S</sub> =2A, V <sub>GS</sub> =0V
trr	-	8	-	ns	I <sub>F</sub> =2A, dI <sub>F</sub> /dt=100A/μs
Qrr	-	3	-		

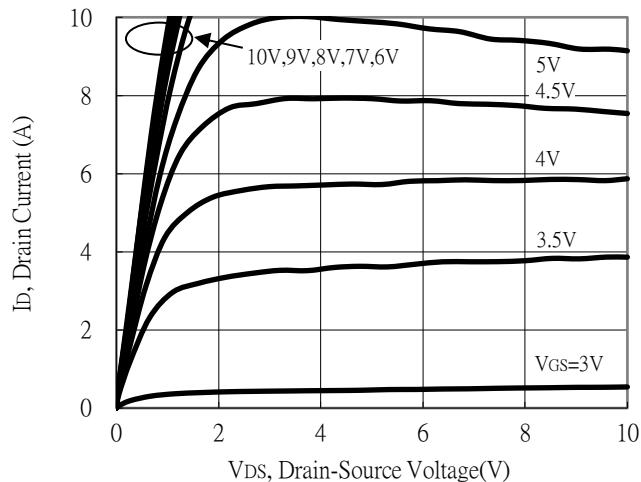
Note:

\*1. Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

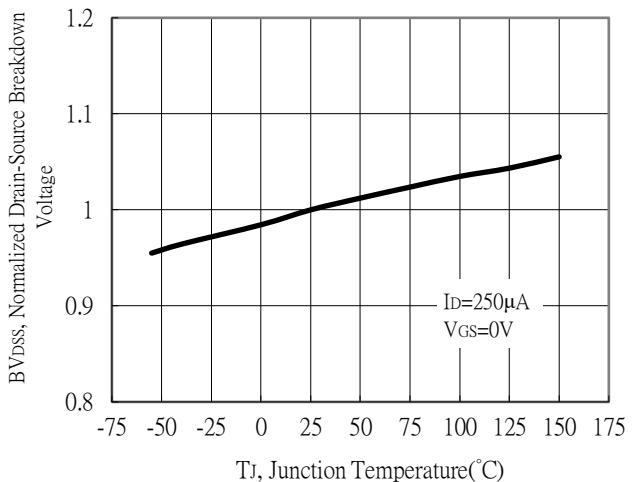
\*2. Independent of operating temperature

## Typical Characteristics

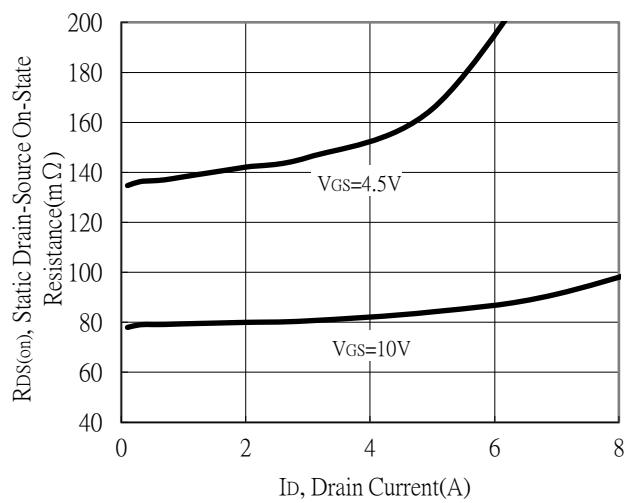
Typical Output Characteristics



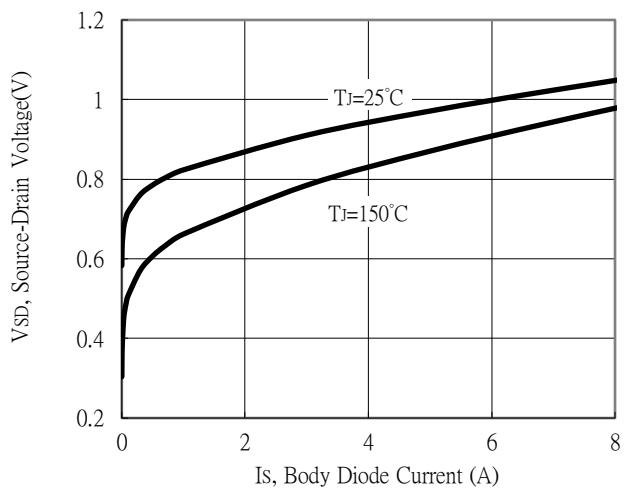
Breakdown Voltage vs Ambient Temperature



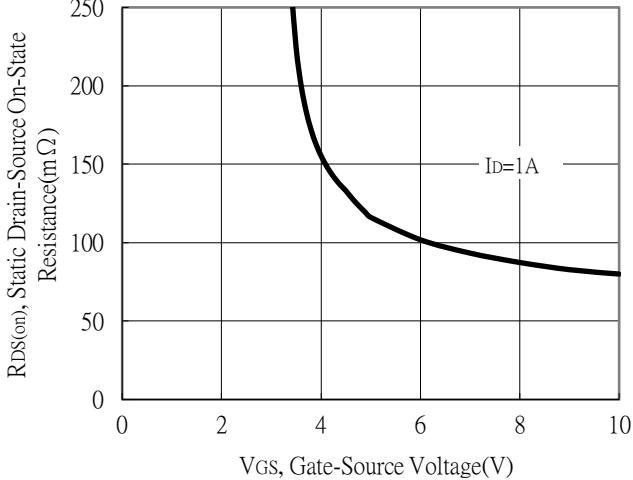
Static Drain-Source On-State resistance vs Drain Current



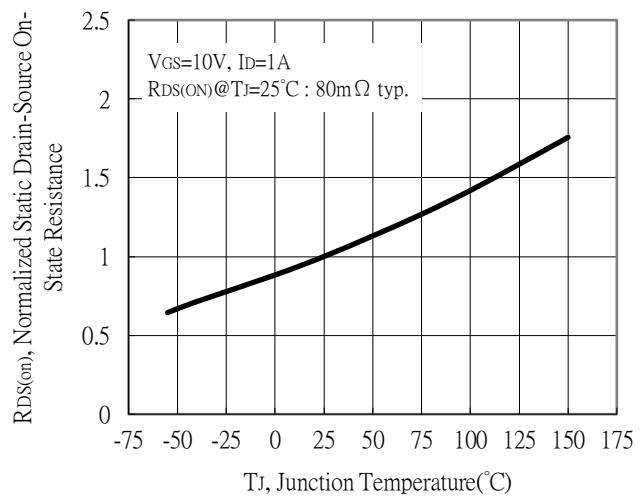
Body Diode Current vs Source-Drain Voltage



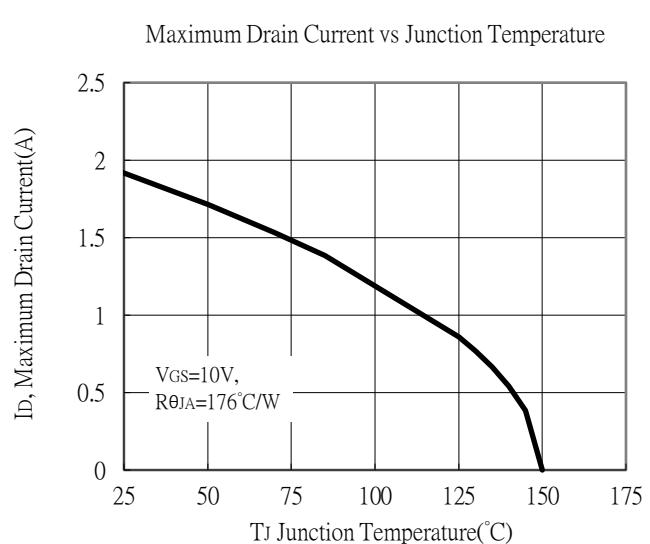
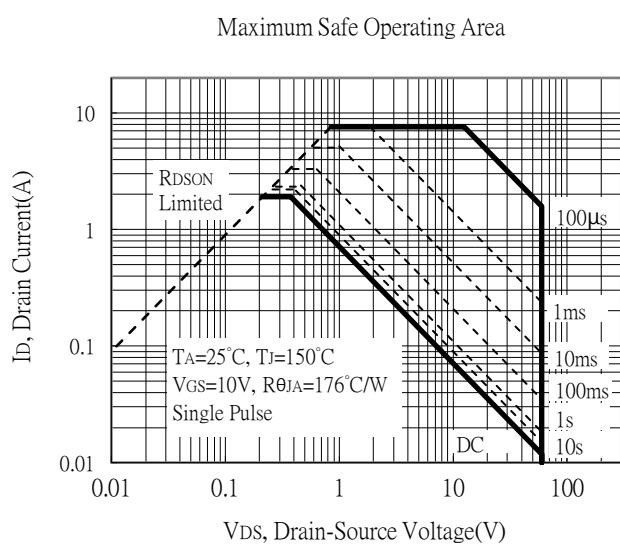
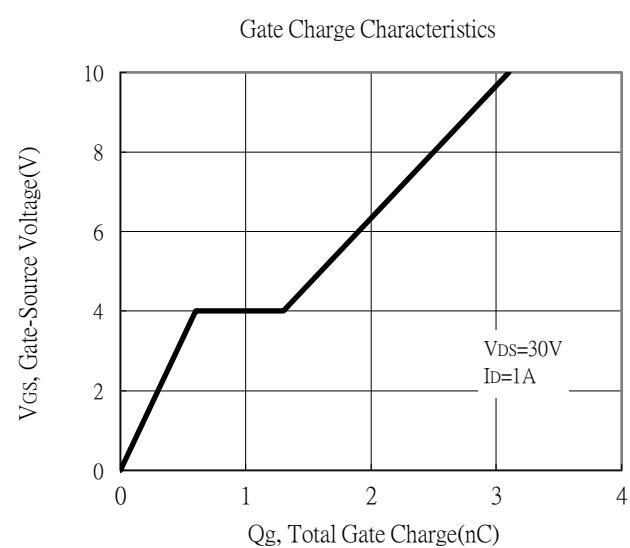
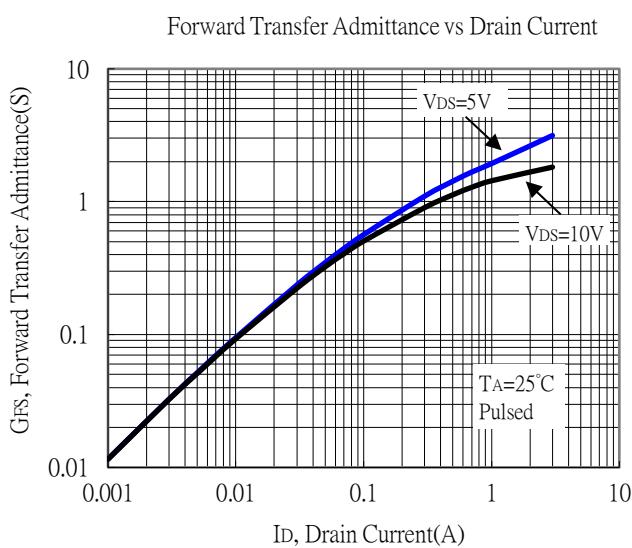
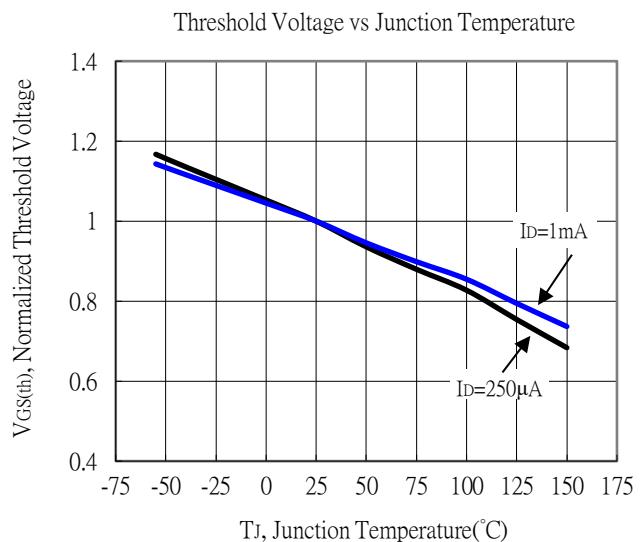
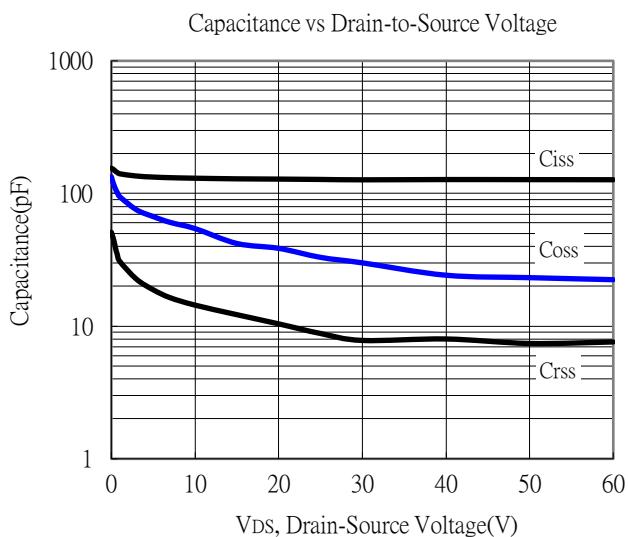
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

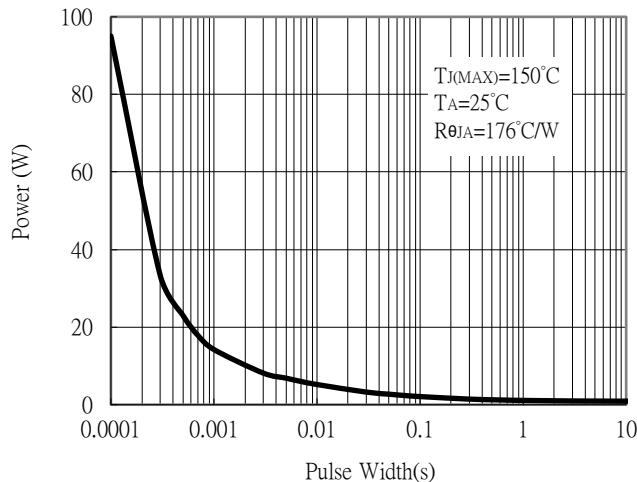


## Typical Characteristics (Cont.)

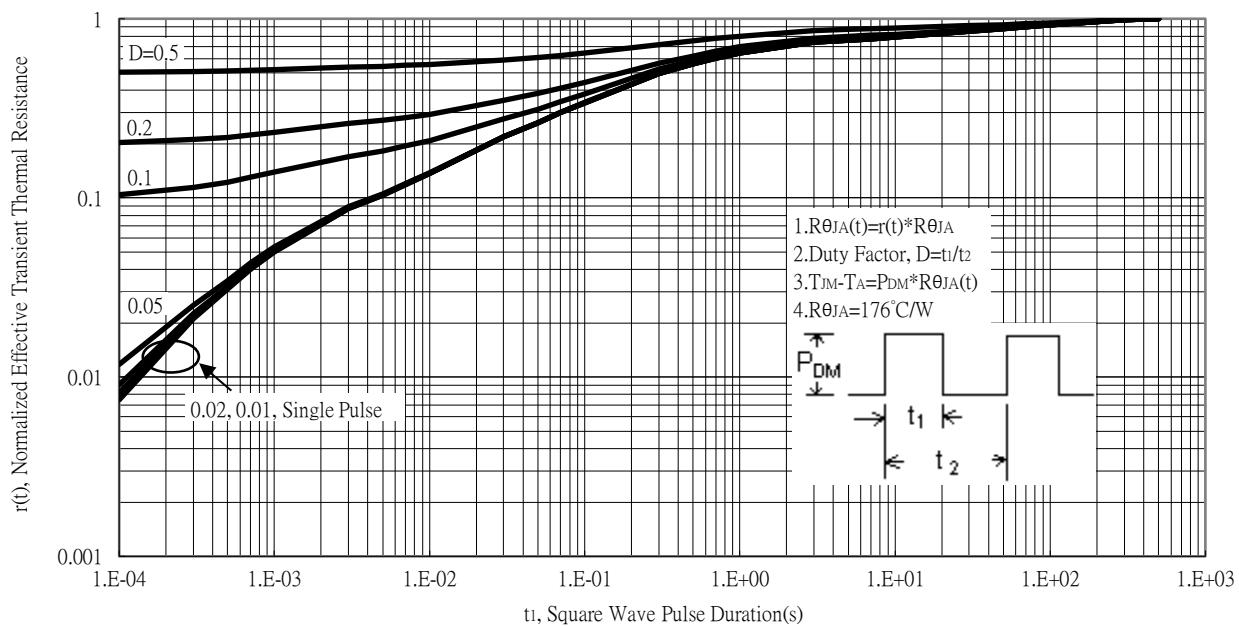


## Typical Characteristics (Cont.)

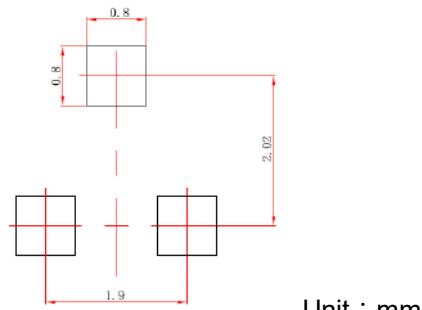
Single Pulse Power Rating, Junction to Ambient



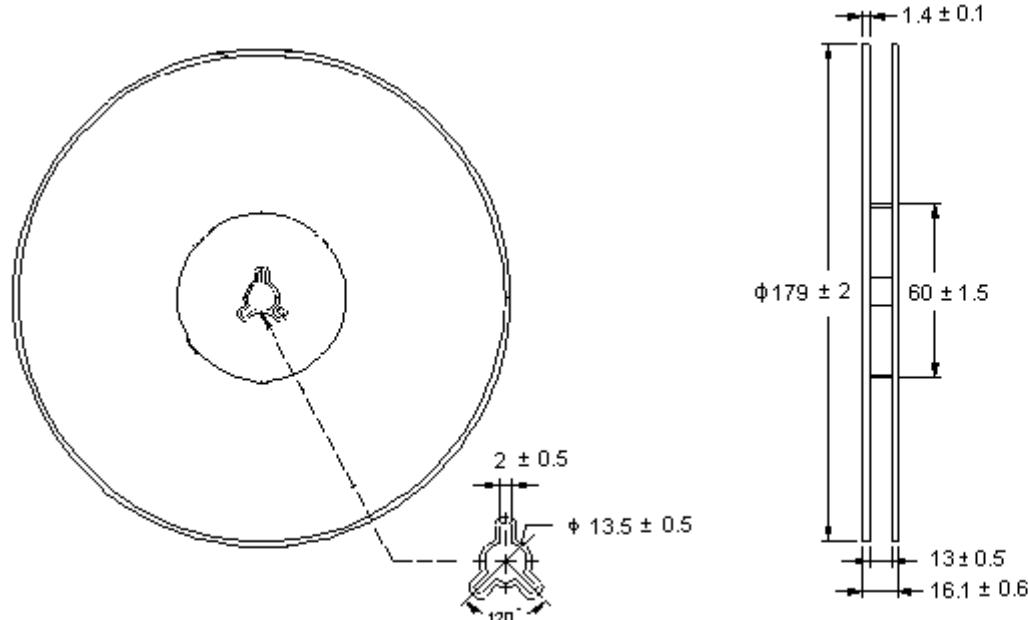
Transient Thermal Response Curves



## Recommended Soldering Footprint

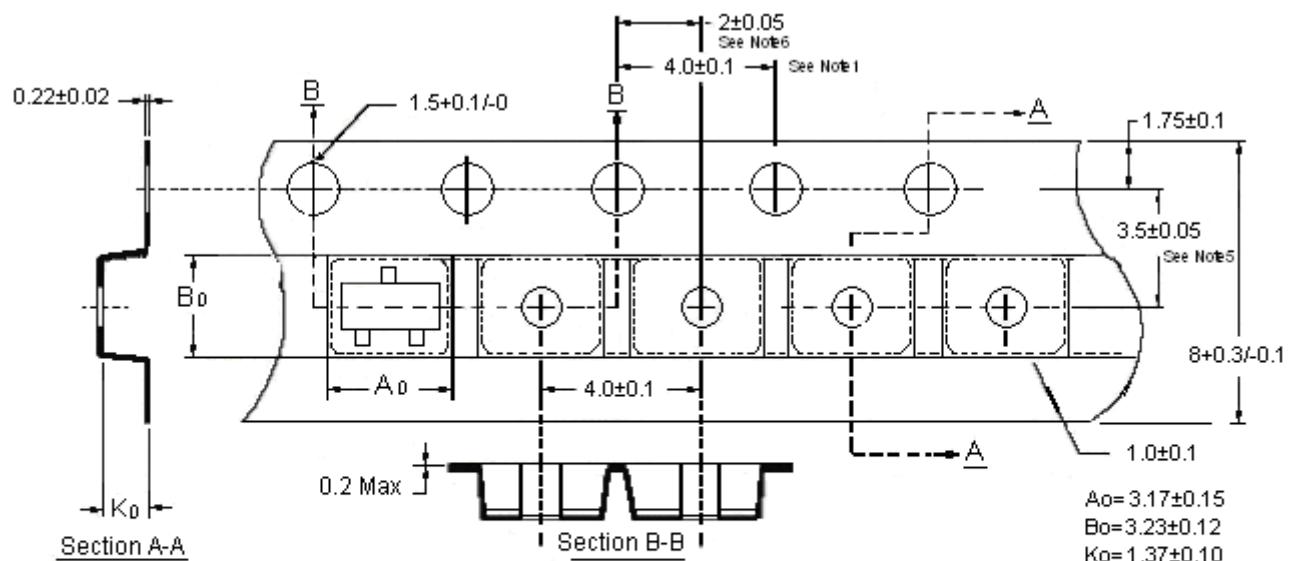


## Reel Dimension



Unit: millimeter

## Carrier Tape Dimension

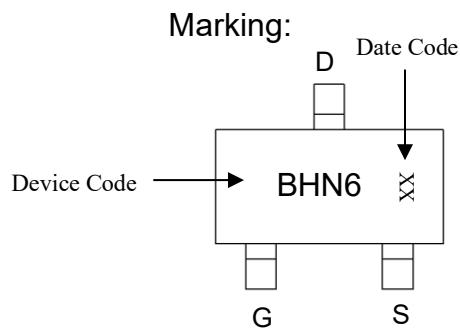
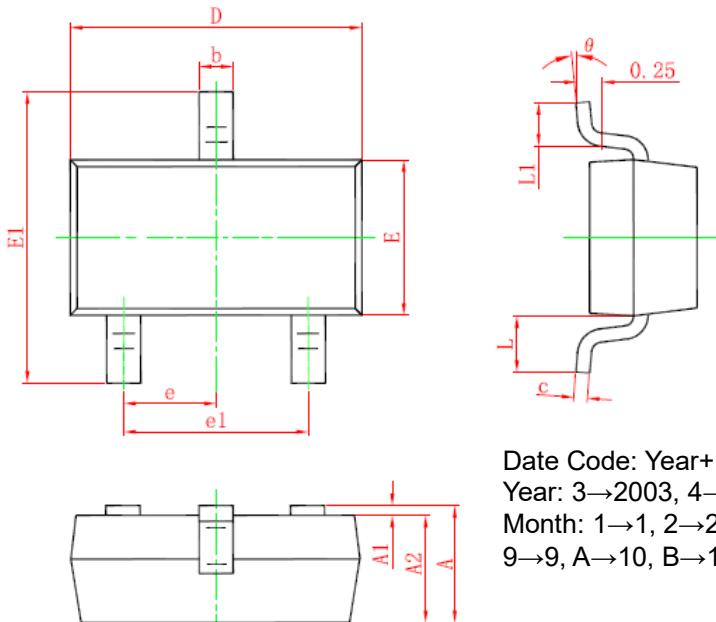


Notes:

1. 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$ .
2. Camber not to exceed 1mm in 100mm.
3. Material : conductive Black Polystyrene.
4.  $A_0$  &  $B_0$  measured on a plane 0.3mm above the bottom of the pocket.
5.  $K_0$  measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Unit : millimeter

## SOT-23 Dimension



3-Lead SOT-23 Plastic  
Surface Mounted Package

Date Code: Year+Month  
Year: 3→2003, 4→2004  
Month: 1→1, 2→2, . . .  
9→9, A→10, B→11, C→12

Style: Pin 1.Gate 2.Source 3.Drain

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.035	0.045	0.900	1.150	E1	0.089	0.100	2.250	2.550
A1	0.000	0.004	0.000	0.100	e	0.037 TYP.		0.950 TYP.	
A2	0.035	0.041	0.900	1.050	e1	0.071	0.079	1.800	2.000
b	0.012	0.020	0.300	0.500	L	0.022 REF.		0.550 REF.	
c	0.003	0.006	0.080	0.150	L1	0.012	0.020	0.300	0.500
D	0.110	0.118	2.800	3.000	θ	0 °	8 °	0 °	8 °
E	0.047	0.055	1.200	1.400					