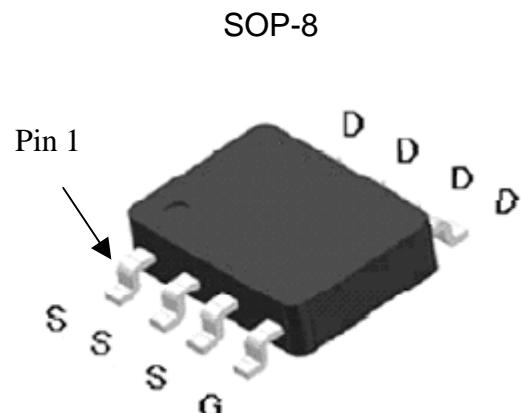


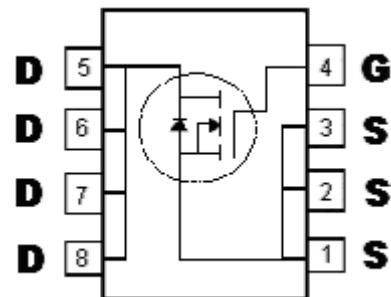
## N-Channel Logic Level Enhancement Mode Power MOSFET

### Features:

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Repetitive Avalanche Rated
- Pb-free lead plating and halogen-free package



BVDSS	30V
ID@ TA=25°C, VGS=10V	19A
ID@ TC=25°C, VGS=10V	27A
RDS(on)@VGS=10V, ID=18A	3.1mΩ (typ)
RDS(on)@VGS=4.5V, ID=15A	4.0mΩ (typ)



G : Gate D : Drain S : Source

### Ordering Information

Device	Package	Shipping
KWB3D0N03BQ8	SOP-8 (Pb-free lead plating and halogen-free package)	2500 pcs / tape & reel



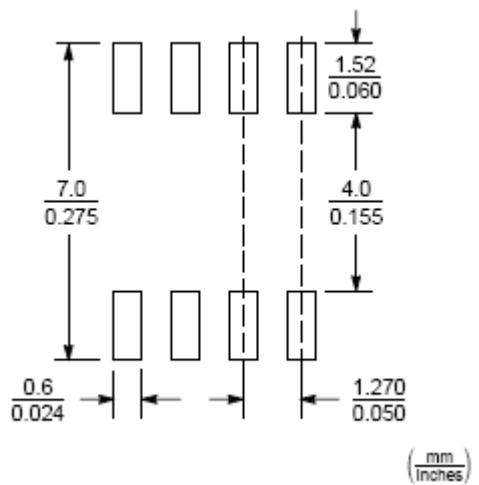
<b>Dynamic</b>					
C <sub>iss</sub>	-	2321	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz
C <sub>oss</sub>	-	400	-		
C <sub>rss</sub>	-	220	-		
Q <sub>g</sub> (V <sub>GS</sub> =10V) *1, 2	-	51	-		
Q <sub>g</sub> (V <sub>GS</sub> =4.5V) *1, 2	-	26	-	nC	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =18A
Q <sub>gs</sub> *1, 2	-	8.5	-		
Q <sub>gd</sub> *1, 2	-	11.1	-		
t <sub>d(ON)</sub> *1, 2	-	17	-		
t <sub>r</sub> *1, 2	-	19	-	ns	V <sub>DS</sub> =15V, I <sub>D</sub> =18A, V <sub>GS</sub> =10V, R <sub>GS</sub> =3Ω
t <sub>d(OFF)</sub> *1, 2	-	55	-		
t <sub>f</sub> *1, 2	-	12	-		
R <sub>g</sub>	-	1.4	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
I <sub>s</sub> *1	-	-	9	A	
I <sub>SM</sub> *3	-	-	36		
V <sub>SD</sub> *1	-	0.69	1	V	I <sub>s</sub> =1A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	15	-	ns	
Q <sub>rr</sub>	-	6.7	-	nC	I <sub>F</sub> =18A, dI <sub>F</sub> /dt=100A/μs

Note : \*1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

\*2.Independent of operating temperature

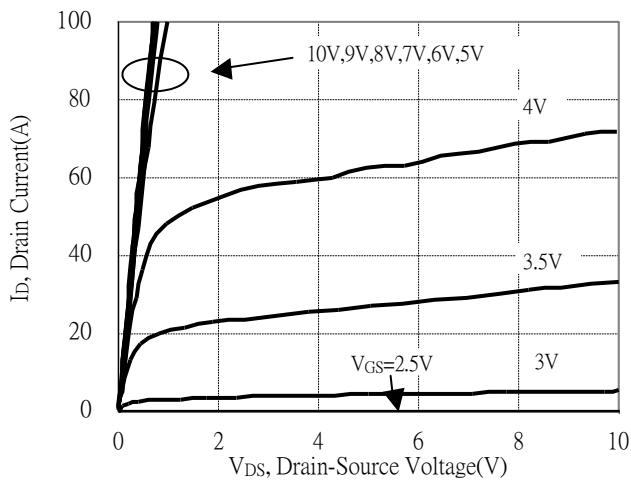
\*3.Pulse width limited by maximum junction temperature.

## Recommended Soldering Footprint

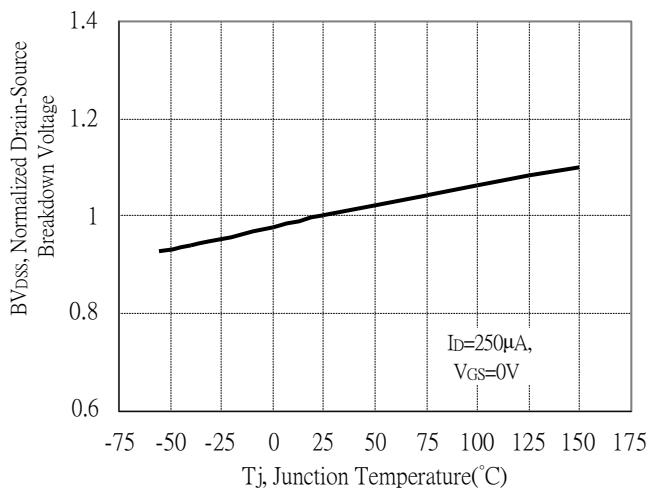


## Typical Characteristics

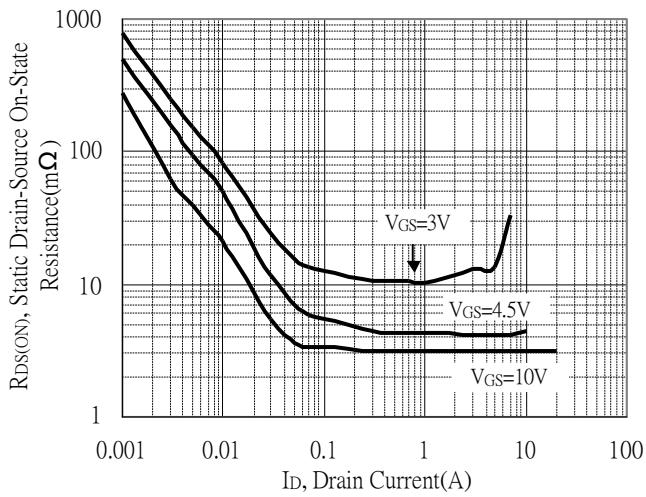
Typical Output Characteristics



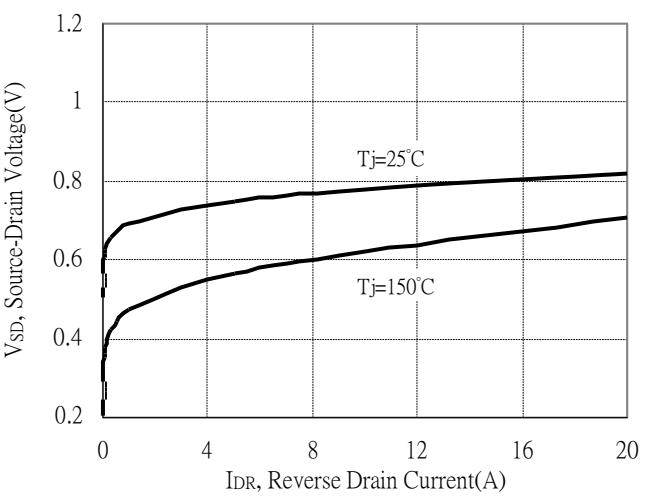
Breakdown Voltage vs Ambient Temperature



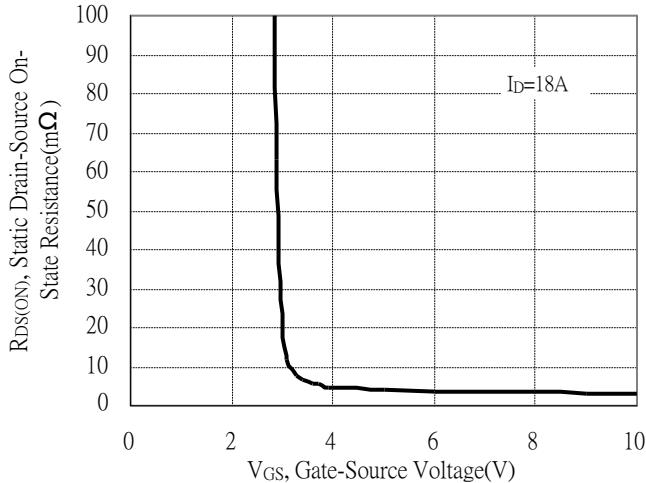
Static Drain-Source On-State resistance vs Drain Current



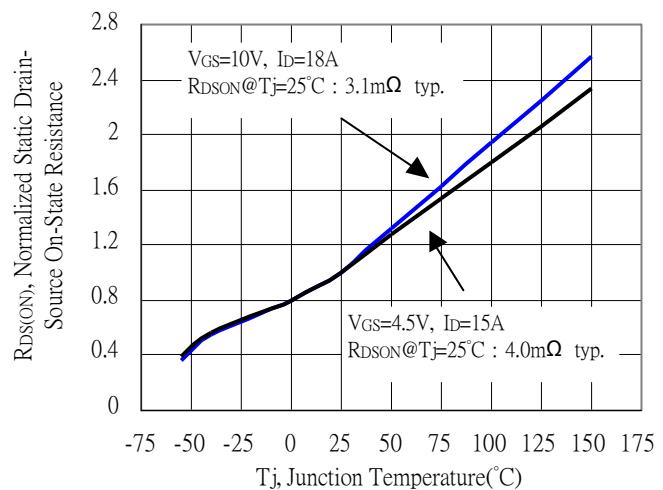
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

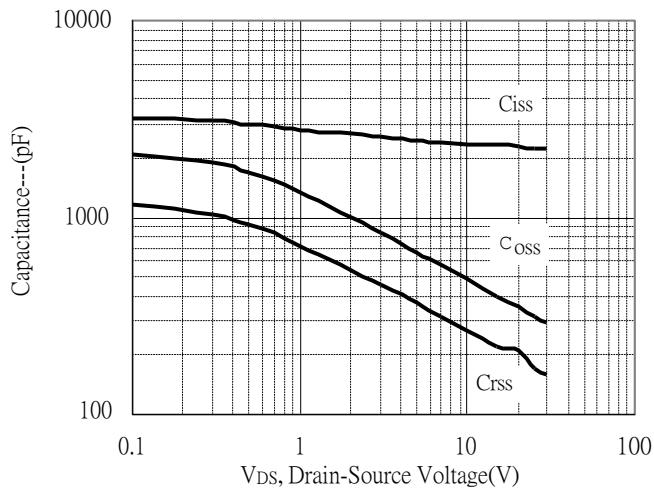


Drain-Source On-State Resistance vs Junction Temperature

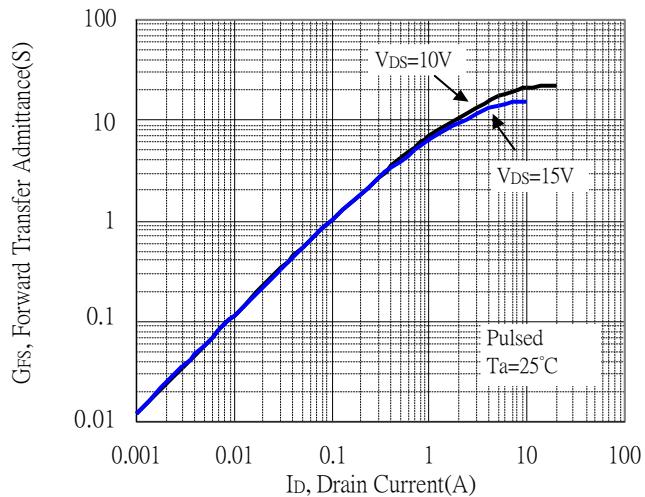


## Typical Characteristics(Cont.)

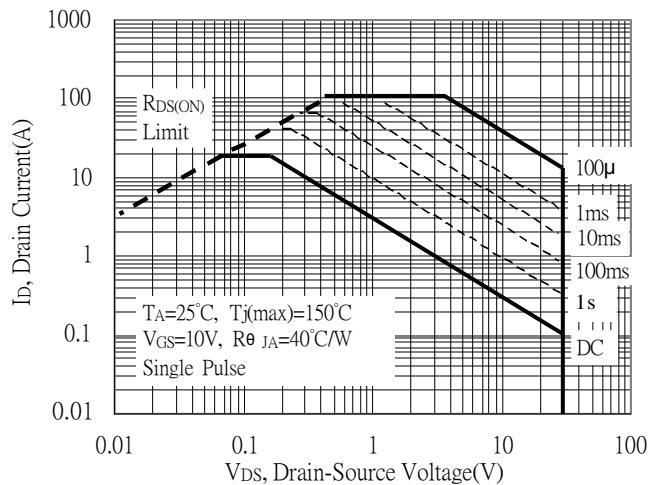
Capacitance vs Drain-to-Source Voltage



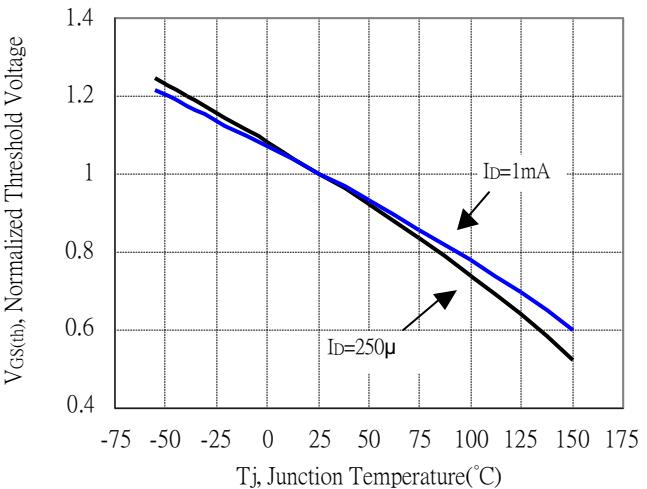
Forward Transfer Admittance vs Drain Current



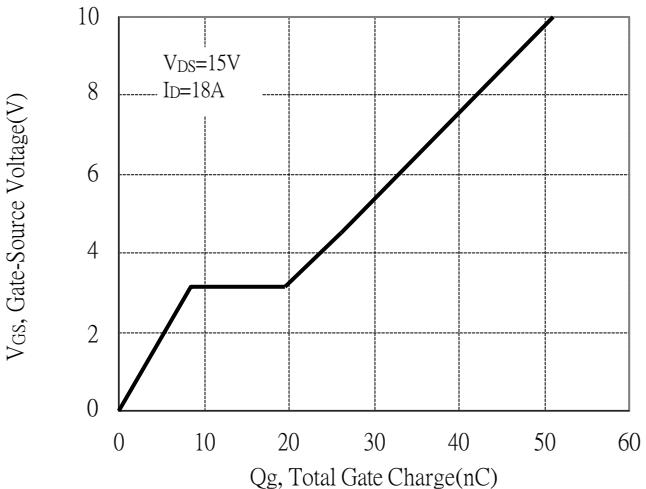
Maximum Safe Operating Area



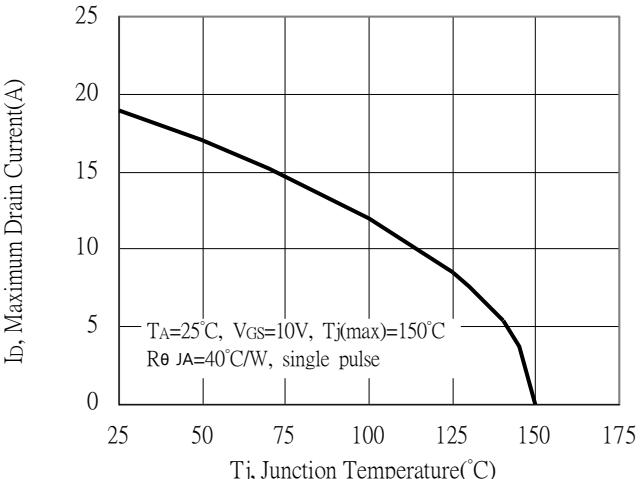
Threshold Voltage vs Junction Temperature



Gate Charge Characteristics

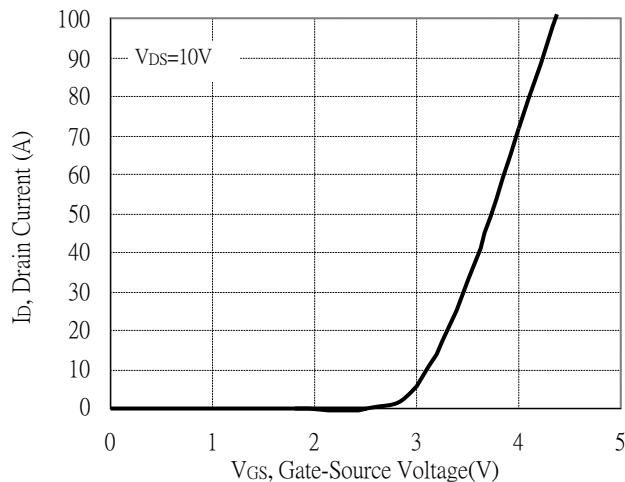


Maximum Drain Current vs Junction Temperature

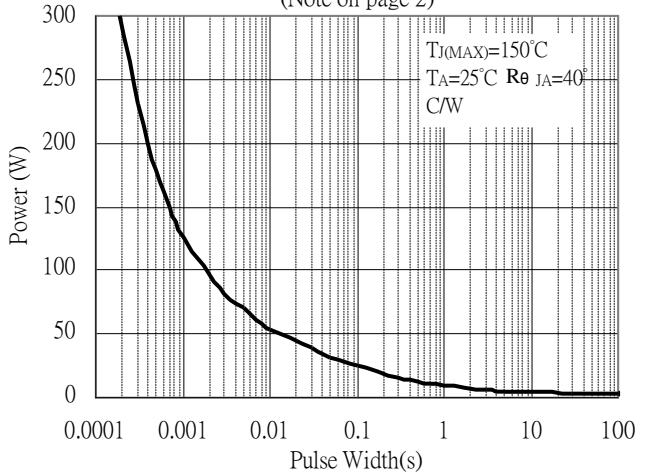


## Typical Characteristics(Cont.)

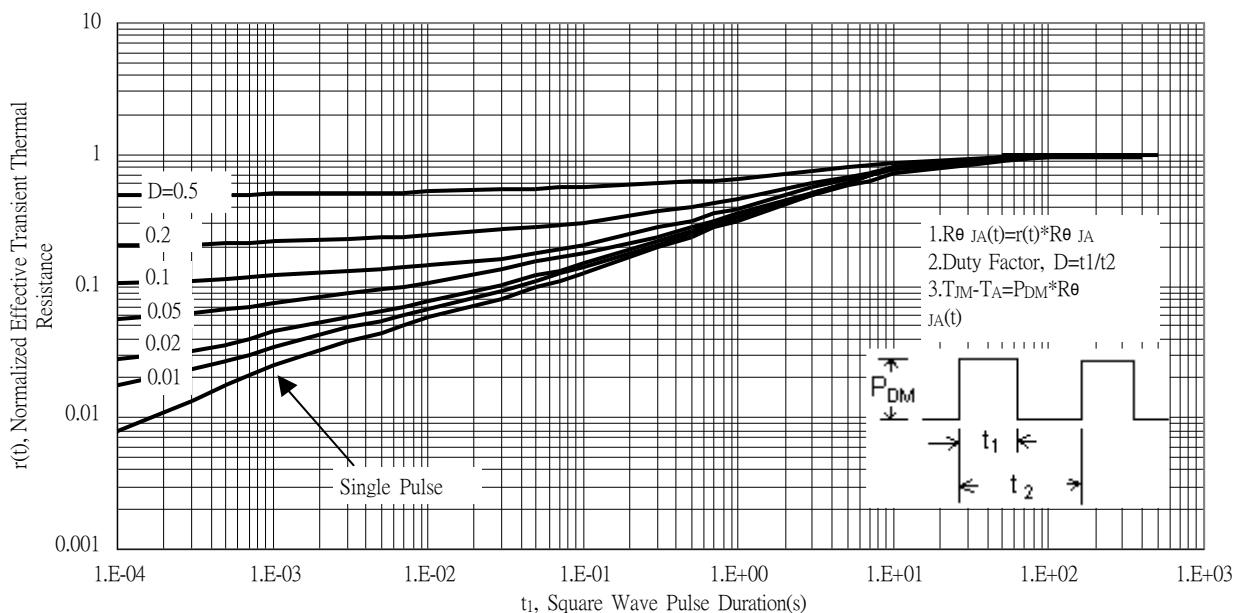
Typical Transfer Characteristics



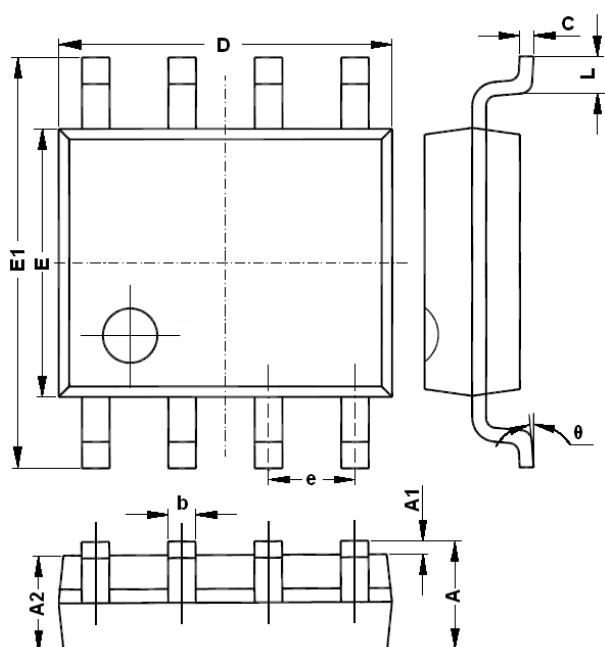
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



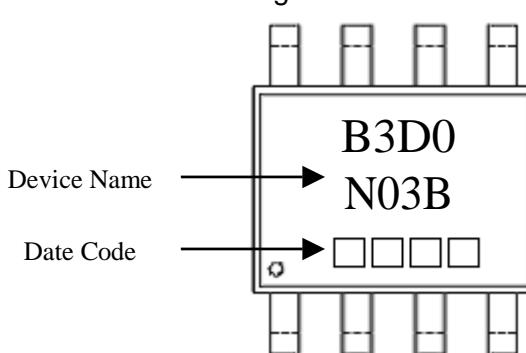
Transient Thermal Response Curves



## SOP-8 Dimension



Marking:



8-Lead SOP-8 Plastic Package  
 Code: Q8

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069	E	3.800	4.000	0.150	0.157
A1	0.100	0.250	0.004	0.010	E1	5.800	6.200	0.228	0.244
A2	1.350	1.550	0.053	0.061	e	1.270 (BSC)		0.050 (BSC)	
b	0.330	0.510	0.013	0.020	L	0.400	1.270	0.016	0.050
c	0.170	0.250	0.006	0.010	θ	0	8°	0	8°
D	4.700	5.100	0.185	0.200					