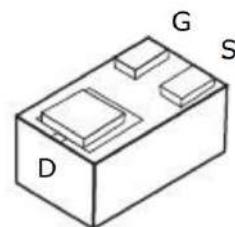


N-Channel Enhancement Mode MOSFET

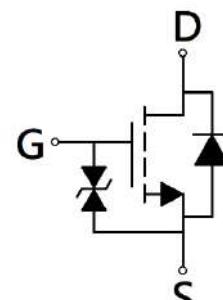
Features:

- ESD protected gate, typical 3kV (HBM)
- High speed switching
- Easily designed drive circuits
- Low-voltage drive
- Easy to use in parallel
- RoHS compliant package

WBFBP-03E



BV_{DSS}	20V
$I_D @ V_{GS}=4.5V, T_c=25^\circ C$	1.4A
$I_D @ V_{GS}=4.5V, T_A=25^\circ C$	1.1A
$R_{DS(ON)} \text{ typ.} @ V_{GS}=4.5V, I_D=0.5A$	0.3Ω
$R_{DS(ON)} \text{ typ.} @ V_{GS}=2.5V, I_D=0.3A$	0.4Ω
$R_{DS(ON)} \text{ typ.} @ V_{GS}=1.8V, I_D=0.3A$	0.8Ω



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KWAK9	WBFBP-03E (Pb-free lead plating and halogen-free package)	10000 pcs / Tape & Reel

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current @ $V_{GS}=4.5\text{V}$, $T_C=25^\circ\text{C}$	I_D	1.4	A
Continuous Drain Current @ $V_{GS}=4.5\text{V}$, $T_C=100^\circ\text{C}$		0.9	
Continuous Drain Current @ $V_{GS}=4.5\text{V}$, $T_A=25^\circ\text{C}$		1.1	
Continuous Drain Current @ $V_{GS}=4.5\text{V}$, $T_A=70^\circ\text{C}$		0.9	
Pulsed Drain Current	I_{DM}	5.6	V
Continuous Body Diode Forward Current @ $T_A=25^\circ\text{C}$	I_S	1	
ESD susceptibility	V_{ESD}	3000	V
Total Power Dissipation	$T_C=25^\circ\text{C}$	1.4	W
	$T_C=100^\circ\text{C}$	0.6	
	$T_A=25^\circ\text{C}$	0.9	
	$T_A=70^\circ\text{C}$	0.6	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ\text{C}$

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	$R_{\theta JC}$	90	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	136	

Note:

- *a. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- *b. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in²FR -4 board with 2 oz. copper, in a still air environment with $T_A=25^\circ\text{C}$. The power dissipation P_D is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.

Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise specified)

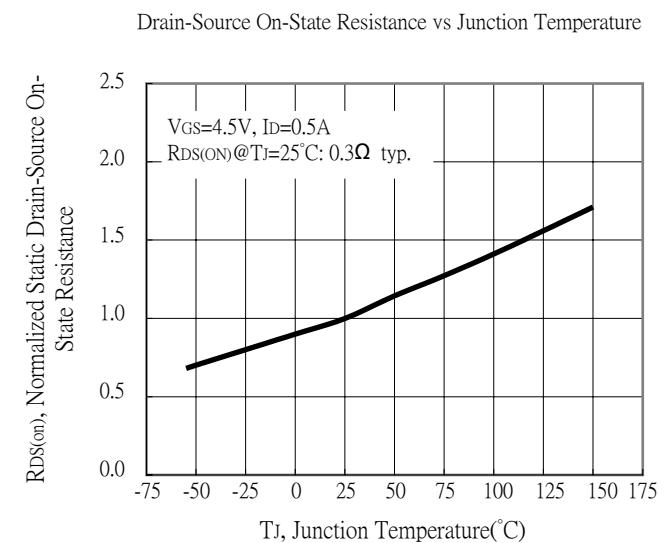
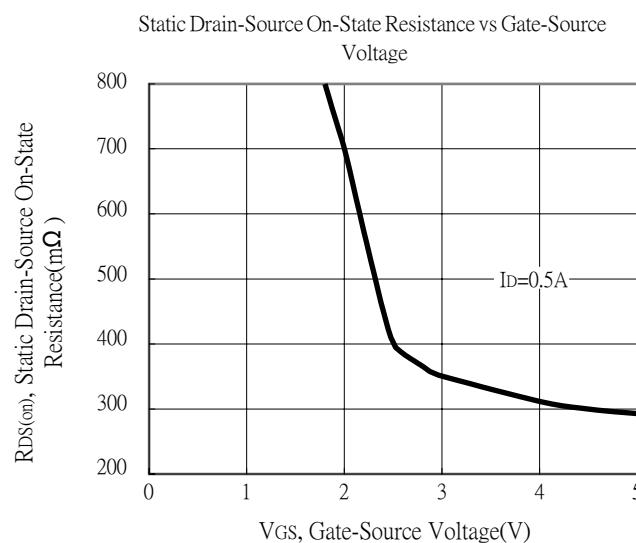
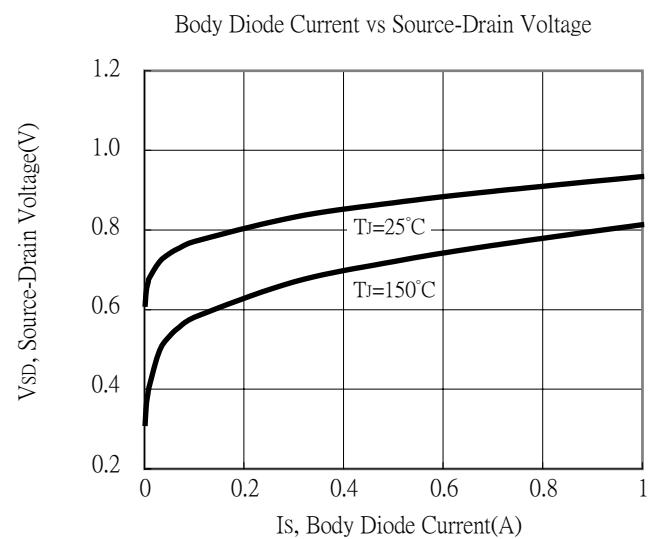
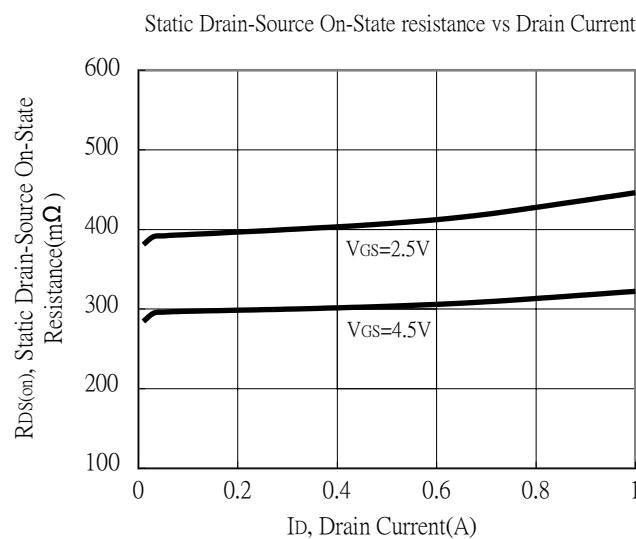
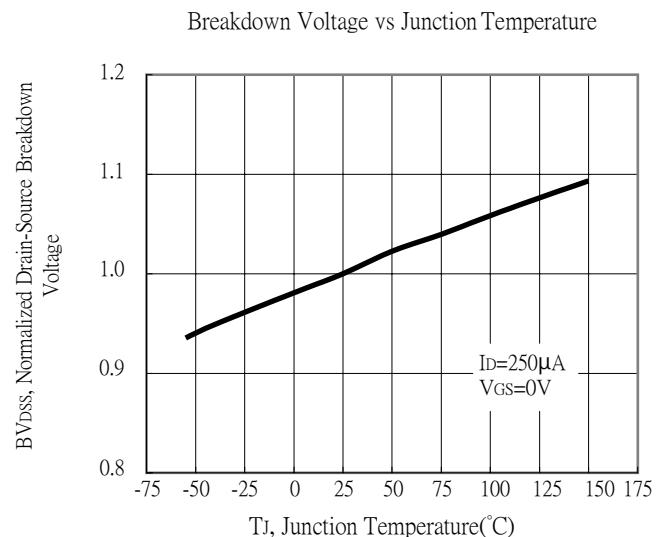
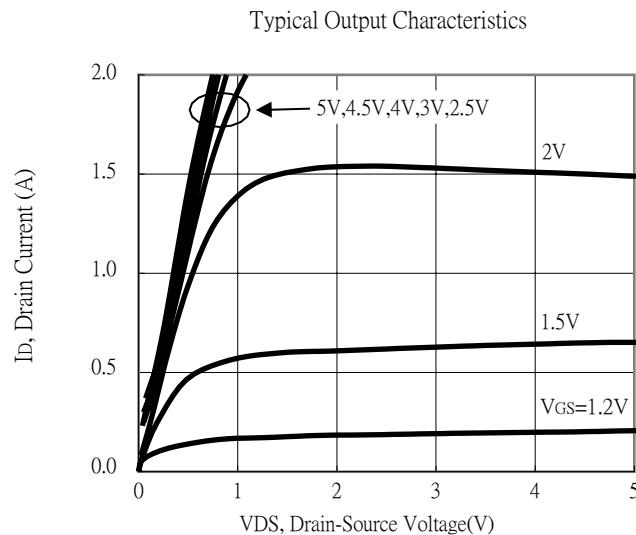
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	20	-	-	V	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$
V _{GS(th)}	0.3	-	1.2	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
G _{FS}	-	1.2	-	S	$V_{DS}=5\text{V}, I_D=0.3\text{A}$
I _{GSS}	-	-	± 10	μA	$V_{GS}=\pm 8\text{V}, V_{DS}=0\text{V}$
I _{DSS}	-	-	1		$V_{DS}=16\text{V}, V_{GS}=0\text{V}$
R _{D(S(ON))}	-	0.3	0.4	Ω	$V_{GS}=4.5\text{V}, I_D=0.5\text{A}$
	-	0.4	0.6		$V_{GS}=2.5\text{V}, I_D=0.3\text{A}$
	-	0.8	1.2		$V_{GS}=1.8\text{V}, I_D=0.3\text{A}$
Dynamic					
C _{iss}	-	32	-	pF	$V_{DS}=10\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$
C _{oss}	-	19	-		
C _{rss}	-	17	-		
Q _g *1, 2	-	0.8	-	nC	$V_{DS}=10\text{V}, I_D=0.3\text{A}, V_{GS}=4.5\text{V}$
Q _{gs} *1, 2	-	0.2	-		
Q _{gd} *1, 2	-	0.15	-		
t _{d(ON)} *1, 2	-	4.8	-	ns	$V_{DS}=10\text{V}, I_D=0.3\text{A}, V_{GS}=4.5\text{V}, R_{GS}=10\Omega$
t _r *1, 2	-	16	-		
t _{d(OFF)} *1, 2	-	20	-		
t _f *1, 2	-	15.6	-		
Source-Drain Diode					
V _{SD} *1	-	0.83	1.2	V	I _s =0.3A, V _{GS} =0V
trr	-	7	-	ns	I _F =0.3A, dI _F /dt=100A/ μs
Qrr	-	1	-	nC	

Note:

*1. Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

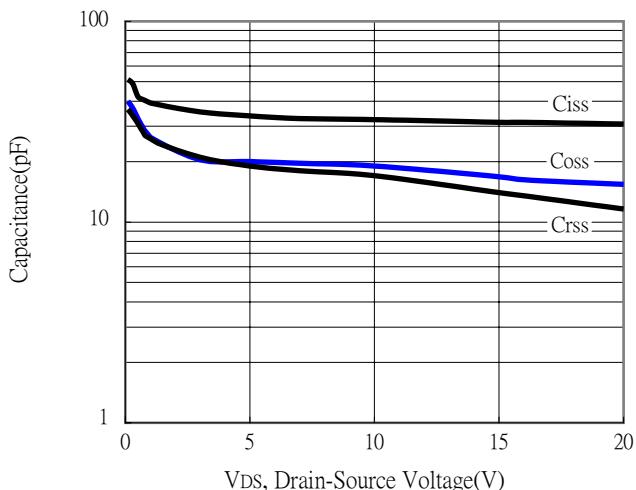
*2. Independent of operating temperature

Typical Characteristics

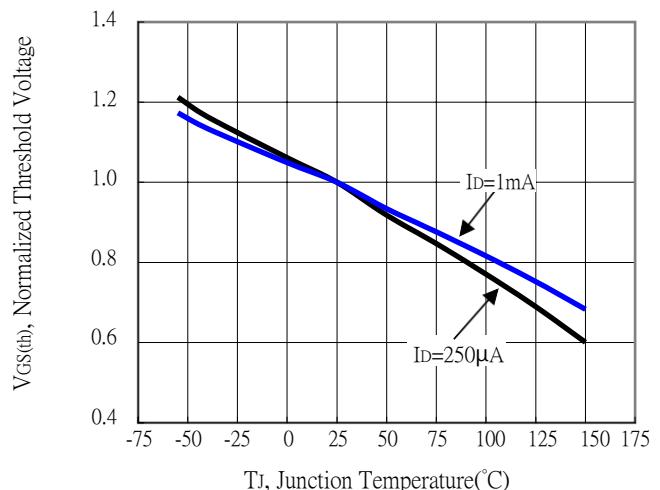


Typical Characteristics (Cont.)

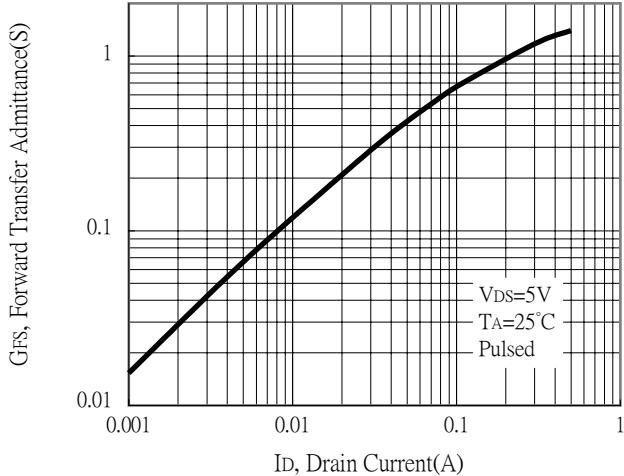
Capacitance vs Drain-to-Source Voltage



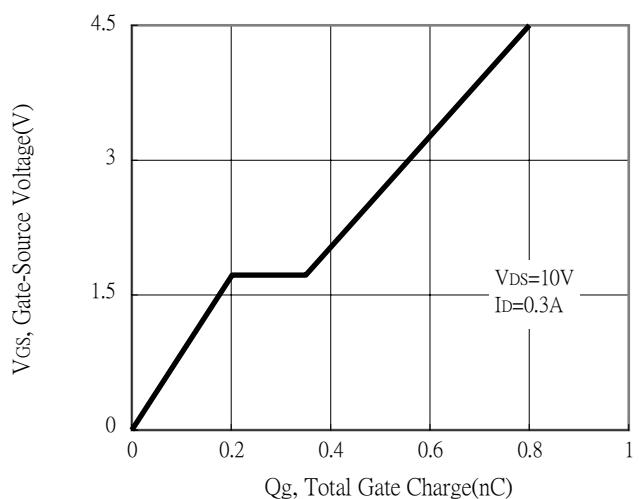
Threshold Voltage vs Junction Temperature



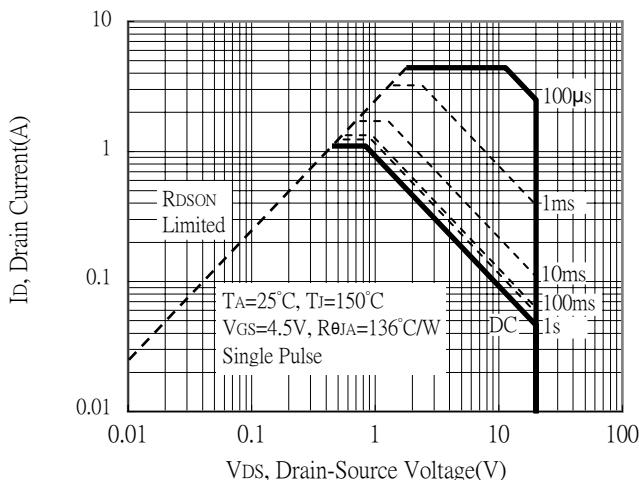
Forward Transfer Admittance vs Drain Current



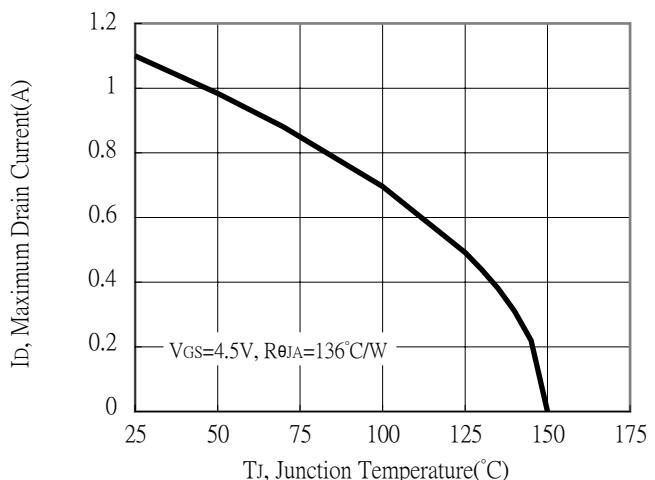
Gate Charge Characteristics



Maximum Safe Operating Area

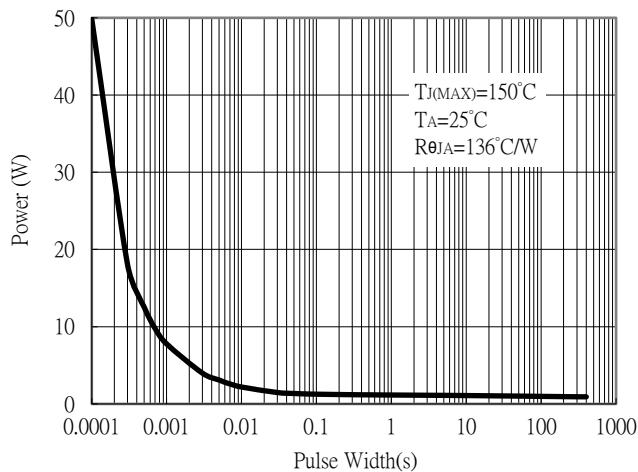


Maximum Drain Current vs Junction Temperature

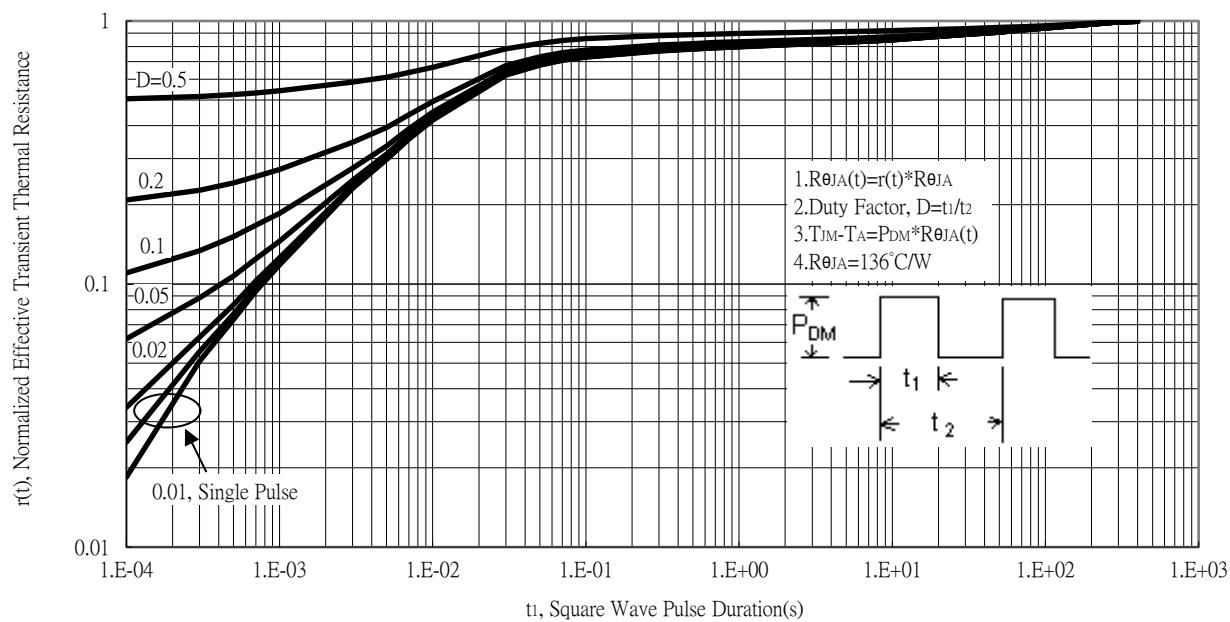


Typical Characteristics (Cont.)

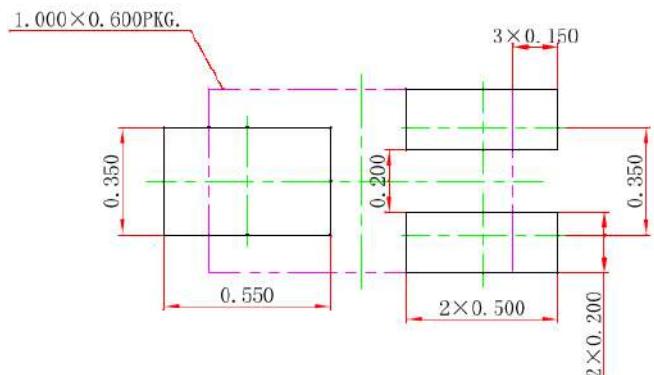
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves



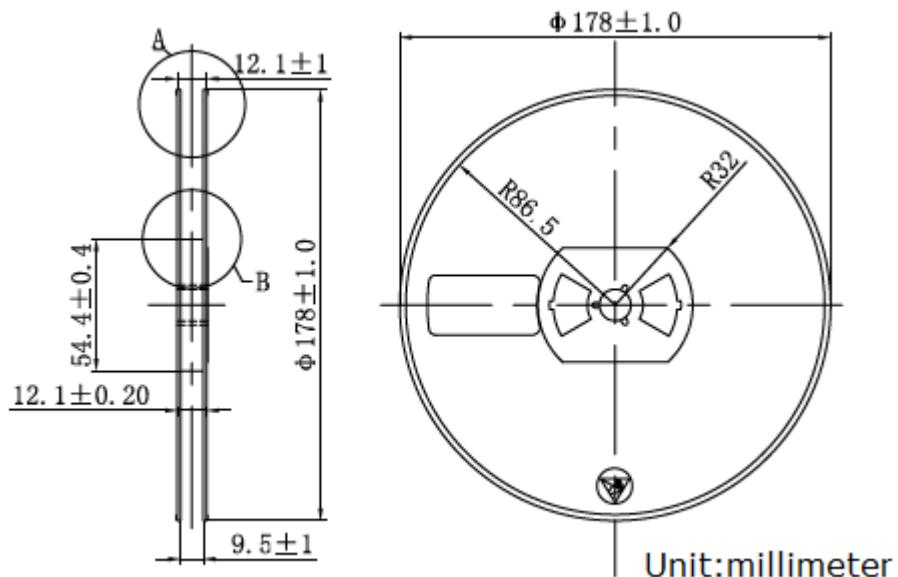
Recommended Soldering Footprint



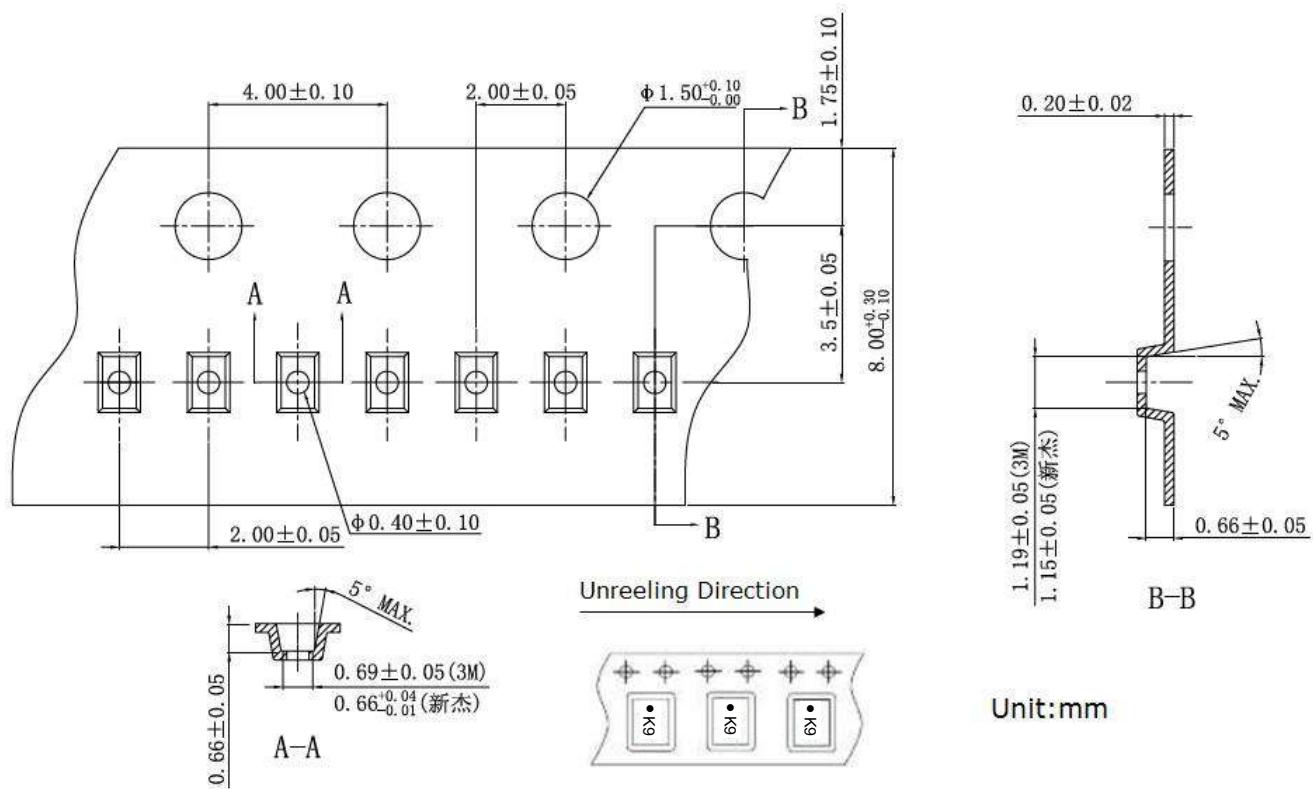
Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.050\text{mm}$.
3. The pad layout is for reference purposes only.

Reel Dimension



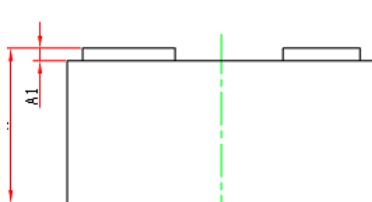
Carrier Tape Dimension



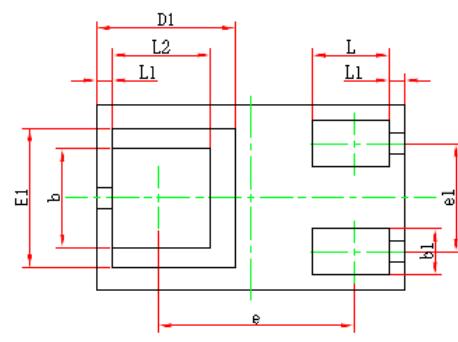
WBFBP-03E Dimension



TOP VIEW

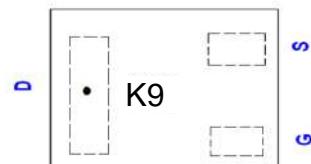


SIDE VIEW



BOTTOM VIEW

Marking:



3-Lead WBFBP-03E Plastic Surface Mounted Package

Style: Pin 1.Gate 2.Source 3.Drain

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.018	0.022	0.450	0.550	b1	0.004	0.008	0.100	0.200
A1	0.000	0.004	0.010	0.100	e	0.025	REF	0.635	REF
D	0.037	0.041	0.950	1.050	e1	0.012	0.016	0.300	0.400
E	0.022	0.026	0.550	0.650	L	0.008	0.012	0.200	0.300
D1	0.018 REF		0.450 REF		L1	0.002 REF		0.050 REF	
E1	0.018 REF		0.450 REF		L2	0.011	0.015	0.270	0.370
b	0.011	0.015	0.270	0.370					