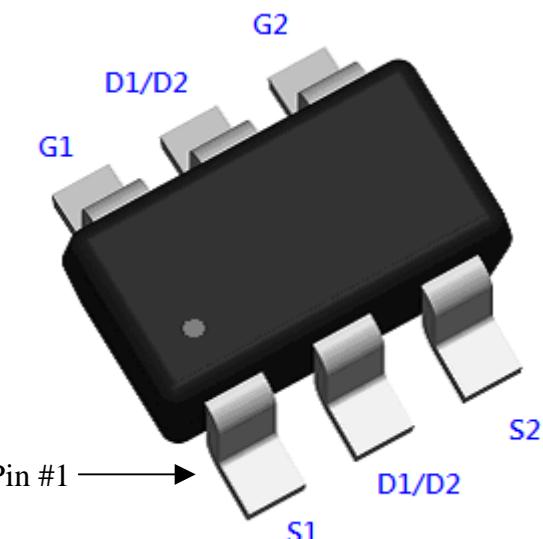


## Common Drain Dual N -Channel Enhancement Mode MOSFET

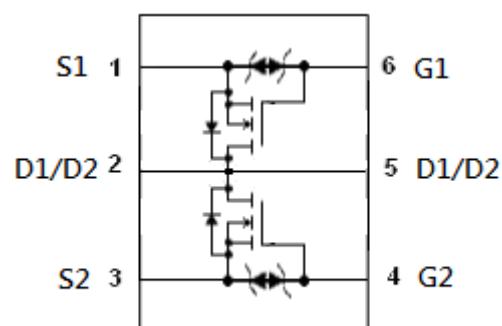
### Features:

- Simple drive requirement
- Low gate charge
- Low on-resistance
- Fast switching speed
- ESD protected gate
- Pb-free lead plating and halogen-free package

SOT-26



<b>BV<sub>DSS</sub></b>		20V
ID	V <sub>GS</sub> =4.5V, T <sub>A</sub> =25°C	7A
R <sub>DSON</sub> (TYP.)	V <sub>GS</sub> =4.5V, ID=5A	15.3 mΩ
	V <sub>GS</sub> =2.5V, ID=2.6A	18.5 mΩ
	V <sub>GS</sub> =1.8V, ID=1A	30.5 mΩ



G : Gate   S : Source   D : Drain

### Ordering Information

Device	Package	Shipping
KWA012A02	SOT-26 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

### Absolute Maximum Ratings ( $T_a=25^\circ C$ , unless otherwise specified)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	20	<b>V</b>
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current @ $V_{GS}=4.5V$ , $T_c=25^\circ C$	$I_D$	12.4	<b>A</b>
Continuous Drain Current @ $V_{GS}=4.5V$ , $T_c=100^\circ C$		7.8	
Continuous Drain Current @ $V_{GS}=4.5V$ , $T_A=25^\circ C$		7	
Continuous Drain Current @ $V_{GS}=4.5V$ , $T_A=70^\circ C$		5.6	
Pulsed Drain Current	$I_{DM}$	50 *1, 2	<b>W</b>
Total Power Dissipation	$P_D$	2 *3	
$T_A=70^\circ C$		1.3 *3	
Operating Junction and Storage Temperature Range	$T_j$ , $T_{stg}$	-55~+150	<b>°C</b>

### Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	20	$^\circ C/W$
Thermal Resistance, Junction-to-ambient, max	$R_{\theta JA}$	62.5 *3	

Note : 1. Pulse width limited by maximum junction temperature.  
 2. Duty cycle  $\leq 1\%$ .  
 3. Surface mounted on a 1 in<sup>2</sup> pad of 2oz copper. In practice  $R_{\theta j-a}$  will be determined by the customer's PCB characteristics.

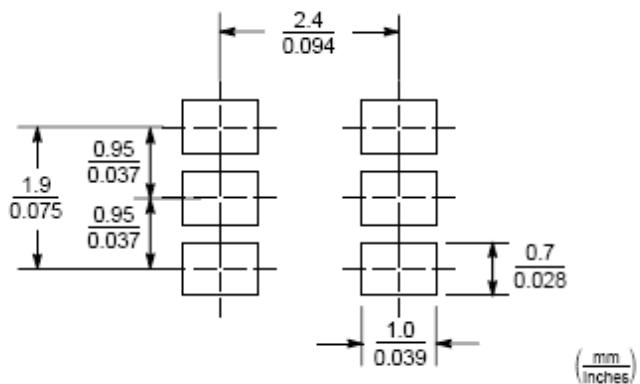
### Electrical Characteristics ( $T_j=25^\circ C$ , unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
<b>Static</b>						
$BV_{DSS}$	20	-	-	V	$V_{GS}=0V$ , $I_D=250\mu A$	
$\Delta BV_{DSS}/\Delta T_j$	-	0.02	-	$V/^\circ C$	Reference to $25^\circ C$ , $I_D=250\mu A$	
$V_{GS(th)}$	0.4	-	1.0	V	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	
$IG_{SS}$	-	-	$\pm 20$	$\mu A$	$V_{GS}=\pm 8V$ , $V_{DS}=0V$	
$ID_{SS}$	-	-	1		$V_{DS}=20V$ , $V_{GS}=0V$	
	-	-	10		$V_{DS}=16V$ , $V_{GS}=0V$ , $T_j=70^\circ C$	
$*R_{DS(ON)}$	-	15.3	22	$m\wedge$	$I_D=5A$ , $V_{GS}=4.5V$	
	-	18.5	30		$I_D=2.6A$ , $V_{GS}=2.5V$	
	-	30.5	65		$I_D=1A$ , $V_{GS}=1.8V$	
$*G_{FS}$	-	4.8	-	S	$V_{DS}=10V$ , $I_D=1A$	
<b>Dynamic</b>						
$C_{iss}$	-	680	-	pF	$V_{DS}=10V$ , $V_{GS}=0V$ , $f=1MHz$	
$C_{oss}$	-	186	-			
$C_{rss}$	-	31	-			

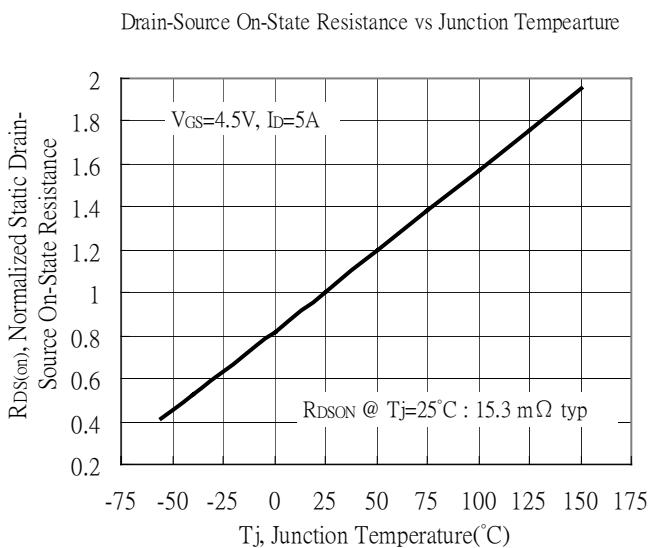
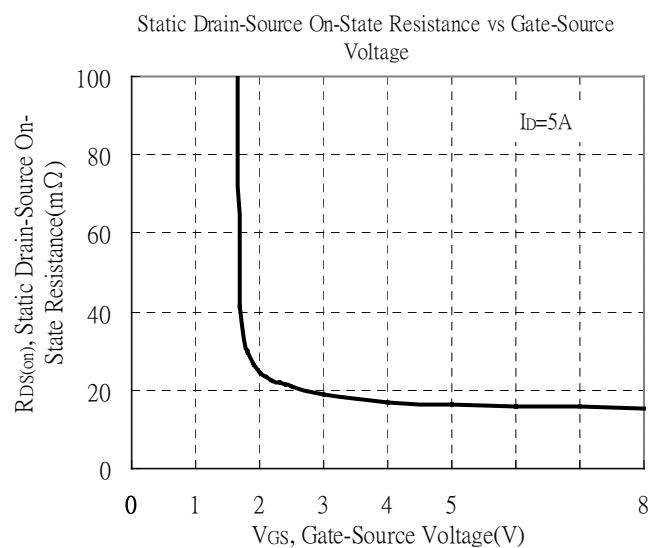
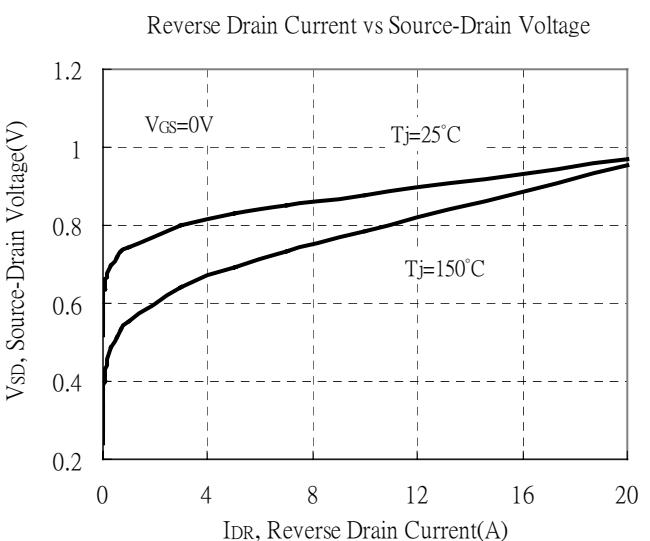
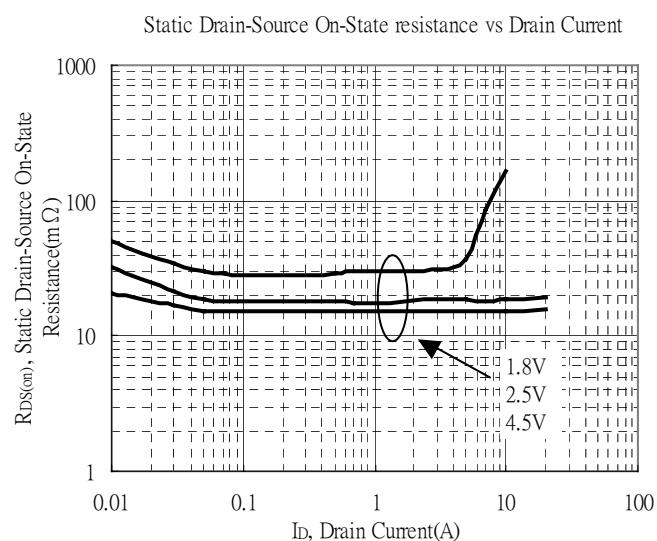
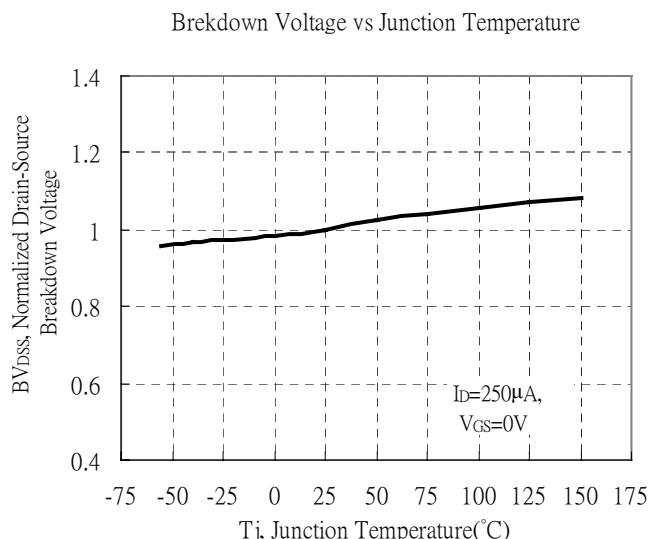
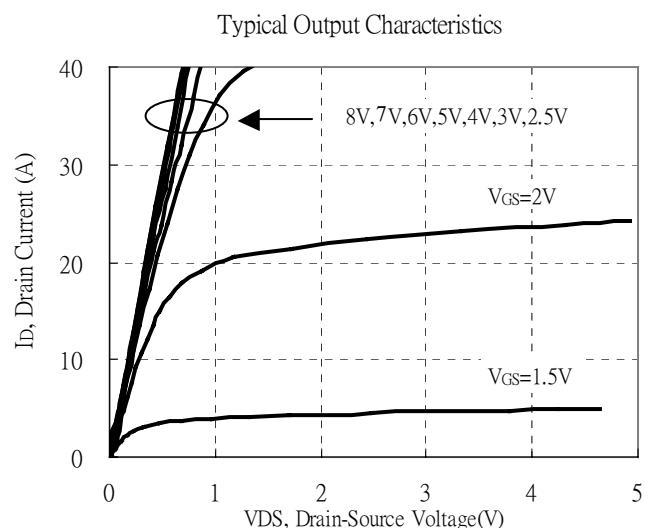
*t <sub>d(ON)</sub>	-	82.4	-	ns	V <sub>DS</sub> =10V, I <sub>D</sub> =5A, V <sub>GS</sub> =4.5V, R <sub>G</sub> =6Ω
*t <sub>r</sub>	-	46.8	-		
*t <sub>d(OFF)</sub>	-	319.4	-		
*t <sub>f</sub>	-	888.8	-		
*Q <sub>g</sub>	-	10.5	-	nC	V <sub>DS</sub> =16V, I <sub>D</sub> =5A, V <sub>GS</sub> =4.5V
*Q <sub>gs</sub>	-	1	-		
*Q <sub>gd</sub>	-	3.1	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	3	A	Is=1.2A, V <sub>GS</sub> =0V
*I <sub>SM</sub>	-	-	5		
*V <sub>SD</sub>	-	0.76	1.2	V	Is=1.2A, V <sub>GS</sub> =0V

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

## Recommended Soldering Footprint

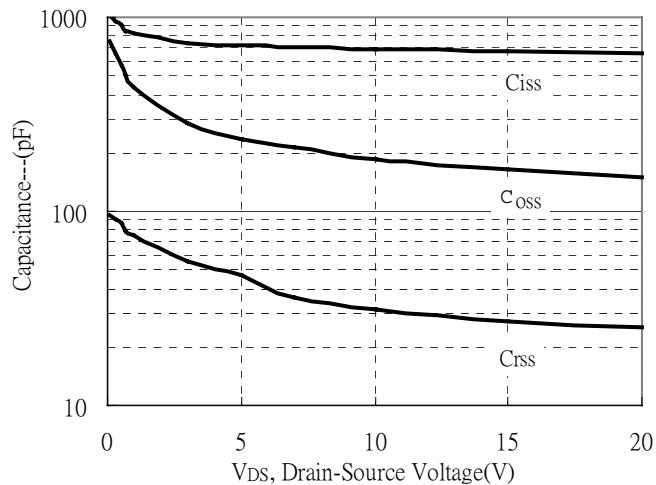


## Typical Characteristics

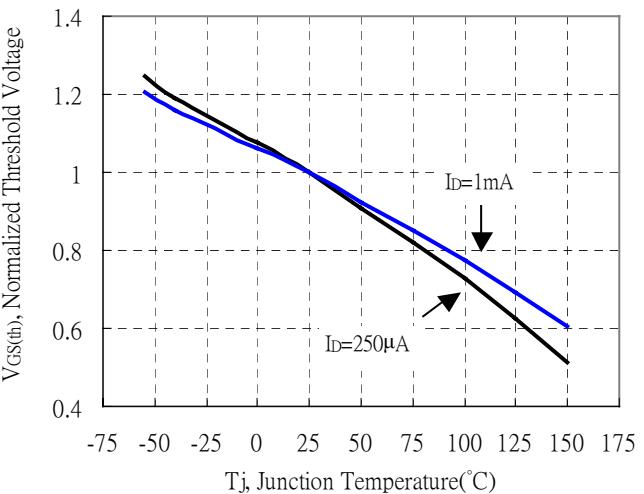


## Typical Characteristics(Cont.)

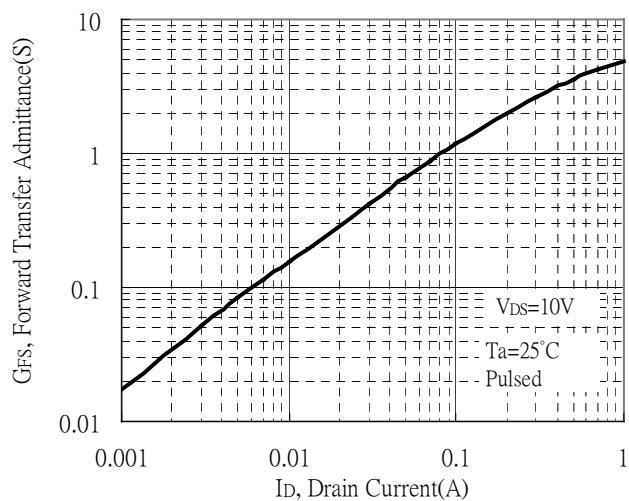
Capacitance vs Drain-to-Source Voltage



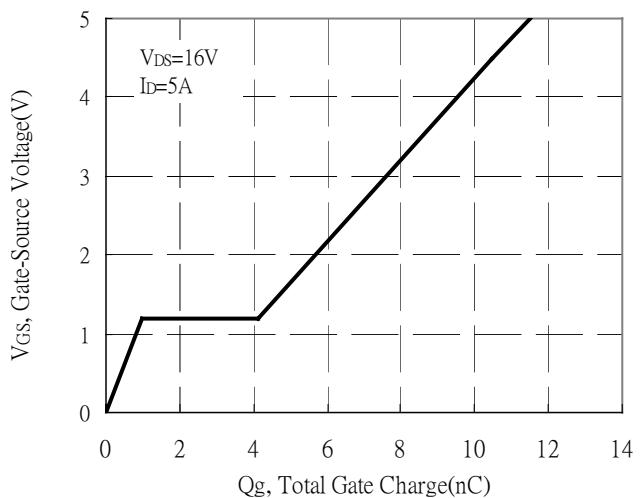
Threshold Voltage vs Junction Temperature



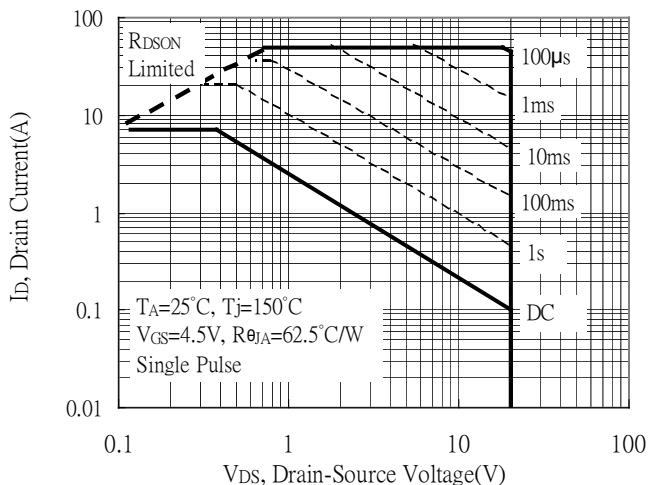
Forward Transfer Admittance vs Drain Current



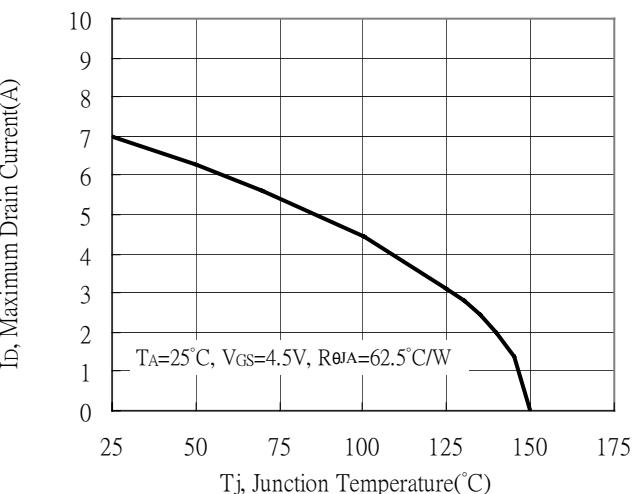
Gate Charge Characteristics



Maximum Safe Operating Area

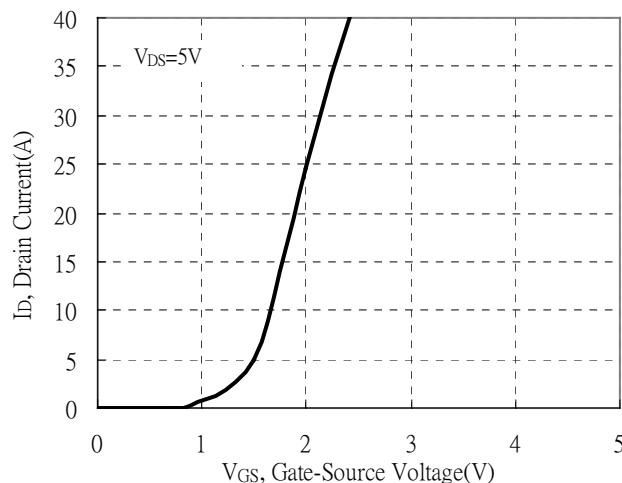


Maximum Drain Current vs Junction Temperature

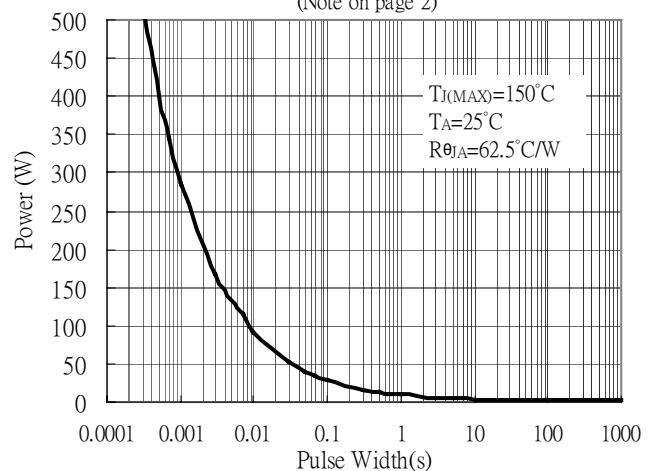


## Typical Characteristics(Cont.)

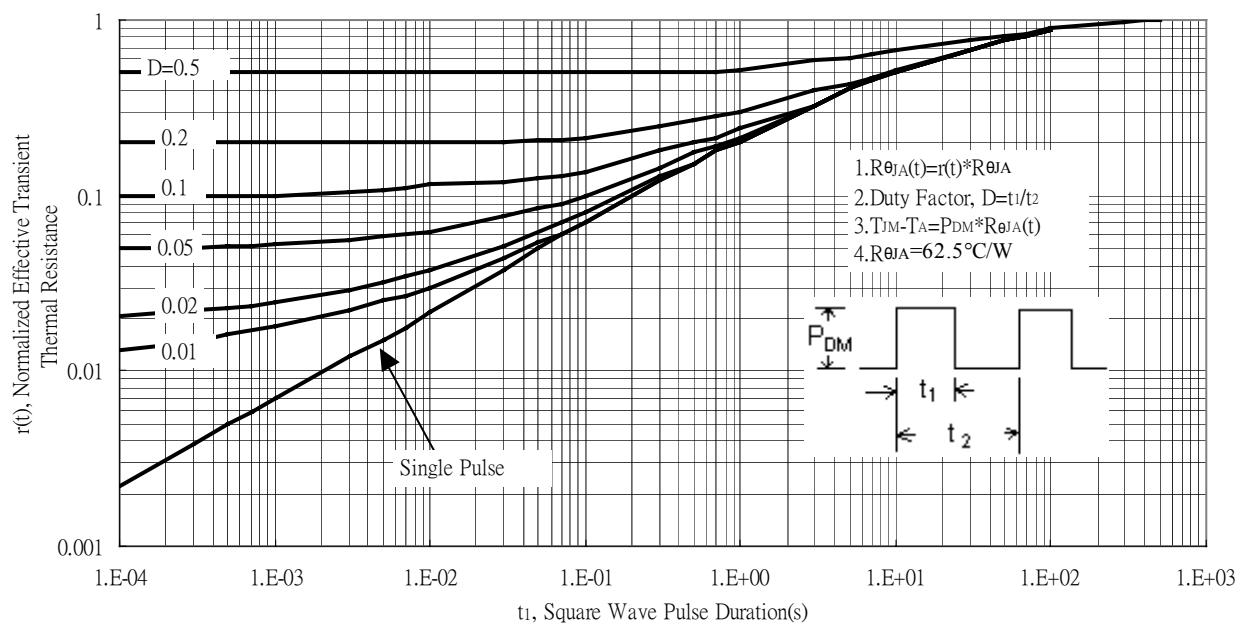
Typical Transfer Characteristics



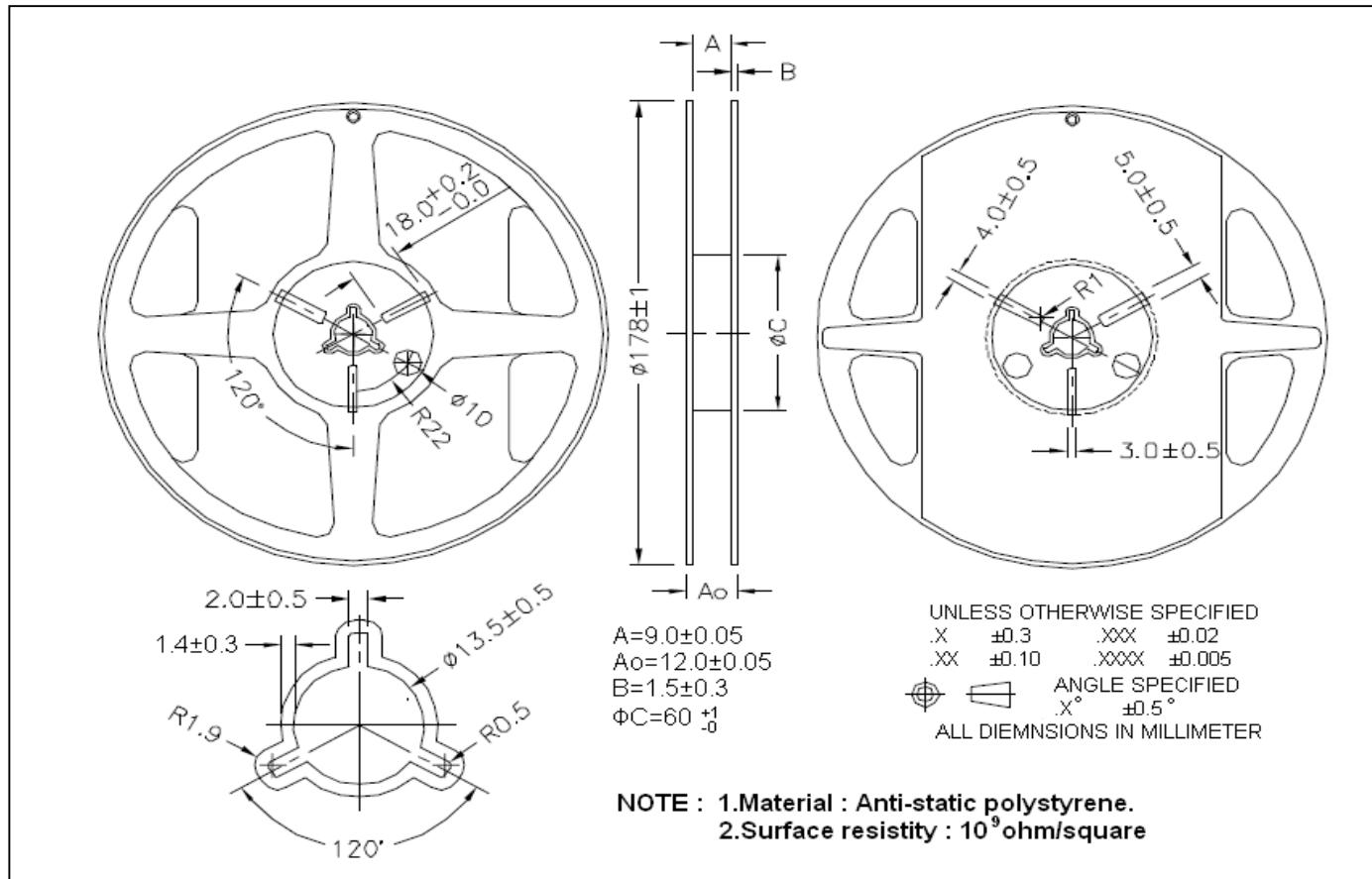
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



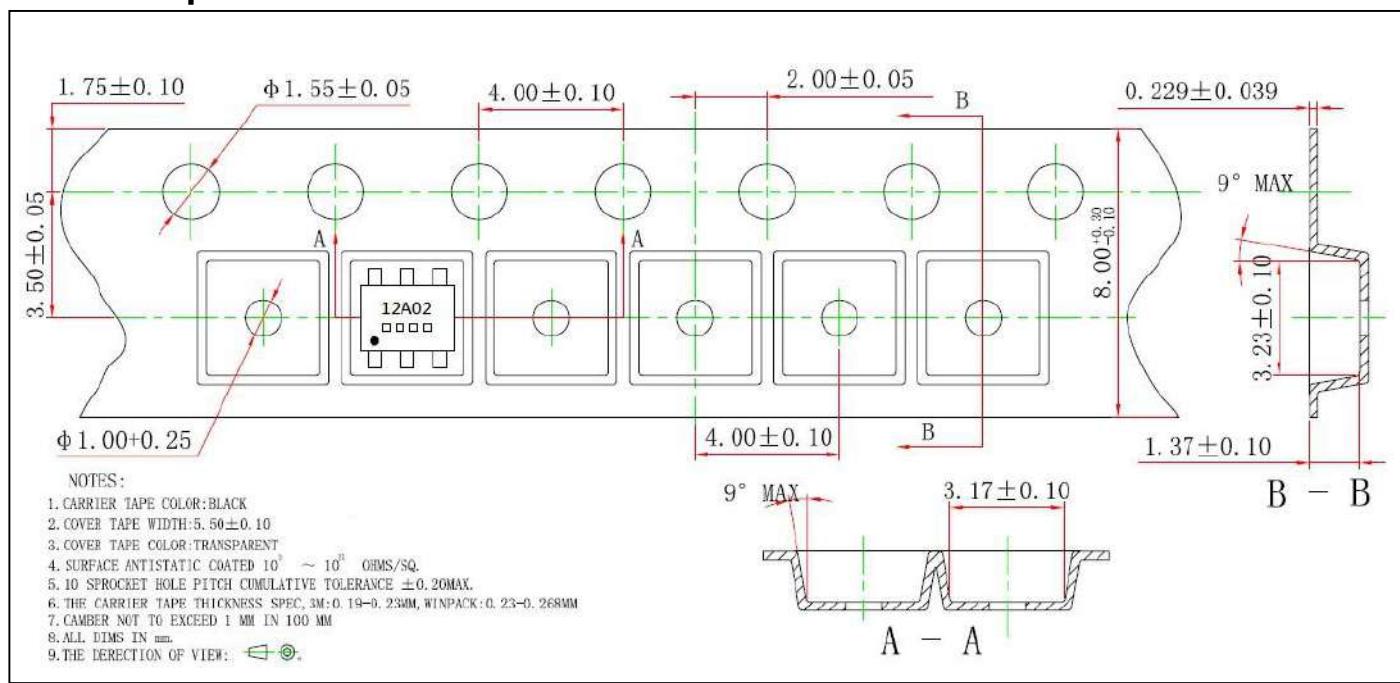
Transient Thermal Response Curves



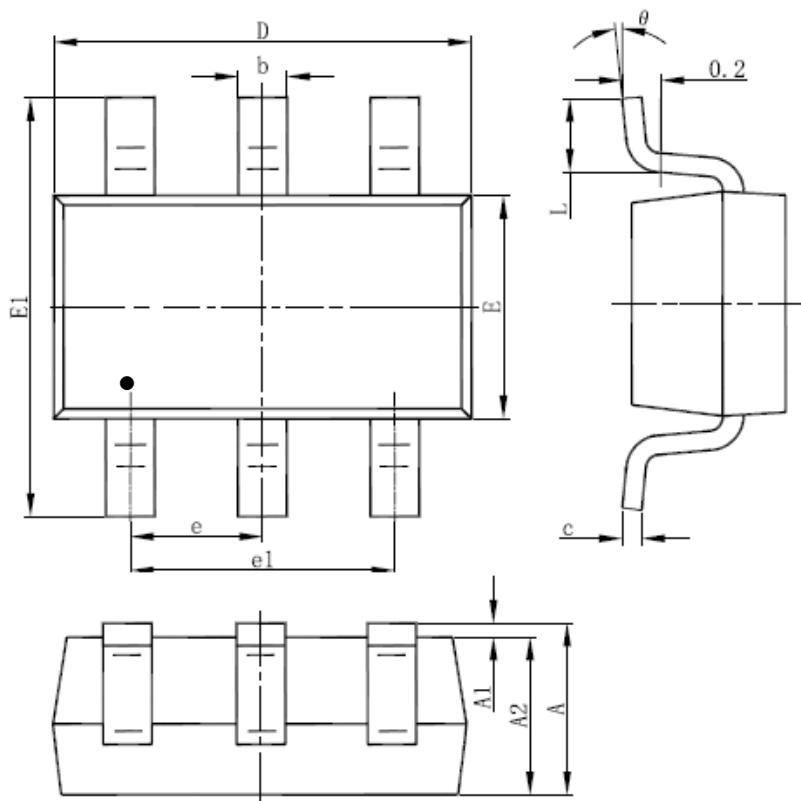
## Reel Dimension



## Carrier Tape Dimension



## SOT-26 Dimension



Marking:

Device Name →  
 Date Code

6-Lead SOT-26 Plastic  
 Surface Mounted Package

Style:

- Pin 1. Source 1 (S1)
- Pin 2. Drain1/Drain2 (D1/D2)
- Pin 3. Source 2 (S2)
- Pin 4. Gate 1 (G1)
- Pin 5. Drain 1/Drain 2 (D1/D2)
- Pin 6. Gate 2 (G2)

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049	E	1.500	1.700	0.059	0.067
A1	0.000	0.100	0.000	0.004	E1	2.650	2.950	0.104	0.116
A2	1.050	1.150	0.041	0.045	e	0.950 (BSC)		0.037 (BSC)	
b	0.300	0.500	0.012	0.020	e1	1.800	2.000	0.071	0.079
c	0.100	0.200	0.004	0.008	L	0.300	0.600	0.012	0.024
D	2.820	3.020	0.111	0.119	θ	0°	8°	0°	8°