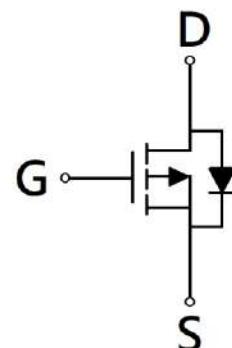
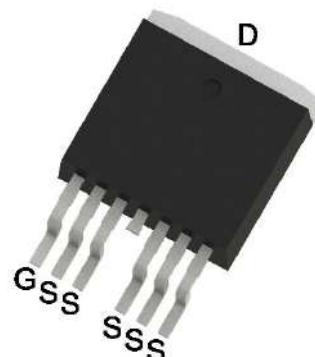


P-Channel Enhancement Mode Power MOSFET

Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

TO-263-7L-4C



G : Gate S : Source D : Drain

BV _{DSS}	-60V
I _D @V _{GS} =-10V, T _C =25°C	-124A
I _D @V _{GS} =-10V, T _A =25°C	-23A
R _{D(S)} (ON) typ. @ V _{GS} =-10V, I _D =-20A	3.5mΩ

Ordering Information

Device	Package	Shipping
KUE2D1P06	TO-263-7L-4C (Pb-free lead plating and RoHS compliant package)	800 pcs / Tape & Reel

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_c=25^\circ\text{C}$	I_D	-124	A
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_c=100^\circ\text{C}$		-78	
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_A=25^\circ\text{C}$		-23	
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_A=70^\circ\text{C}$		-18	
Pulsed Drain Current	I_{DM}	-496	
Continuous Body Diode Forward Current @ $T_c=25^\circ\text{C}$	I_S	-115	
Pulsed Body Diode Forward Current @ $T_c=25^\circ\text{C}$	I_{SM}	-460	
Avalanche Current @ $L=0.1\text{mH}$	I_{AS}	-60	
Avalanche Energy @ $L=0.5\text{mH}$	E_{AS}	625	mJ
Total Power Dissipation	$T_c=25^\circ\text{C}$	*a 139	W
	$T_c=100^\circ\text{C}$	*a 56	
	$T_A=25^\circ\text{C}$	*b 4.6	
	$T_A=70^\circ\text{C}$	*b 2.9	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	$R_{\theta JC}$	0.9	°C/W
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	27	

Note:

- *a. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- *b. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^\circ\text{C}$. The power dissipation P_D is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.

Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-60	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-2.5	-	-5		V _{DS} =V _{GS} , I _D =-250μA
G _{FS}	-	13.8	-	S	V _{DS} =-10V, I _D =-20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-48V, V _{GS} =0V
R _{DSS(ON)}	-	3.5	4.5	mΩ	V _{GS} =-10V, I _D =-20A
Dynamic					
C _{iss}	-	17000	-	pF	V _{DS} =-30V, V _{GS} =0V, f=1MHz
C _{oss}	-	1000	-		
C _{rss}	-	280	-		
R _g	-	2	-	Ω	f=1MHz
Q _g *1, 2	-	260	-	nC	V _{DS} =-30V, I _D =-20A, V _{GS} =-10V
Q _{gs} *1, 2	-	88	-		
Q _{gd} *1, 2	-	58	-		
t _{d(ON)} *1, 2	-	73	-	ns	V _{DS} =-30V, I _D =-20A, V _{GS} =-10V, R _{GS} =1Ω
t _r *1, 2	-	50	-		
t _{d(OFF)} *1, 2	-	185	-		
t _f *1, 2	-	80	-		
Source-Drain Diode					
V _{SD} *1	-	-0.78	-1.2	V	I _S =-20A, V _{GS} =0V
trr	-	43	-	ns	I _F =-20A, dI _F /dt=100A/μs
Qrr	-	62	-	nC	

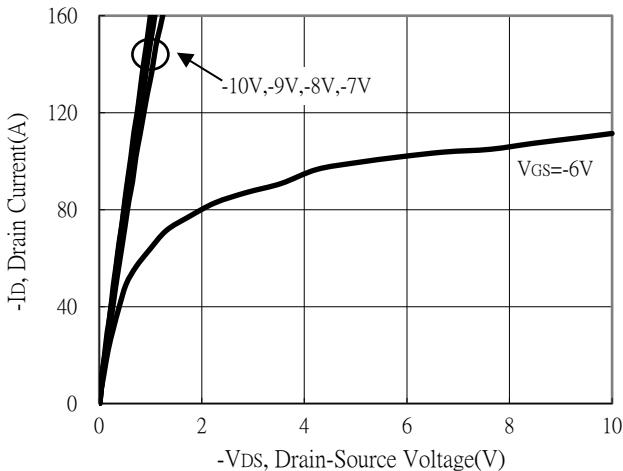
Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

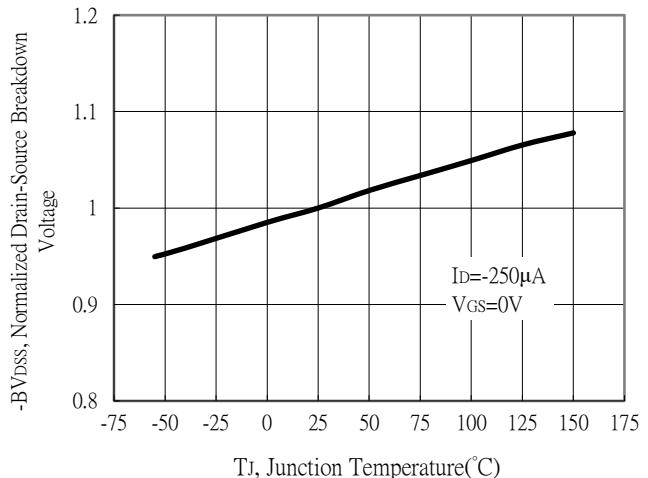
*2. Independent of operating temperature

Typical Characteristics

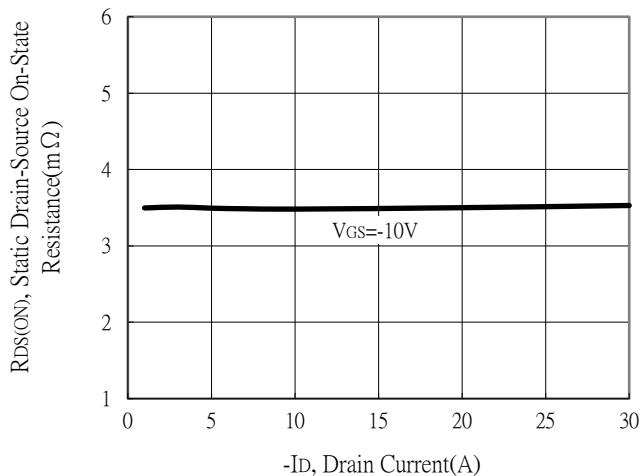
Typical Output Characteristics



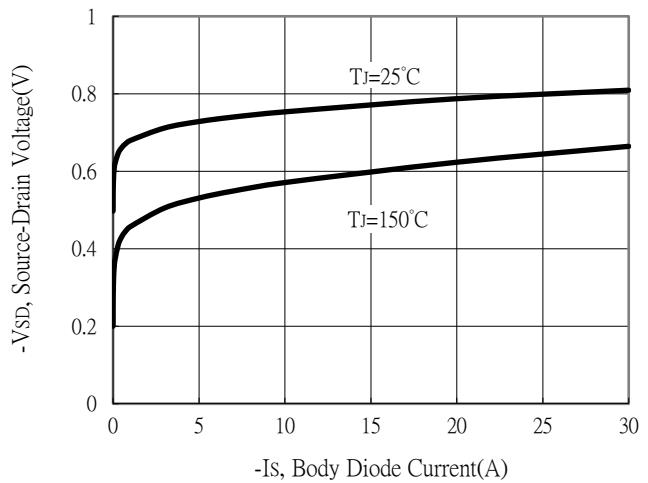
Breakdown Voltage vs Ambient Temperature



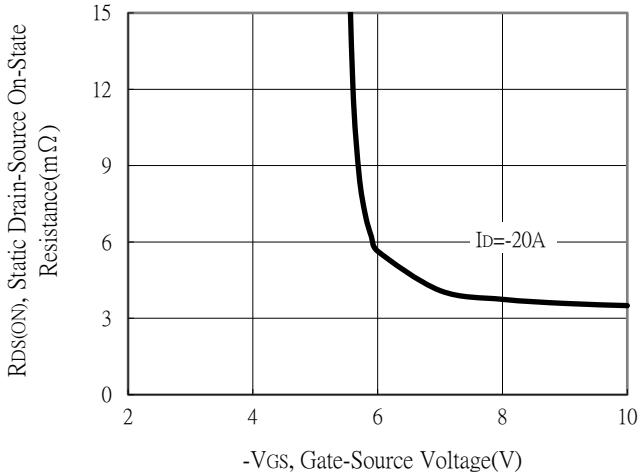
Static Drain-Source On-State resistance vs Drain Current



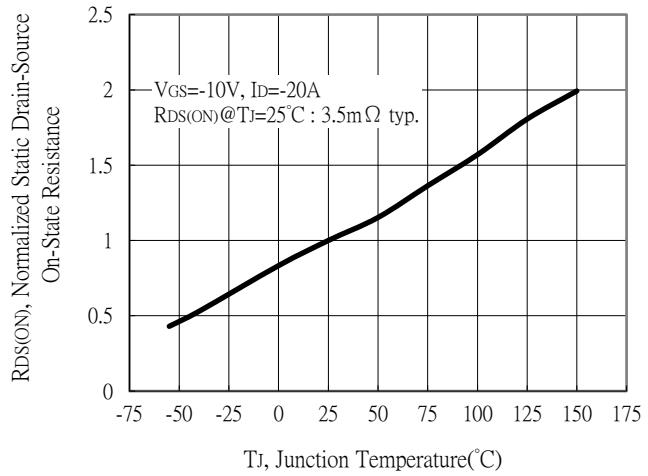
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

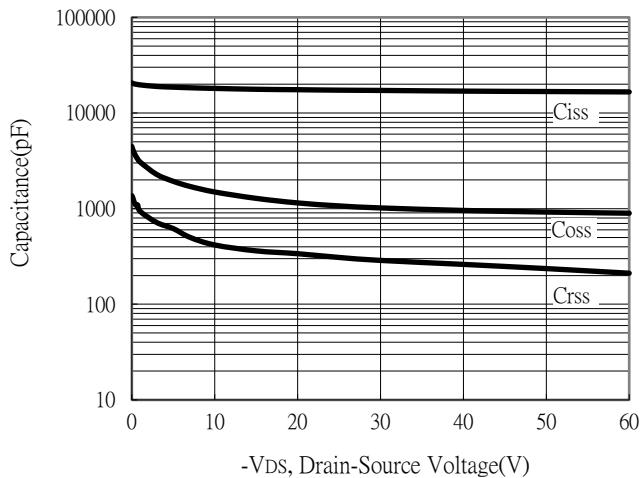


Drain-Source On-State Resistance vs Junction Temperature

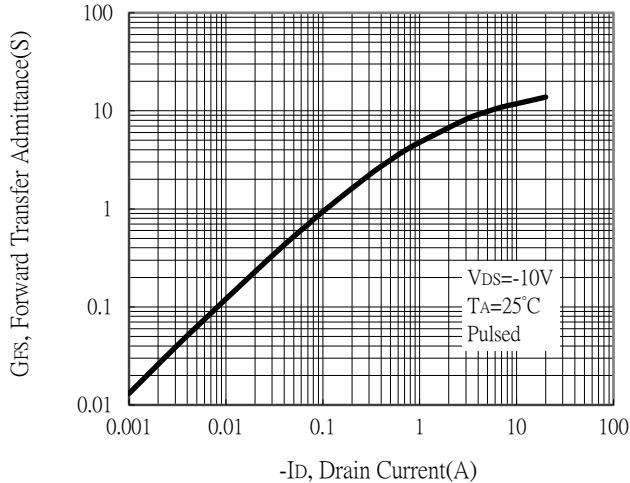


Typical Characteristics (Cont.)

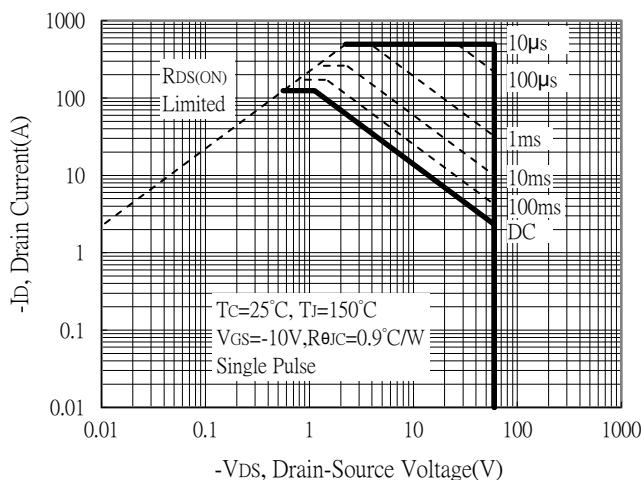
Capacitance vs Drain-to-Source Voltage



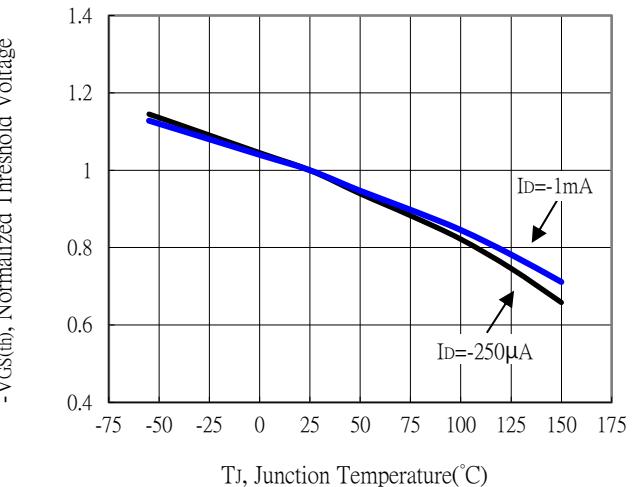
Forward Transfer Admittance vs Drain Current



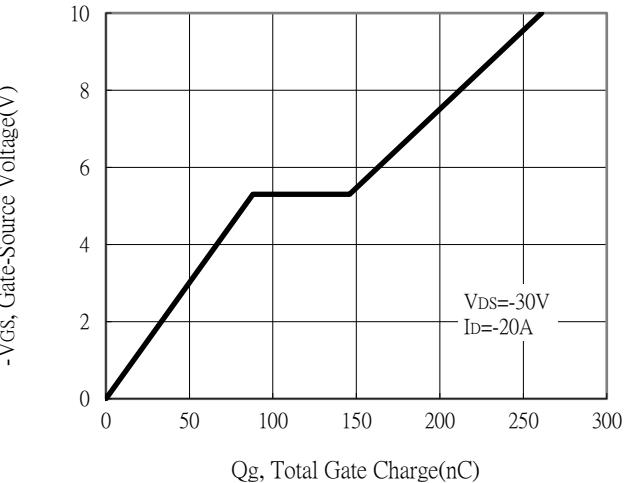
Maximum Safe Operating Area



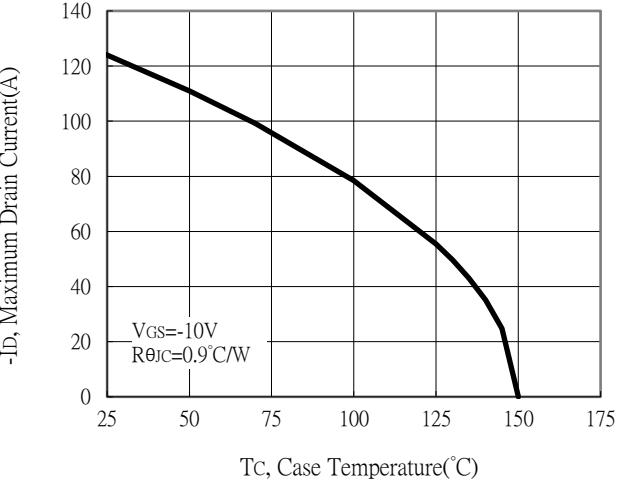
Threshold Voltage vs Junction Temperature



Gate Charge Characteristics

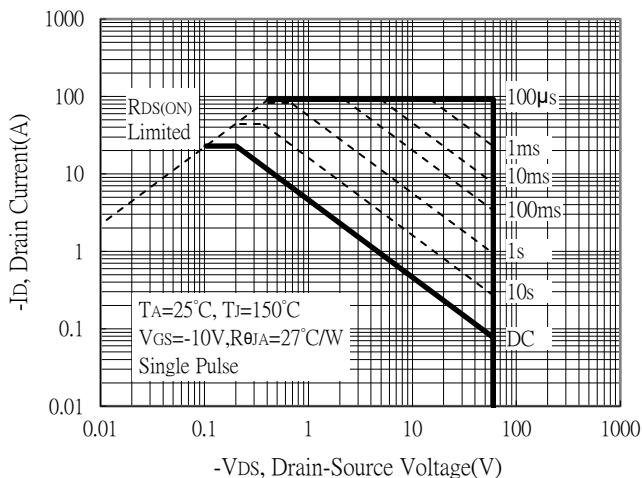


Maximum Drain Current vs Case Temperature

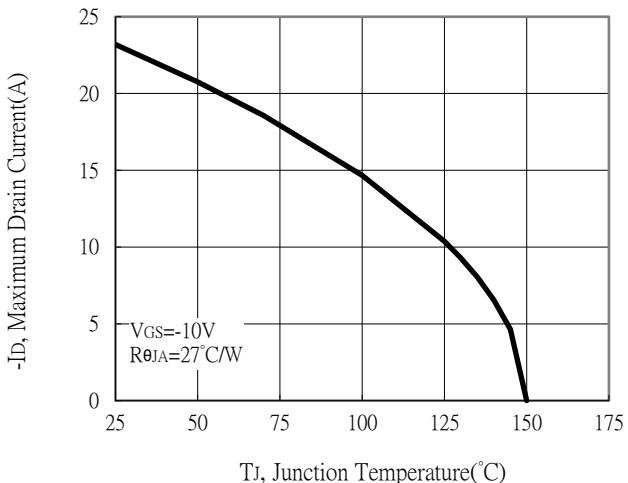


Typical Characteristics (Cont.)

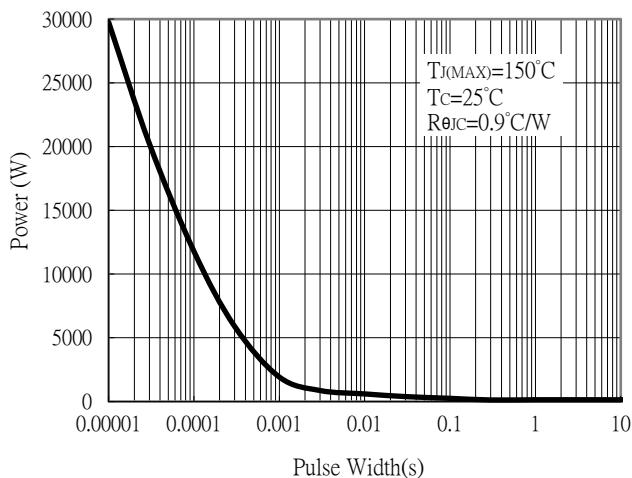
Maximum Safe Operating Area



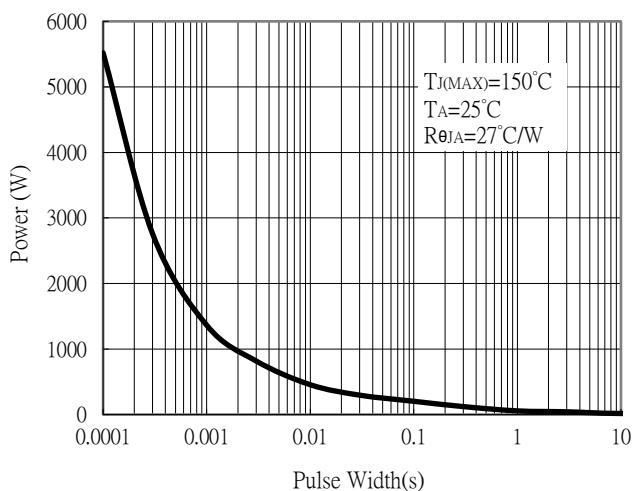
Maximum Drain Current vs Junction Temperature



Single Pulse Power Rating, Junction to Case

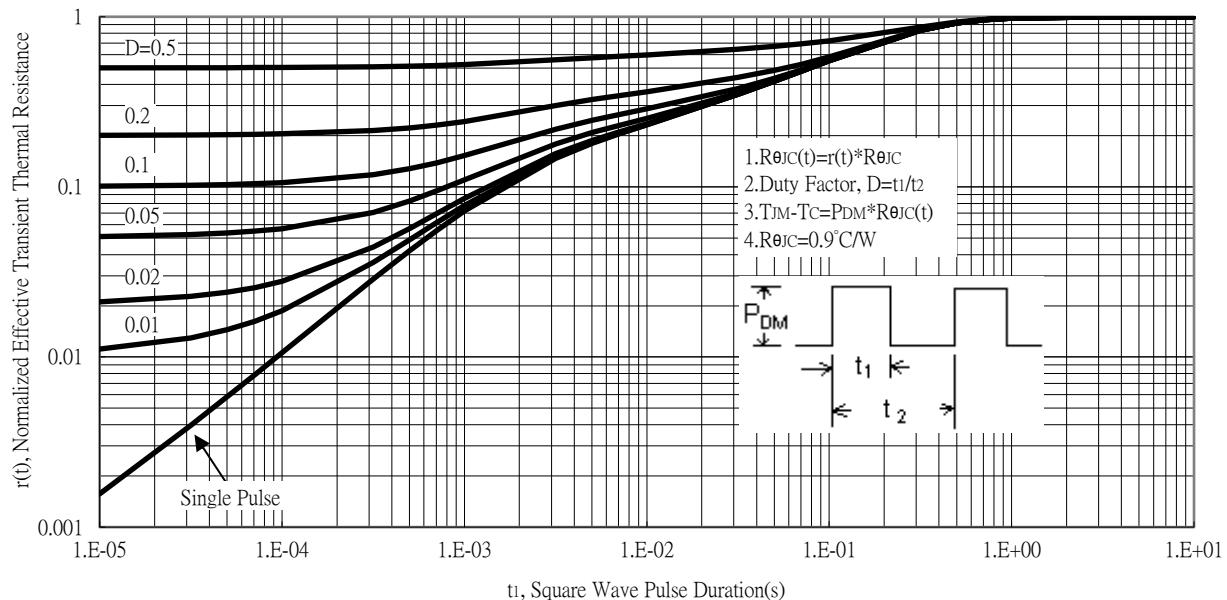


Single Pulse Power Rating, Junction to Ambient

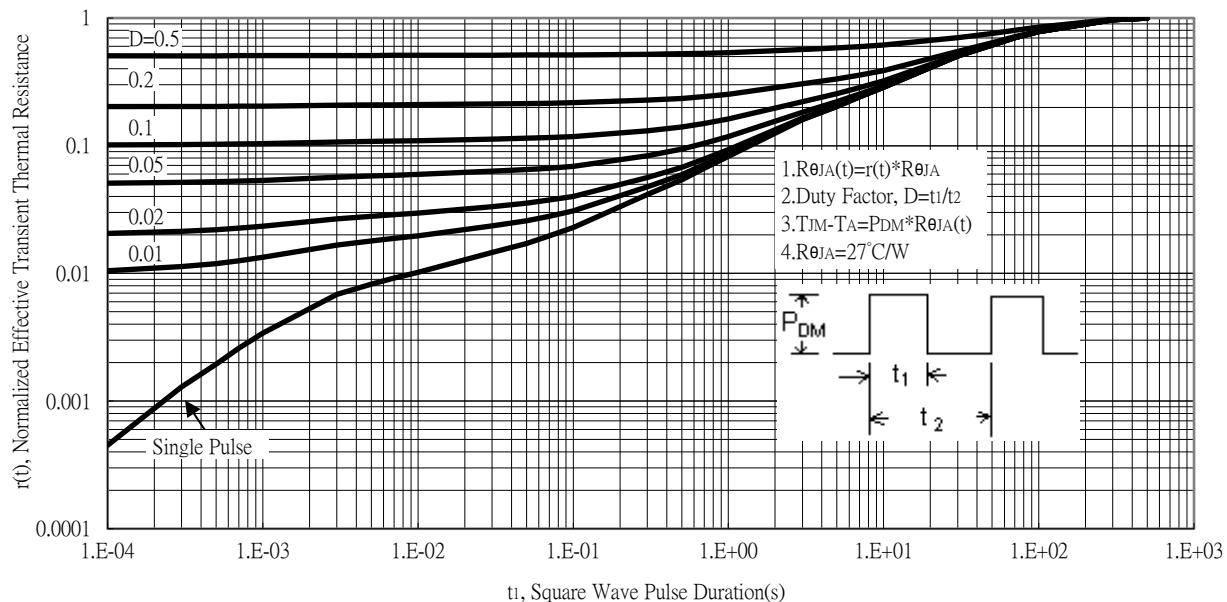


Typical Characteristics (Cont.)

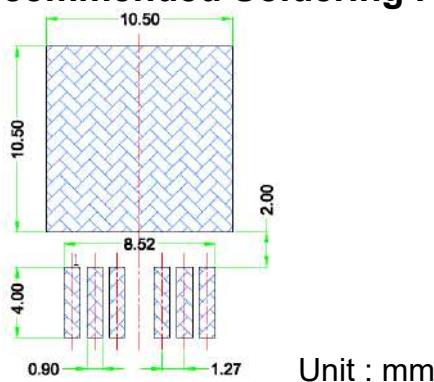
Transient Thermal Response Curves



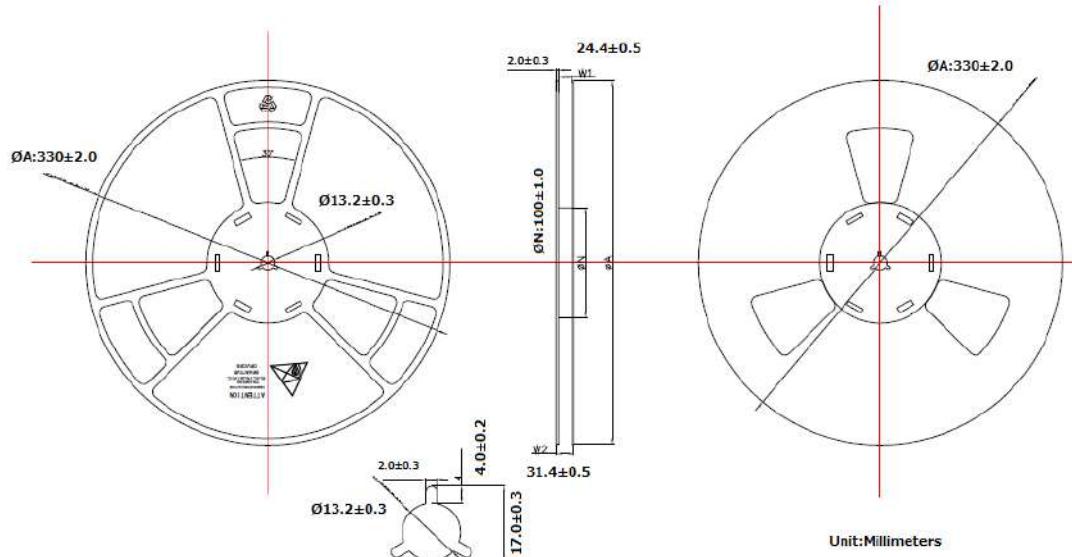
Transient Thermal Response Curves



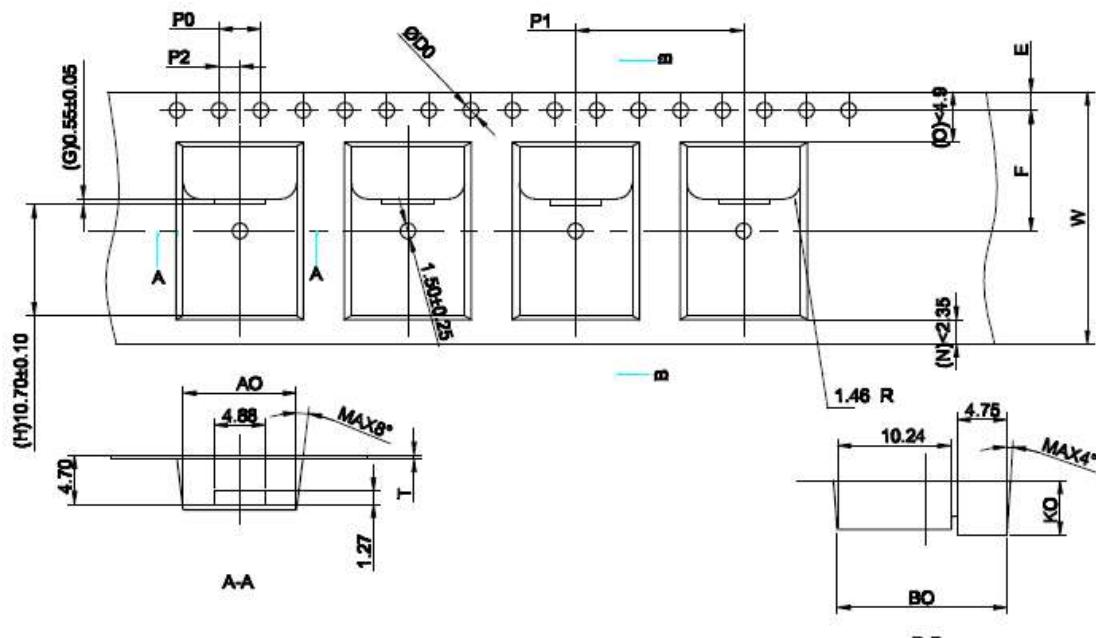
Recommended Soldering Footprint



Reel Dimension

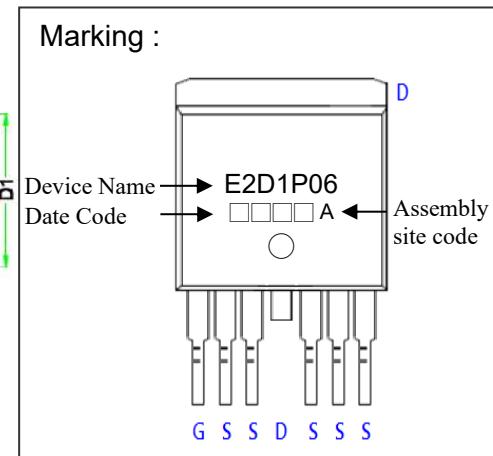
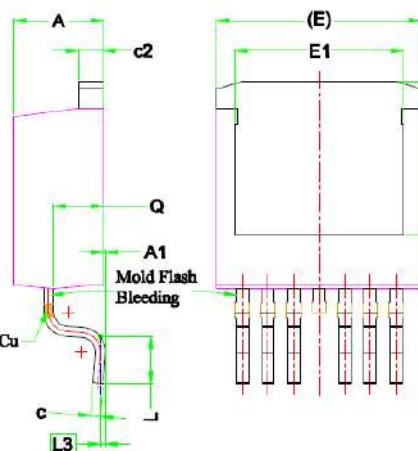
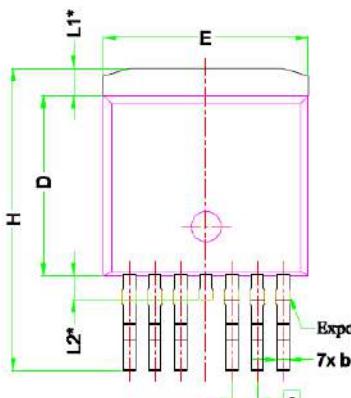


Carrier Tape Dimension



SYMBOL	A_0	B_0	K_0	P_0	P_1	P_2	T	N
SPEC	10.80 ± 0.10	18.13 ± 0.10	5.21 ± 0.10	4.00 ± 0.10	16.00 ± 0.10	2.00 ± 0.10	0.35 ± 0.05	<2.35
SYMBOL	E	F	$\text{Ø}D_0$	W	H	G	O	
SPEC	1.75 ± 0.10	11.50 ± 0.10	1.55 ± 0.05	$24.00\pm 0.30/-0.10$	10.70 ± 0.10	0.55 ± 0.05	<4.9	

TO-263-7L-4C Dimension



Style : Pin 1. Gate
 Pin 2, 3, 5, 6, 7 : Source
 Pin 4. Drain

Date Code(counting from left to right) :
 1st code: year code, the last digit of Christian year
 2nd code : month code, Jan→A, Feb→B, Mar→C,
 Apr→D, May→E, Jun→F, Jul→G, Aug→H,
 Sep→J, Oct→K, Nov→L, Dec→M
 3rd and 4th codes : production serial number, 01~99

7-Lead Plastic Surface Mounted TO-263-7L Package

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.167	0.183	4.24	4.64	E1	0.271	0.310	6.89	7.89
A1	0.000	0.010	0.00	0.25	e	0.050 BSC		1.27	BSC
b	0.020	0.028	0.50	0.70	H	0.575	0.625	14.61	15.88
c	0.016	0.024	0.40	0.60	L	0.070	0.110	1.78	2.79
c2	0.045	0.055	1.15	1.40	L1	0.054 REF.		1.36 REF.	
D	0.347	0.355	8.82	9.02	L2	0.047 REF.		1.20 REF.	
D1	0.270	-	6.86	-	L3	0.010 BSC		0.25 BSC	
E	0.392	0.408	9.96	10.36	Q	0.091	0.106	2.30	2.70