

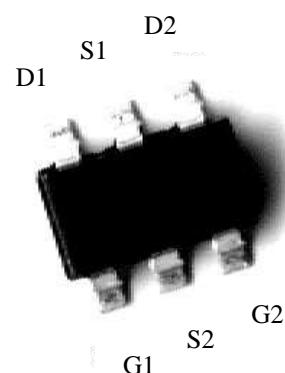
Dual P-Channel Enhancement Mode Power MOSFET

Features:

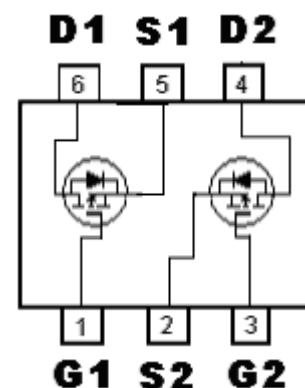
- Simple drive requirement
- Low gate charge
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package

Outline

SOT-26



Equivalent Circuit



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KT2621	SOT-26 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel

Absolute Maximum Ratings ($T_a=25^\circ C$)

Parameter	Symbol	Limits	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current @ $T_A=25^\circ C$ (Note 1)	I_D	-3	A
Continuous Drain Current @ $T_A=70^\circ C$ (Note 1)		-2.4	
Pulsed Drain Current (Note 2)	I_{DM}	-20	
Total Power Dissipation (Note 1)	P_D	1.14	W
Linear Derating Factor		0.01	W / °C
Operating Junction and Storage Temperature	T_j, T_{stg}	-55~+150	°C

Note : 1.Surface mounted on 1 in²copper pad of FR-4 board, $t \leq 5$ sec

2.Pulse width limited by maximum junction temperature

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	80	°C/W
Thermal Resistance, Junction-to-ambient, max	$R_{\theta JA}$	110 (Note)	

Note : Surface mounted on 1 in²copper pad of FR-4 board, $t \leq 5$ sec; 180°C/W when mounted on minimum copper pad

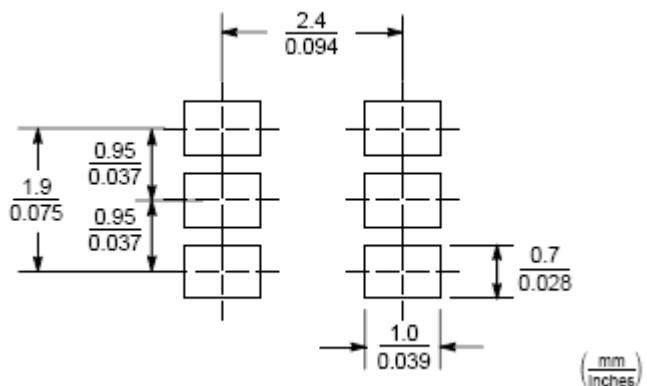
Electrical Characteristics ($T_j=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Static						
BV_{DSS}	-20	-	-	V	$V_{GS}=0V, I_D=-250\mu A$	
$\Delta BV_{DSS}/\Delta T_j$	-	-0.01	-	$V/^\circ C$	Reference to 25°C, $I_D=-250\mu A$	
$V_{GS(th)}$	-0.4	-	-1.2	V	$V_{DS}=V_{GS}, I_D=-250\mu A$	
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 10V, V_{DS}=0V$	
I_{DSS}	-	-	-1	μA	$V_{DS}=-20V, V_{GS}=0V$	
	-	-	-10		$V_{DS}=-16V, V_{GS}=0V, T_j=125^\circ C$	
$*R_{DS(ON)}$	-	78	108	mΩ	$I_D=-2.5A, V_{GS}=-4.5V$	
	-	115	150		$I_D=-2A, V_{GS}=-2.5V$	
	-	125	210		$I_D=-1A, V_{GS}=-1.8V$	
$*G_{FS}$	-	5	-	S	$V_{DS}=-5V, I_D=-2A$	
Dynamic						
C_{iss}	-	429	-	pF	$V_{DS}=-20V, V_{GS}=0V, f=1MHz$	
C_{oss}	-	45	-			
C_{rss}	-	41	-			
$*t_{d(ON)}$	-	4.2	-	ns	$V_{DS}=-10V, I_D=-1A,$ $V_{GS}=-10V, R_G=3.3\Omega, R_D=10\Omega$	
$*t_r$	-	20.8	-			
$*t_{d(OFF)}$	-	50	-			
$*t_f$	-	5.2	-			

*Qg	-	6	-	nC	V _{DS} =-16V, I _D =-2A, V _{GS} =-4.5V
*Qgs	-	0.8	-		
*Qgd	-	2.4	-		
Source-Drain Diode					
*V _{SD}	-	-0.8	-1.2	V	V _{GS} =0V, I _S =-1A
*trr	-	6.3	-	ns	I _F =-2A, V _{GS} =0V, dI _F /dt=100A/μs
*Qrr	-	2.1	-	nC	

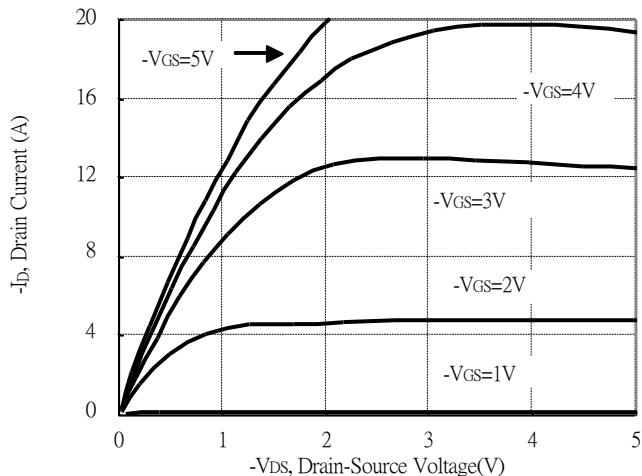
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended Soldering Footprint

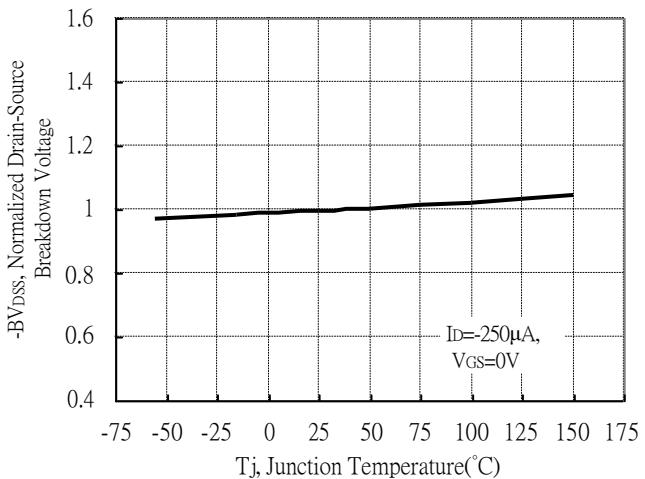


Typical Characteristics

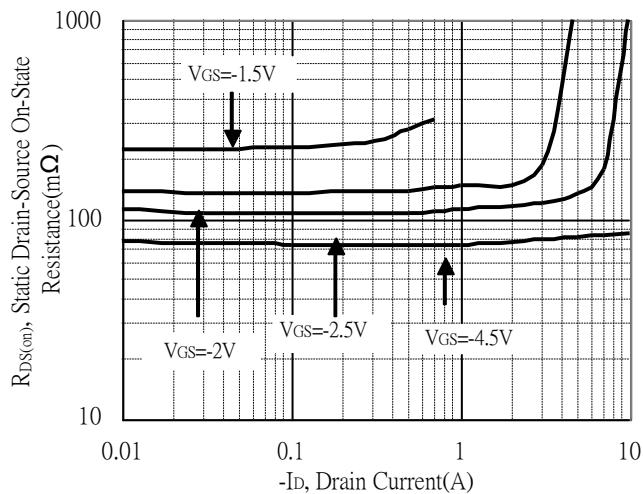
Typical Output Characteristics



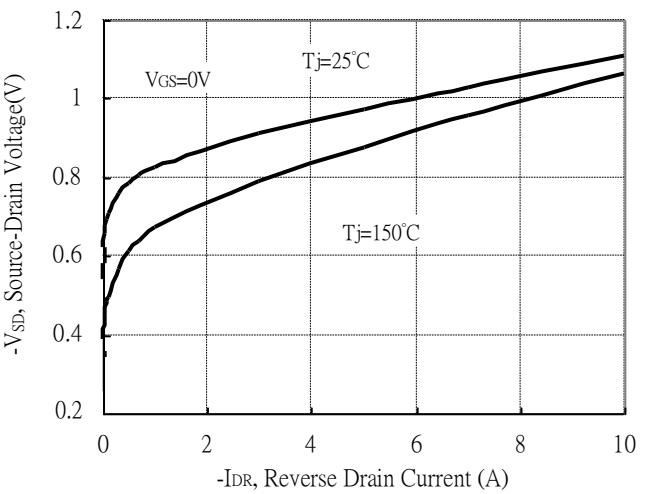
Breakdown Voltage vs Ambient Temperature



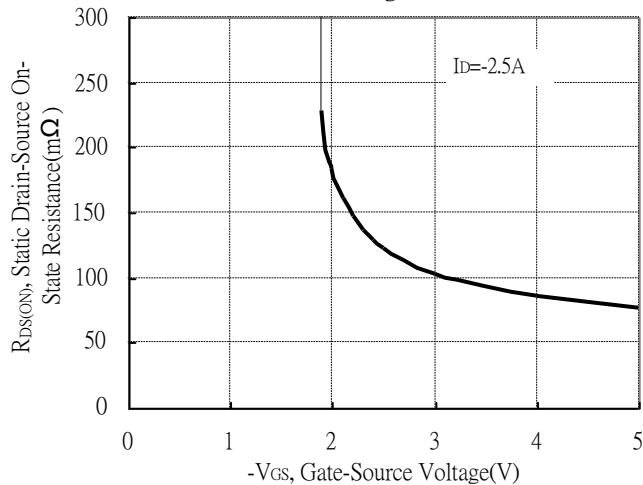
Static Drain-Source On-State resistance vs Drain Current



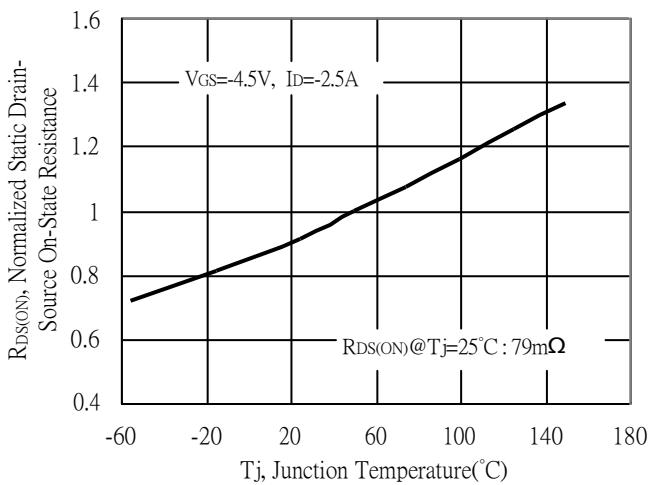
Reverse Drain Current vs Source-Drain Voltage

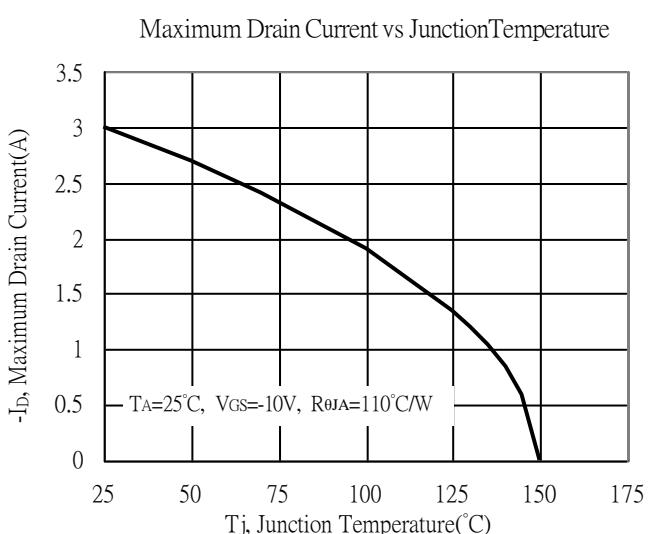
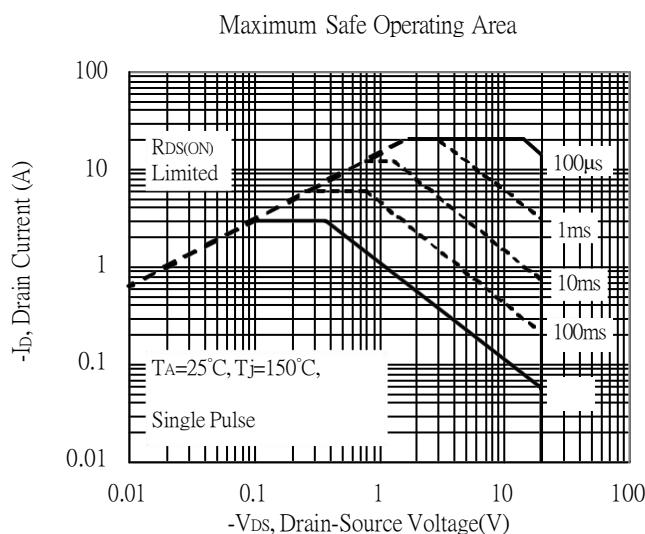
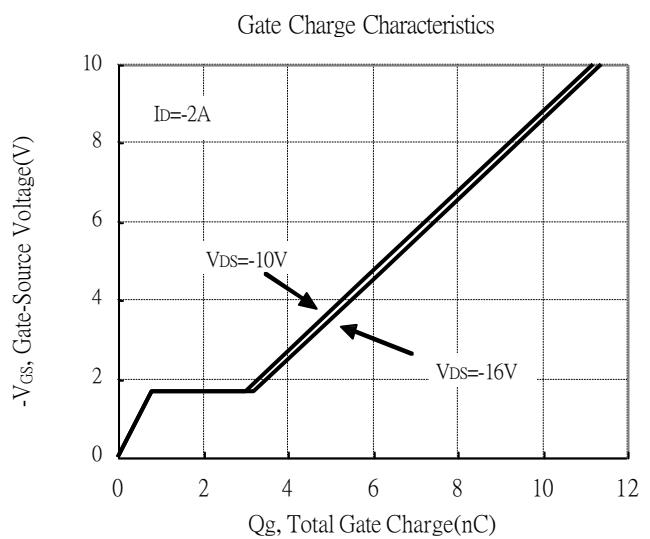
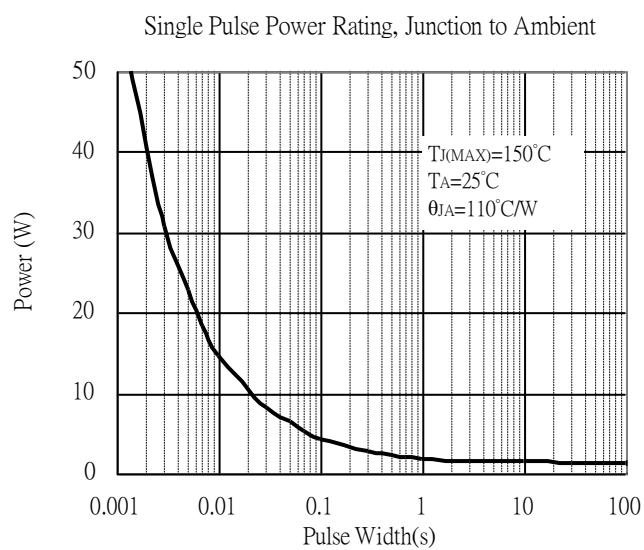
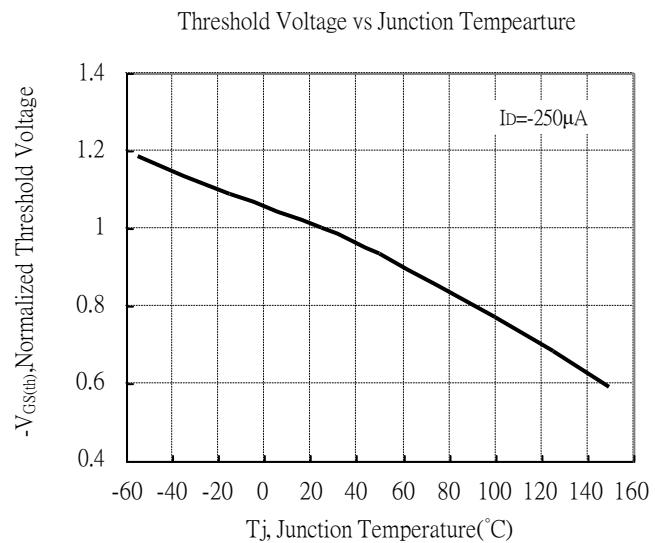
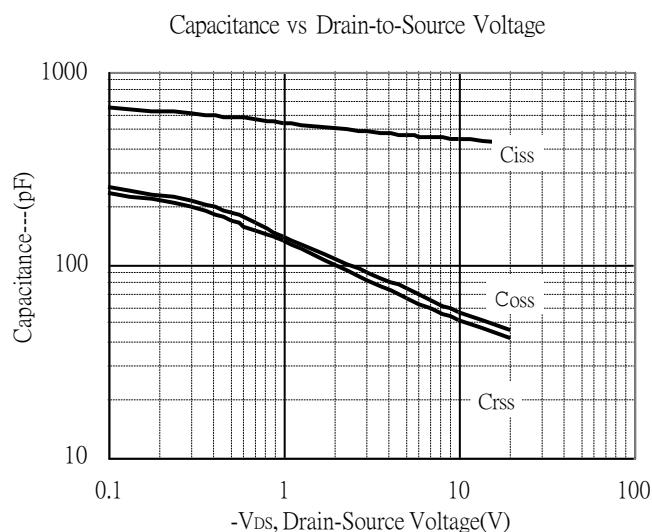


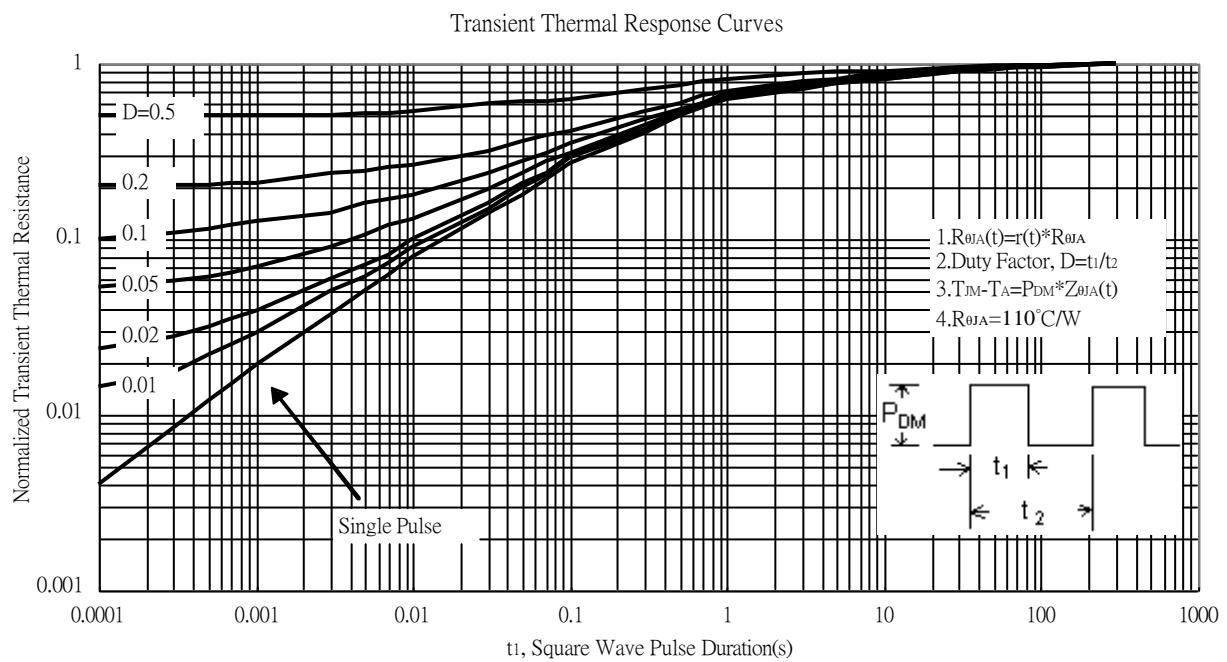
Static Drain-Source On-State Resistance vs Gate-Source Voltage



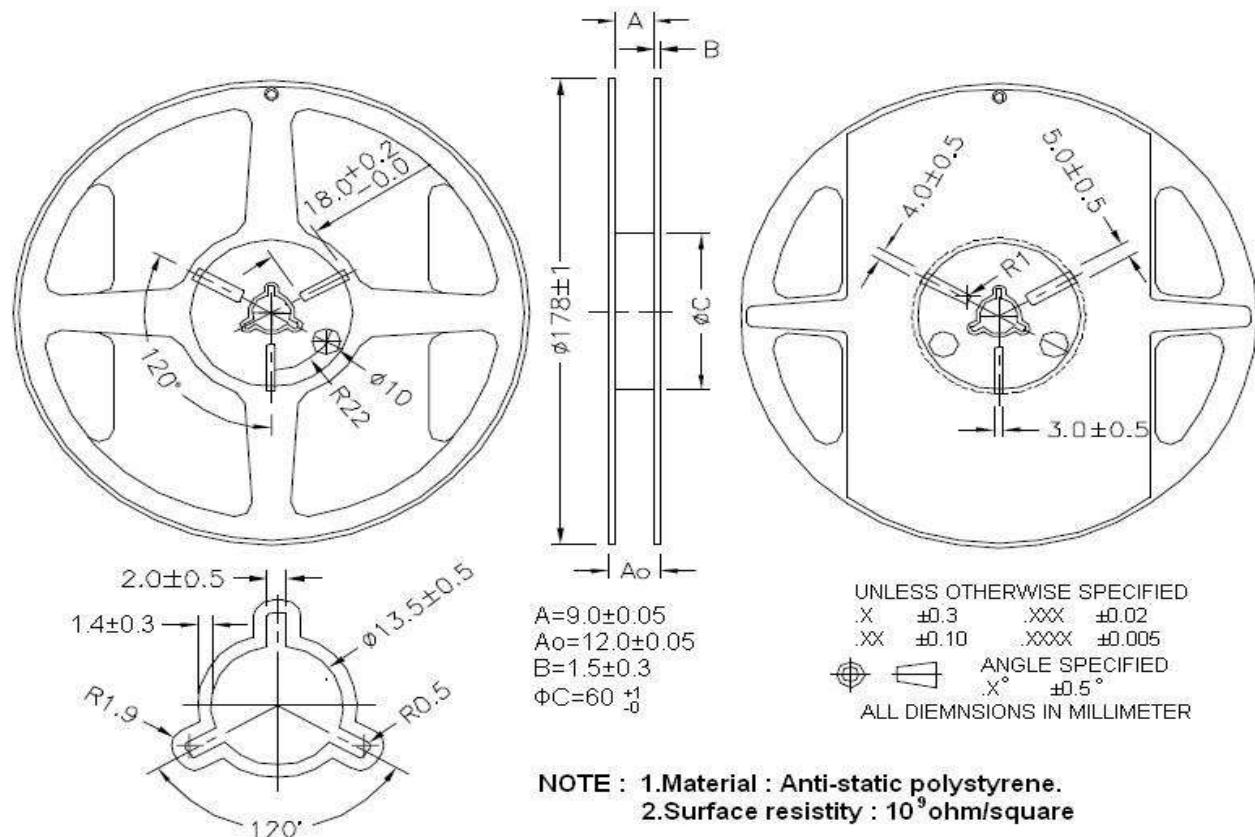
Drain-Source On-State Resistance vs Junction Temperature



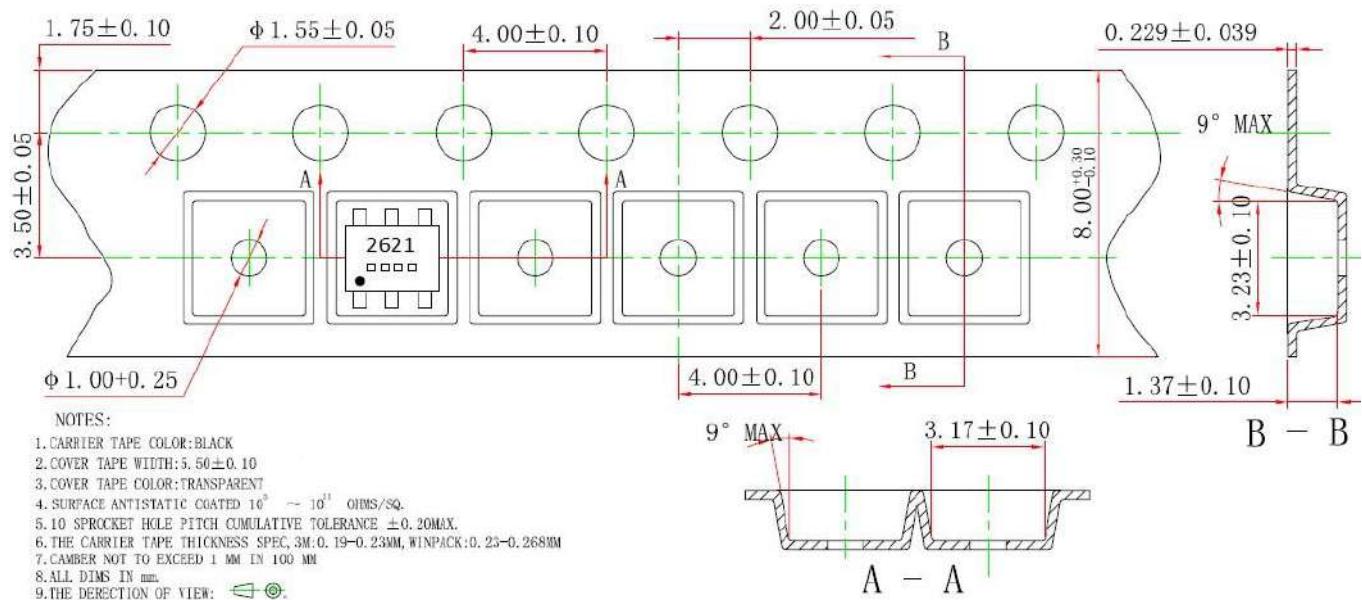


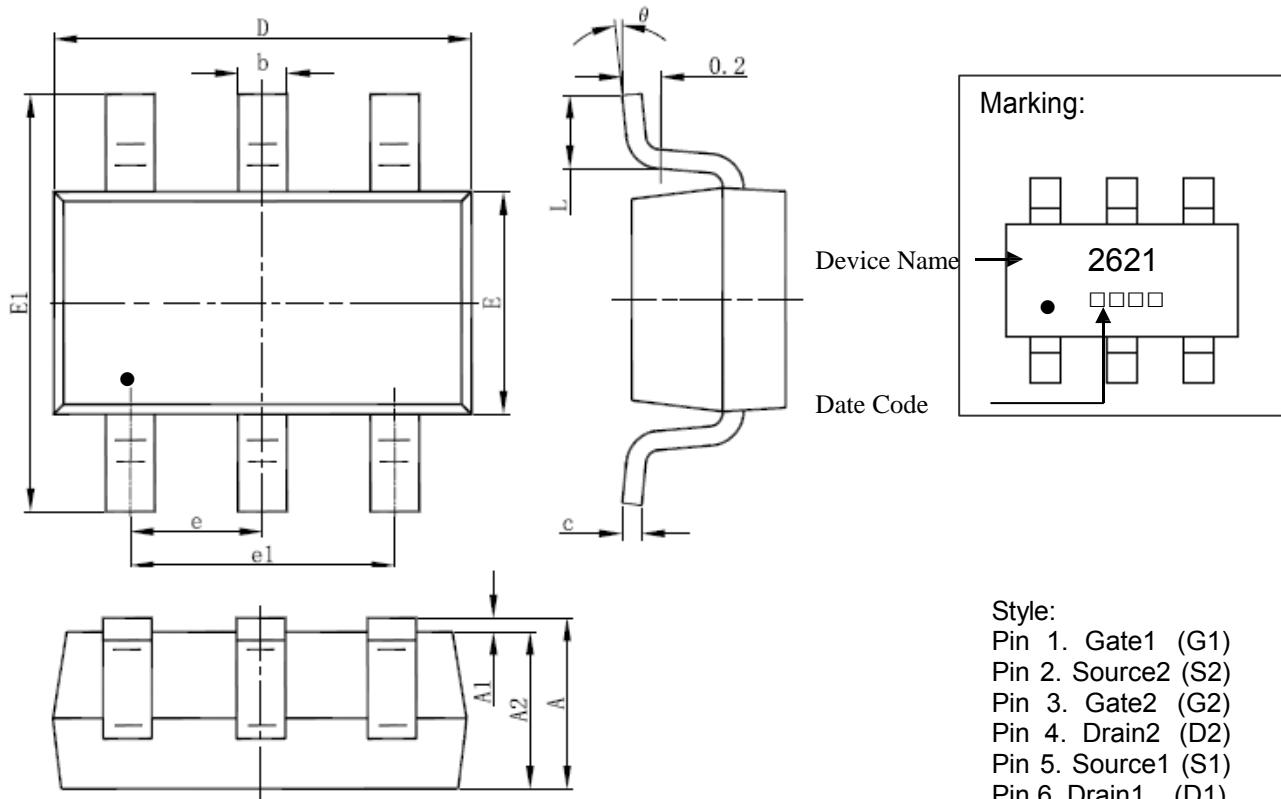


Reel Dimension



Carrier Tape Dimension





DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049	E	1.500	1.700	0.059	0.067
A1	0.000	0.100	0.000	0.004	E1	2.650	2.950	0.104	0.116
A2	1.050	1.150	0.041	0.045	e	0.950 (BSC)		0.037 (BSC)	
b	0.300	0.500	0.012	0.020	e1	1.800	2.000	0.071	0.079
c	0.100	0.200	0.004	0.008	L	0.300	0.600	0.012	0.024
D	2.820	3.020	0.111	0.119	θ	0°	8°	0°	8°