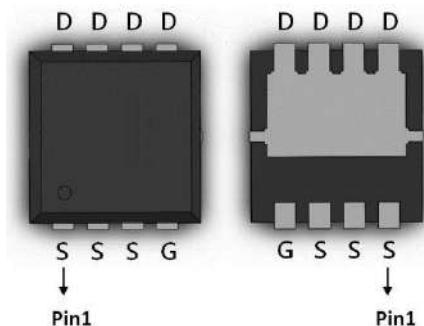


## P-Channel Enhancement Mode Power MOSFET

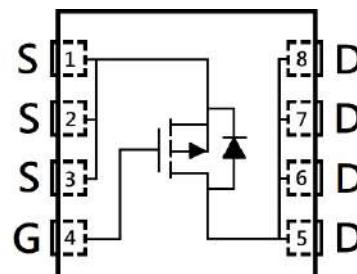
### Features:

- Single Drive Requirement
- Low On Resistance
- Fast Switching Characteristic

DFN3×3



**KSPREJ0P20**



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KSPREJ0P20	DFN3×3 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	-200	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current @ $V_{GS}=-10\text{V}$ , $T_C=25^\circ\text{C}$	$I_D$	-3.9	A
Continuous Drain Current @ $V_{GS}=-10\text{V}$ , $T_C=100^\circ\text{C}$		-2.5	
Continuous Drain Current @ $V_{GS}=-10\text{V}$ , $T_A=25^\circ\text{C}$		-1	
Continuous Drain Current @ $V_{GS}=-10\text{V}$ , $T_A=70^\circ\text{C}$		-0.8	
Pulsed Drain Current	$I_{DM}$	-9.7	mJ
Continuous Body Diode Forward Current @ $T_C=25^\circ\text{C}$	$I_S$	-3.9	
Avalanche Current @ $L=0.1\text{mH}$	$I_{AS}$	-9	
Avalanche Energy @ $L=0.5\text{mH}$	$E_{AS}$	15	
Total Power Dissipation	$T_C=25^\circ\text{C}$	*a 33	W
	$T_C=100^\circ\text{C}$	*a 13	
	$T_A=25^\circ\text{C}$	*b 2.3	
	$T_A=70^\circ\text{C}$	*b 1.5	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	°C

### Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	$R_{\theta JC}$	3.8	°C/W
Thermal Resistance, Junction-to-ambient	$R_{\theta JA}$	53	

Note:

- \*a. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=150^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- \*b. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR -4 board with 2 oz. copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The power dissipation  $P_D$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*c. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^\circ\text{C}$ .

**Electrical Characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
<b>Static</b>						
BV <sub>DSS</sub>	-200	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-2	-	-4			
G <sub>FS</sub>	-	3	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1A	
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-160V, V <sub>GS</sub> =0V	
R <sub>DSS(ON)</sub>	-	0.75	1	Ω	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1A	
<b>Dynamic</b>						
C <sub>iss</sub>	-	680	-	pF	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V, f=1MHz	
C <sub>oss</sub>	-	30	-			
C <sub>rss</sub>	-	25	-			
R <sub>g</sub>	-	11	-	Ω	f=1MHz	
Q <sub>g</sub> *1, 2	-	15	-	nC	V <sub>DS</sub> =-100V, I <sub>D</sub> =-1A, V <sub>GS</sub> =-10V	
Q <sub>gs</sub> *1, 2	-	3.4	-			
Q <sub>gd</sub> *1, 2	-	3.8	-			
t <sub>d(ON)</sub> *1, 2	-	11	-	ns	V <sub>DS</sub> =-100V, I <sub>D</sub> =-1A, V <sub>GS</sub> =-10V, R <sub>GS</sub> =1Ω	
t <sub>r</sub> *1, 2	-	18	-			
t <sub>d(OFF)</sub> *1, 2	-	37	-			
t <sub>f</sub> *1, 2	-	80	-			
<b>Source-Drain Diode</b>						
V <sub>SD</sub> *1	-	-0.79	-1.2	V	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	
tr	-	43	-	ns	I <sub>F</sub> =-1A, dI <sub>F</sub> /dt=100A/μs	
Q <sub>rr</sub>	-	73	-	nC		

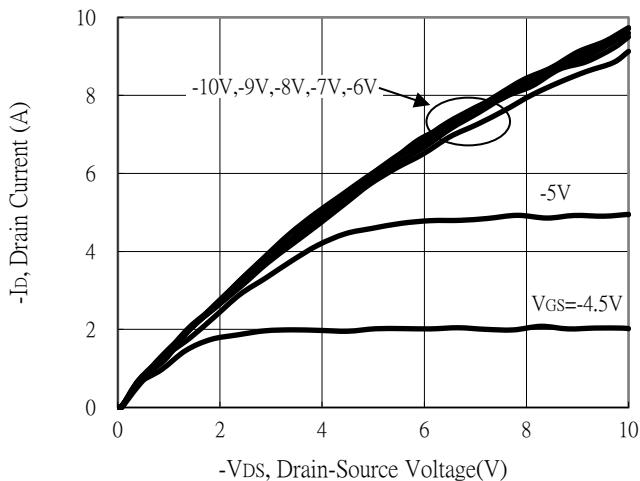
Note:

\*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

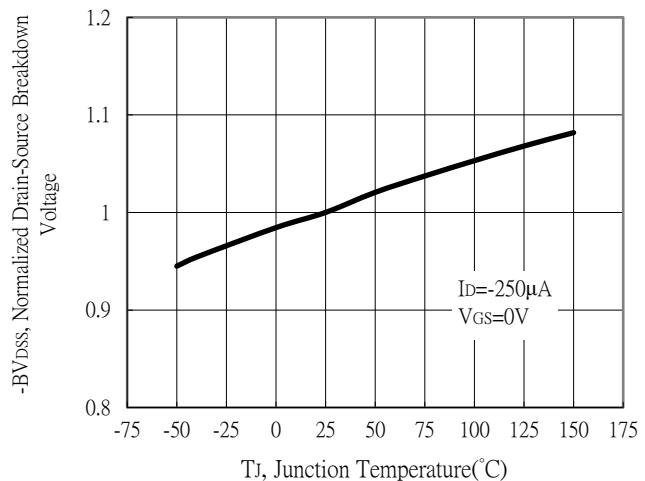
\*2. Independent of operating temperature

## Typical Characteristics

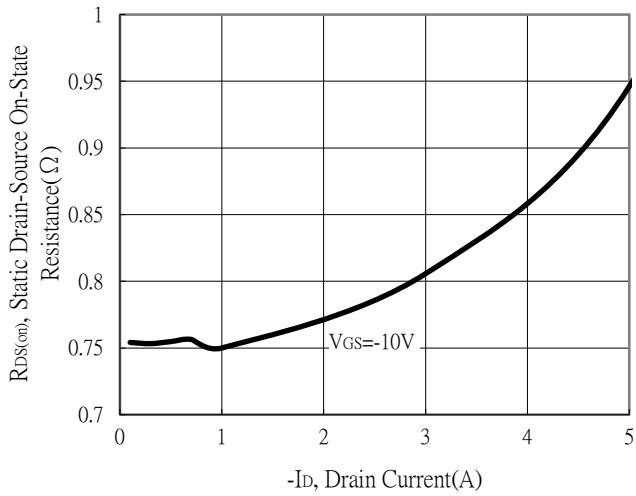
Typical Output Characteristics



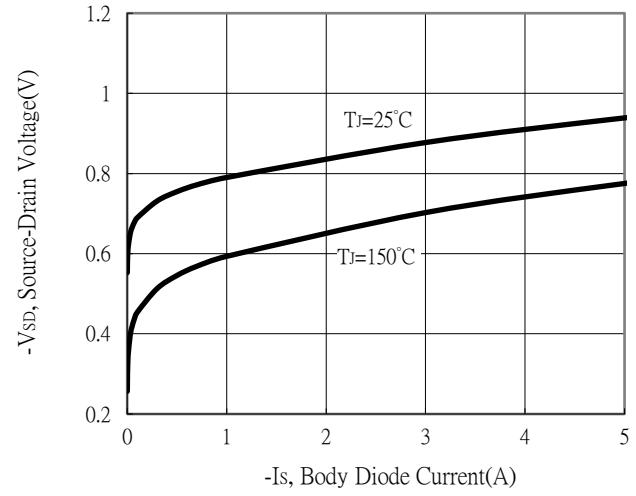
Breakdown Voltage vs Ambient Temperature



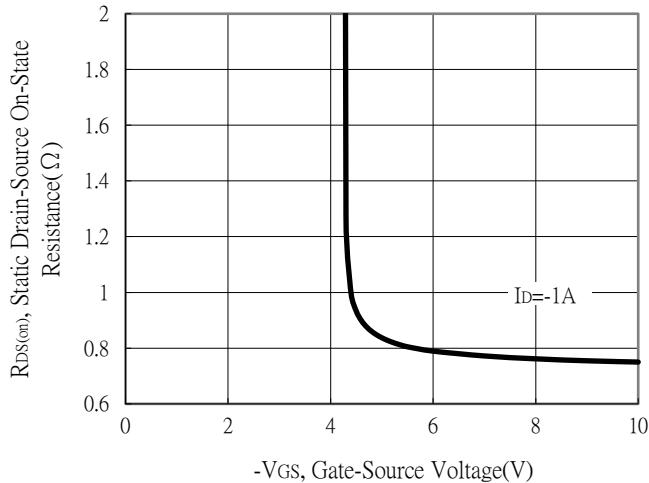
Static Drain-Source On-State resistance vs Drain Current



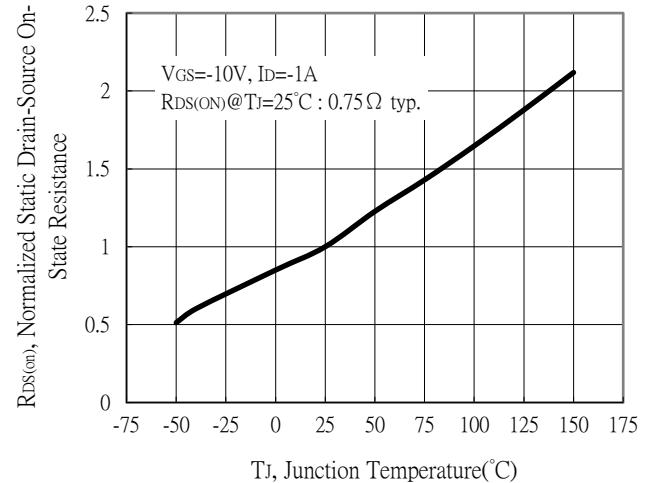
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

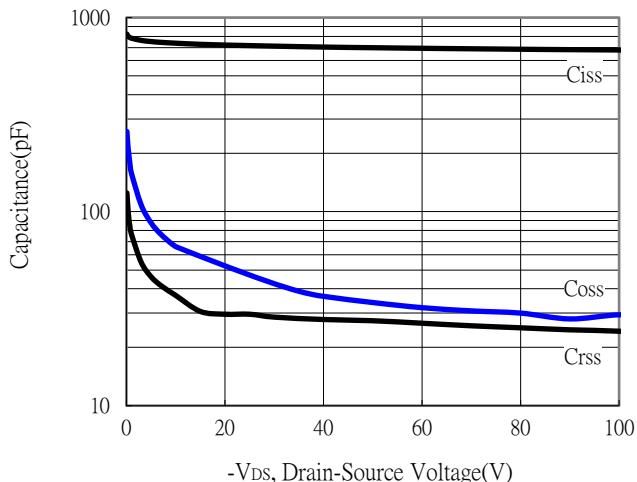


Drain-Source On-State Resistance vs Junction Temperature

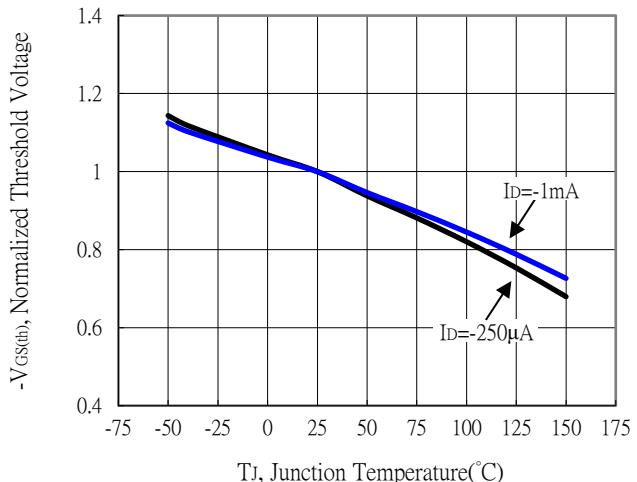


## Typical Characteristics (Cont.)

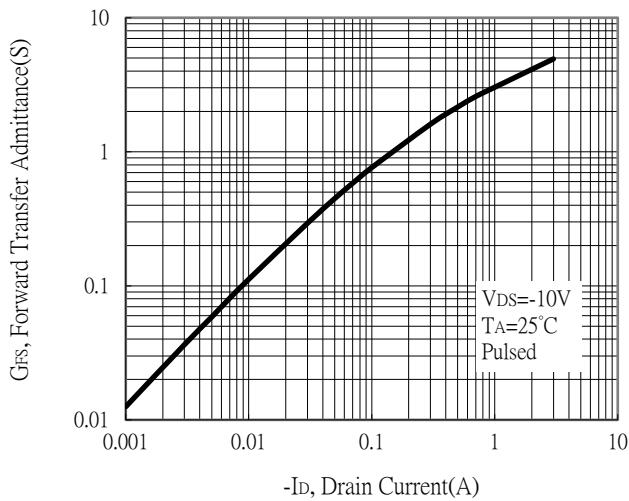
Capacitance vs Drain-to-Source Voltage



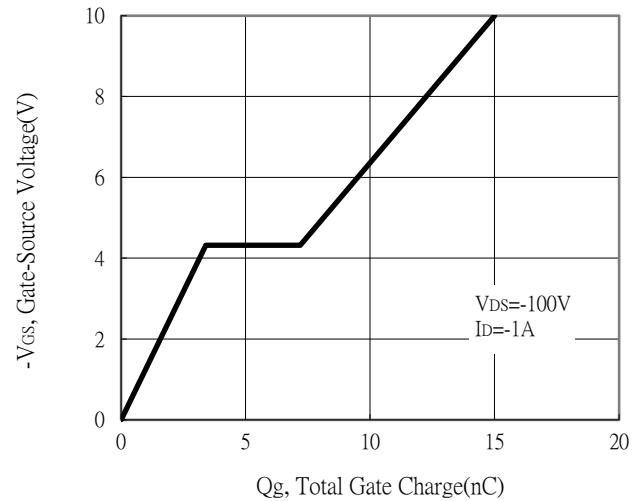
Threshold Voltage vs Junction Temperature



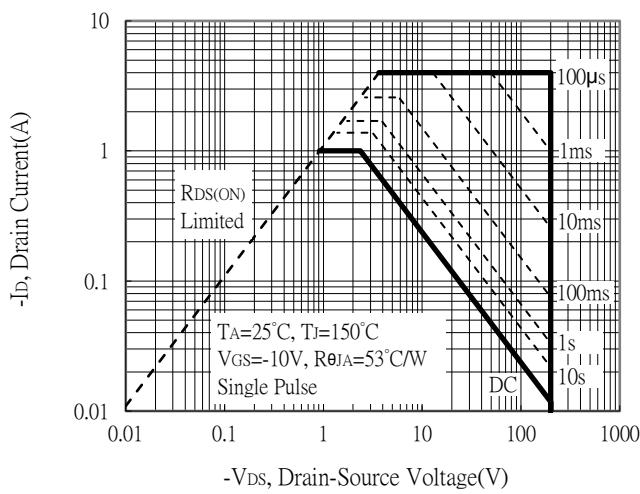
Forward Transfer Admittance vs Drain Current



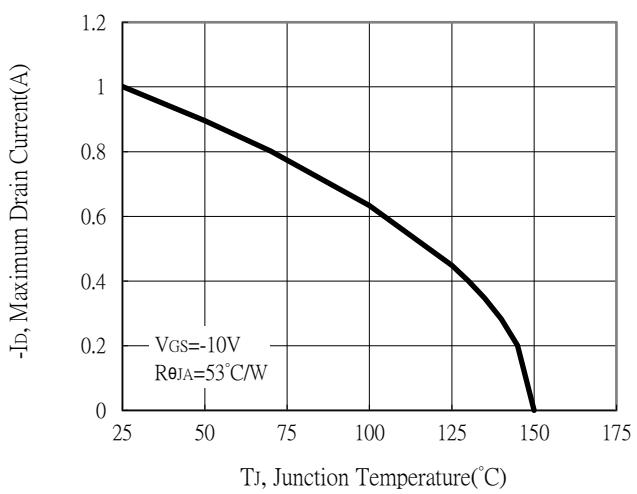
Gate Charge Characteristics



Maximum Safe Operating Area

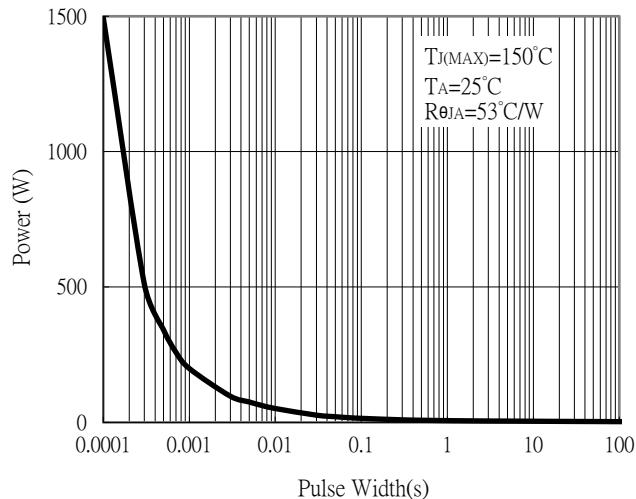


Maximum Drain Current vs Junction Temperature

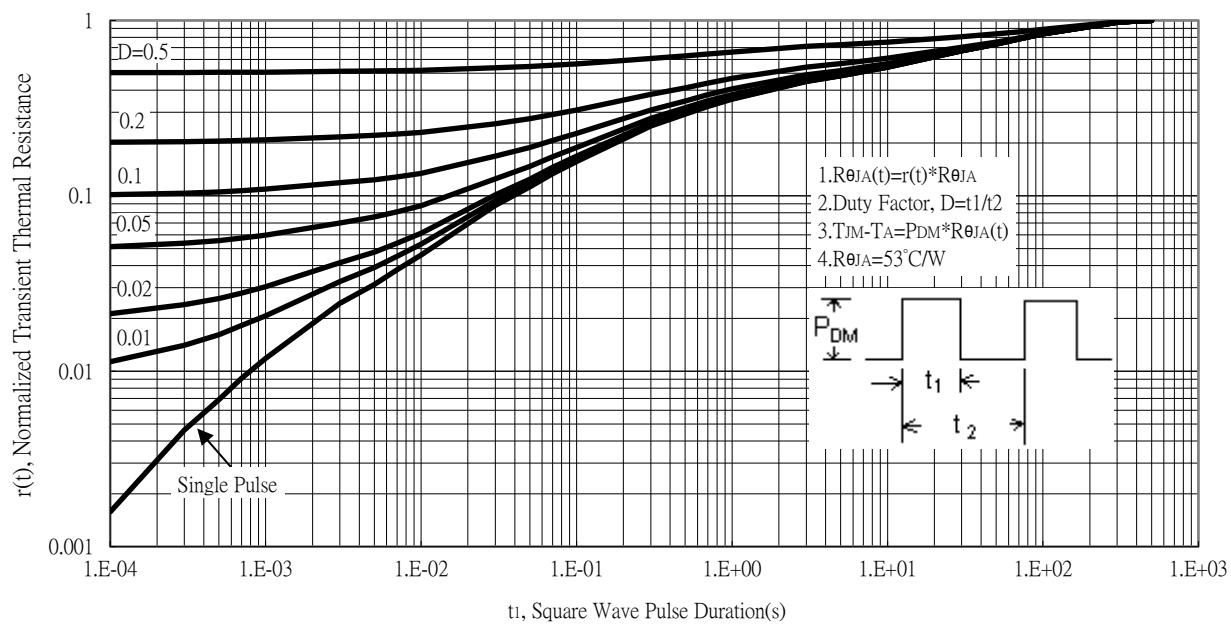


## Typical Characteristics (Cont.)

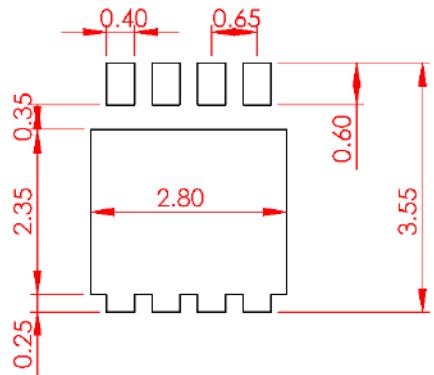
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves

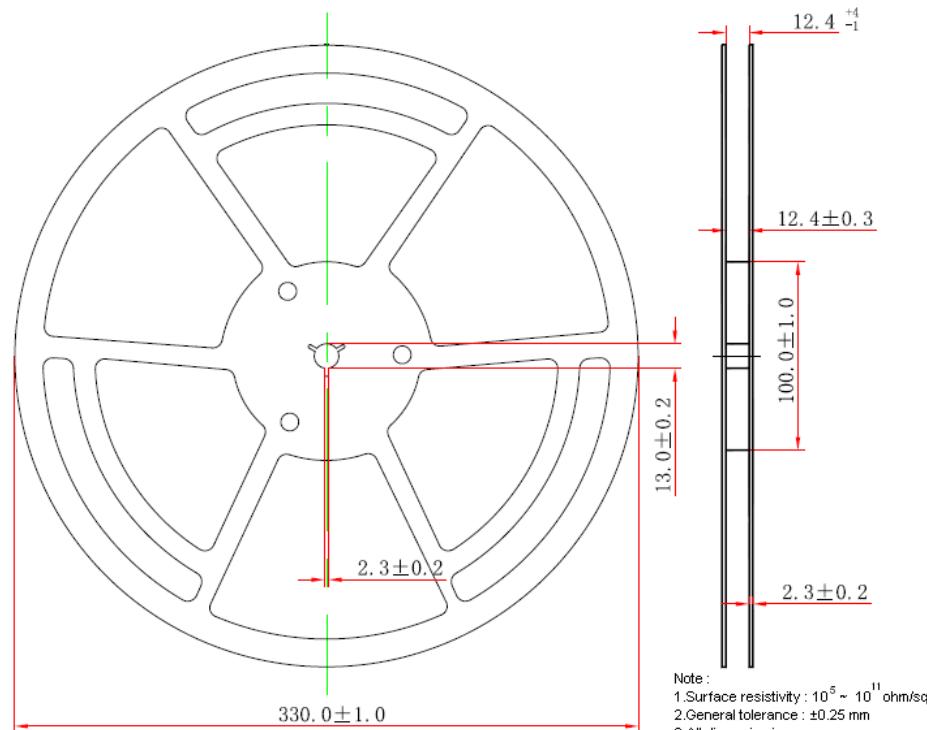


## Recommended Soldering Footprint

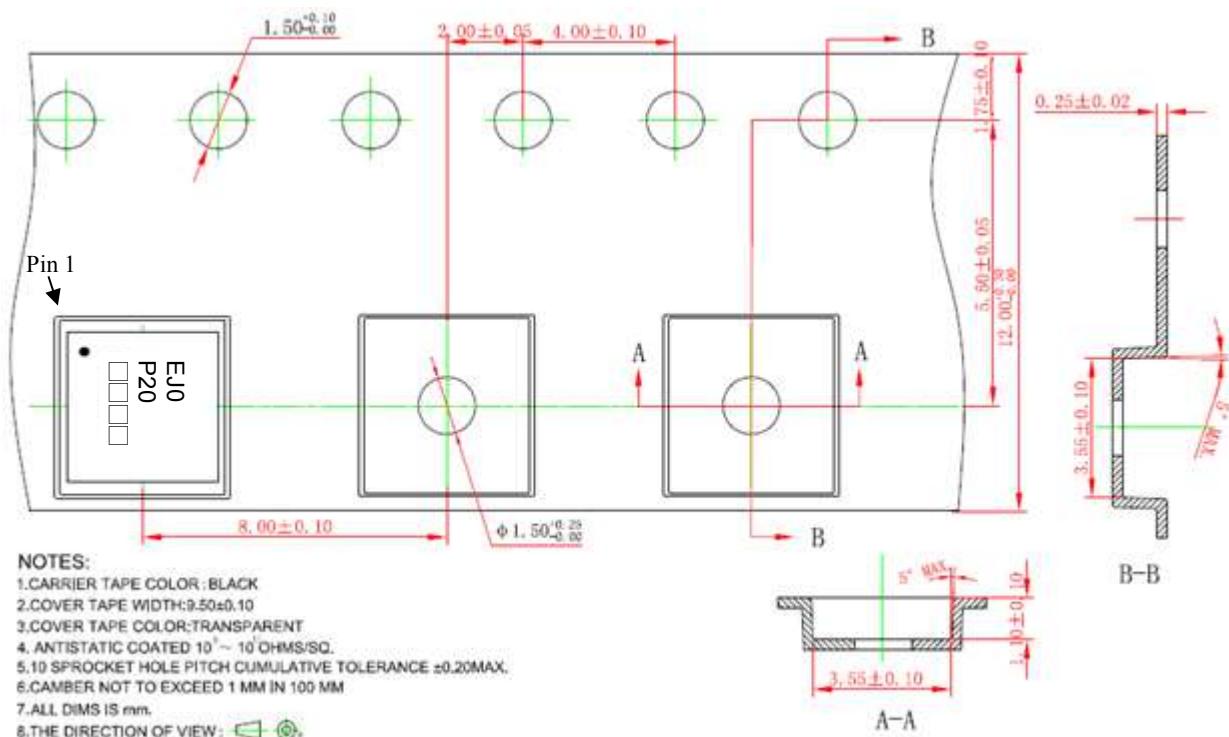


Unit : mm

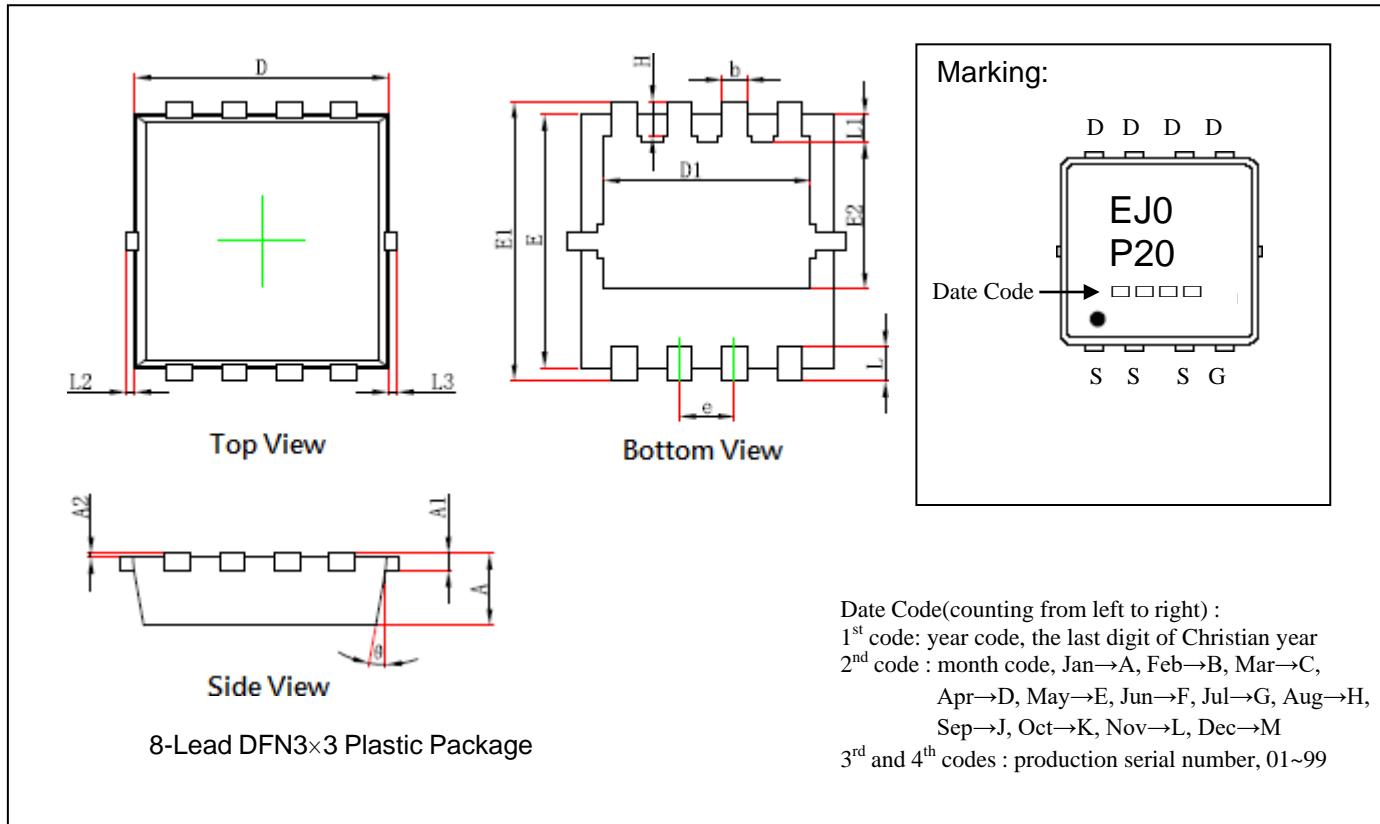
## Reel Dimension



## Carrier Tape Dimension



### DFN3x3 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.026	0.033	0.650	0.850	b	0.008	0.016	0.200	0.400
A1	0.006	REF	0.152	REF	e	0.022	0.030	0.550	0.750
A2	0.000	0.002	0.000	0.050	L	0.012	0.020	0.300	0.500
D	0.114	0.122	2.900	3.100	L1	0.007	0.019	0.180	0.480
D1	0.091	0.102	2.300	2.600	L2	0.000	0.004	0.000	0.100
E	0.114	0.122	2.900	3.100	L3	0.000	0.004	0.000	0.100
E1	0.124	0.136	3.150	3.450	H	0.012	0.020	0.315	0.515
E2	0.060	0.076	1.535	1.935	θ	9°	13°	9°	13°