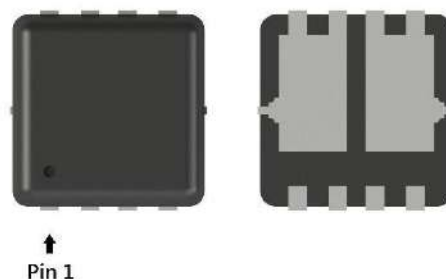


Dual N-Channel Enhancement Mode Power MOSFET

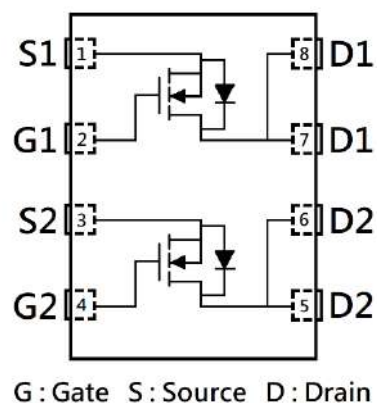
Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

DFN3×3



BV_{DSS}	30V
$I_D@V_{GS}=10V, T_C=25^{\circ}C$	32A
$I_D@V_{GS}=10V, T_A=25^{\circ}C$	10A
$R_{DS(ON)}@V_{GS}=10V, I_D=10A$	7.5mΩ
$R_{DS(ON)}@V_{GS}=4.5V, I_D=10A$	11mΩ



Ordering Information

Device	Package	Shipping
KSPRB5D8A03R	DFN3×3 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

Absolute Maximum Ratings (T_A=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @ V _{GS} =10V, T _C =25°C	I _D	32	A
Continuous Drain Current @ V _{GS} =10V, T _C =100°C		20	
Continuous Drain Current @ V _{GS} =10V, T _A =25°C		10	
Continuous Drain Current @ V _{GS} =10V, T _A =70°C		8	
Pulsed Drain Current	I _{DM}	128	
Continuous Body Diode Forward Current @ T _C =25°C	I _S	13	
Pulsed Body Diode Forward Current @ T _C =25°C	I _{SM}	128	
Avalanche Current @ L=0.1mH	I _{AS}	15	
Avalanche Energy @ L=0.5mH	E _{AS}	16	mJ
Total Power Dissipation	P _D	16	W
		6.4	
		1.4	
		0.9	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	R _{θJC}	8	°C/W
Thermal Resistance, Junction-to-ambient	R _{θJA}	87	

Note:

- *a. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- *b. The value of R_{θJA} is measured with the device mounted on 1 in² FR -4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.

Electrical Characteristics (T_A=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	30	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1	-	2.5		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	15	-	S	V _{DS} =5V, I _D =7A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =24V, V _{GS} =0V
R _{DS(ON)}	-	7.5	10	mΩ	V _{GS} =10V, I _D =10A
	-	11	15		V _{GS} =4.5V, I _D =10A
Dynamic					
C _{iss}	-	780	-	pF	V _{DS} =15V, V _{GS} =0V, f=1MHz
C _{oss}	-	520	-		
C _{rss}	-	60	-		
R _g	-	0.8	-	Ω	f=1MHz
Q _g *1, 2	-	7.8	-	nC	V _{DS} =15V, I _D =10A, V _{GS} =4.5V
Q _g *1, 2	-	14	-		V _{DS} =15V, I _D =10A, V _{GS} =10V
Q _{gs} *1, 2	-	3.3	-		
Q _{gd} *1, 2	-	3	-		
t _{d(ON)} *1, 2	-	9	-	ns	V _{DS} =15V, I _D =10A, V _{GS} =10V, R _{GS} =3Ω
t _r *1, 2	-	12	-		
t _{d(OFF)} *1, 2	-	23	-		
t _f *1, 2	-	6	-		
Source-Drain Diode					
V _{SD} *1	-	0.9	1.2	V	I _S =10A, V _{GS} =0V
t _{rr}	-	18	-	ns	I _F =10A, dI _F /dt=100A/μs
Q _{rr}	-	6	-	nC	

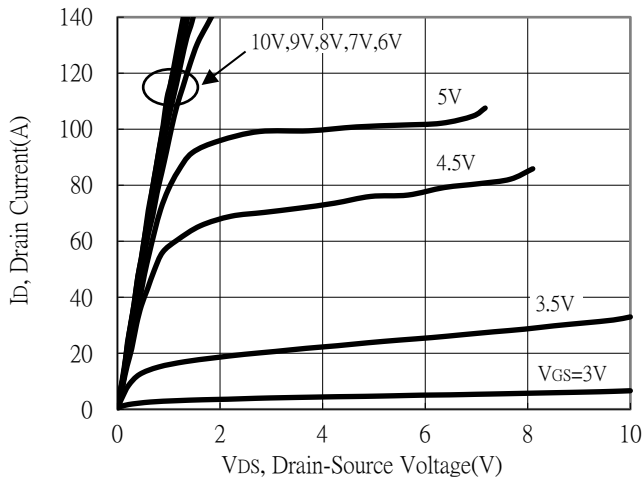
Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

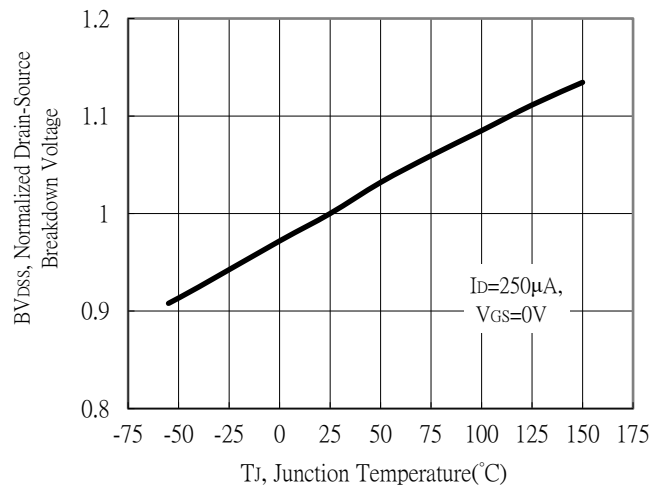
*2. Independent of operating temperature

Typical Characteristics

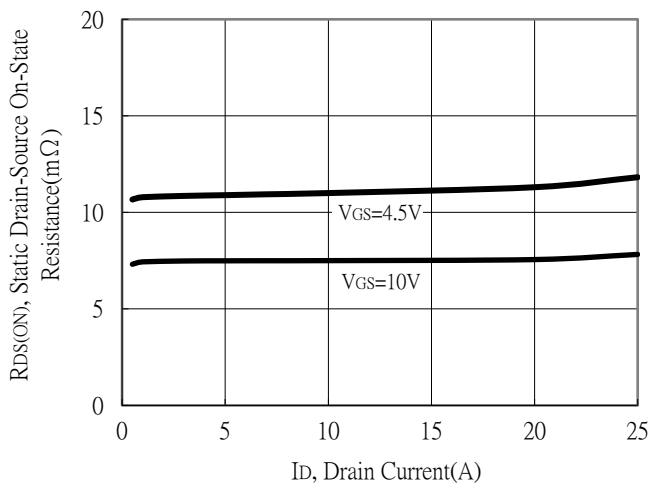
Typical Output Characteristics



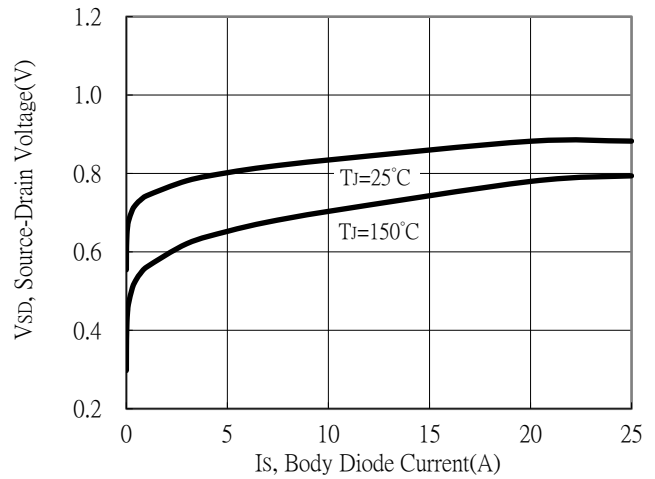
Breakdown Voltage vs Ambient Temperature



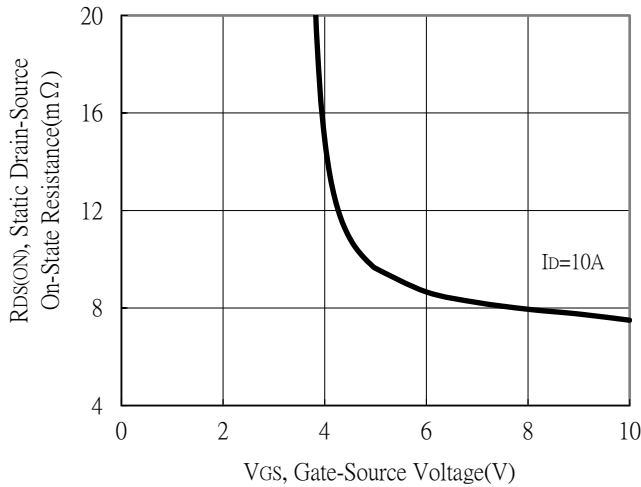
Static Drain-Source On-State resistance vs Drain Current



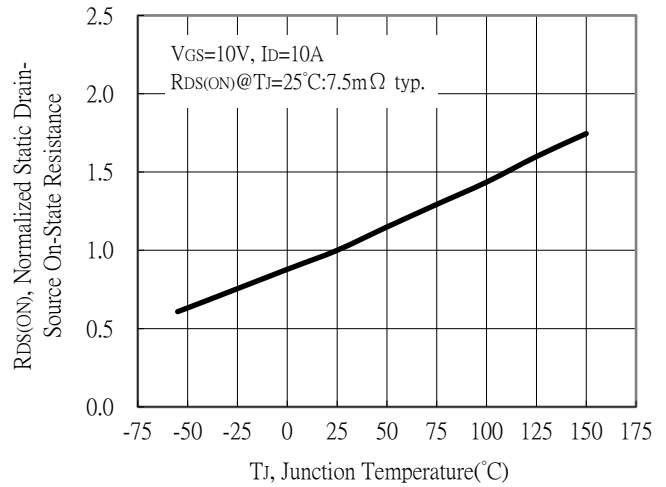
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

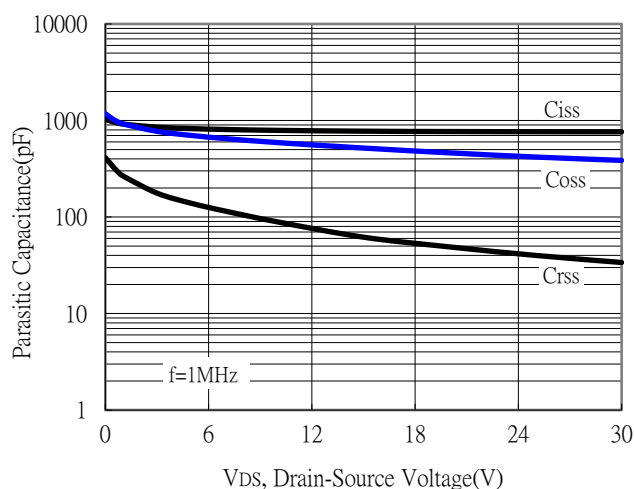


Drain-Source On-State Resistance vs Junction Temperature

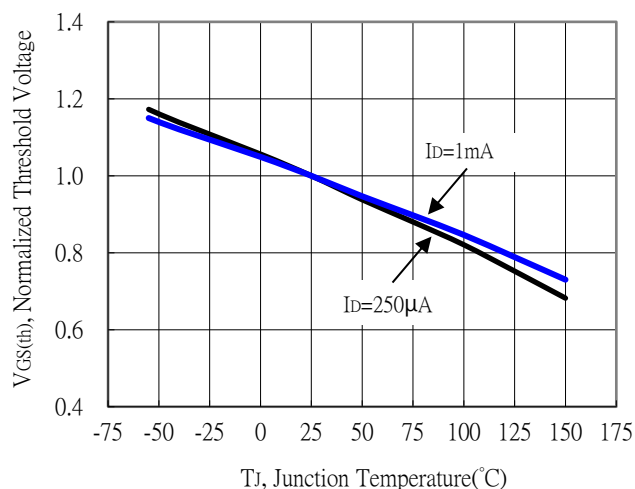


Typical Characteristics (Cont.)

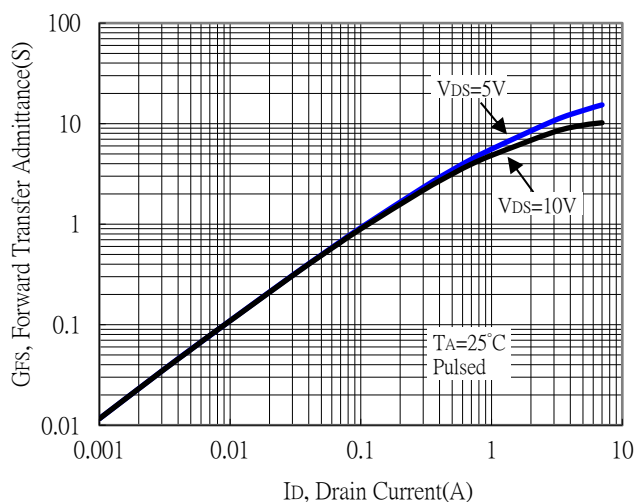
Capacitance vs Drain-to-Source Voltage



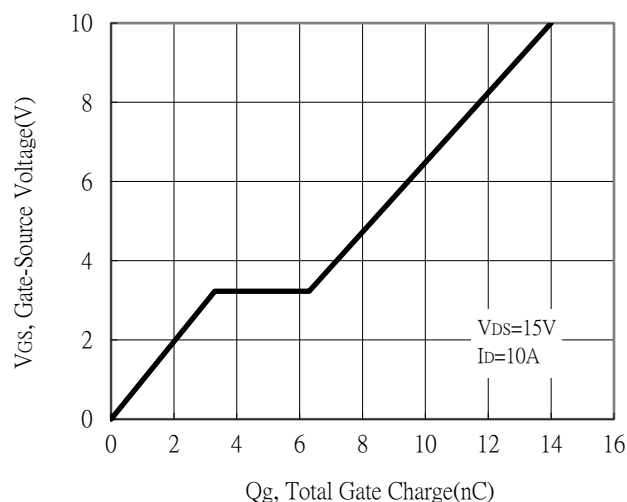
Threshold Voltage vs Junction Temperature



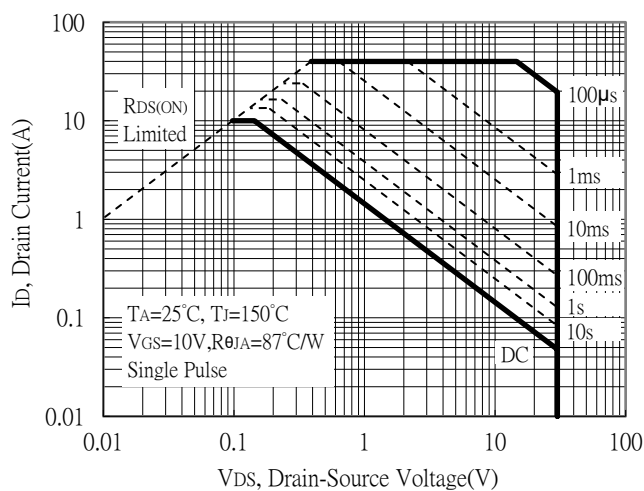
Forward Transfer Admittance vs Drain Current



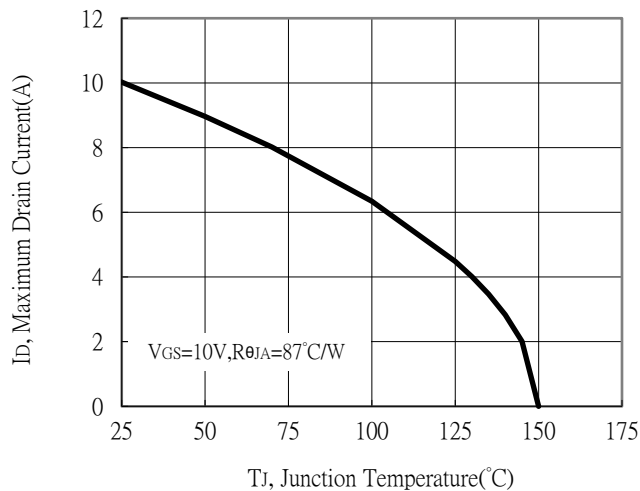
Gate Charge Characteristics



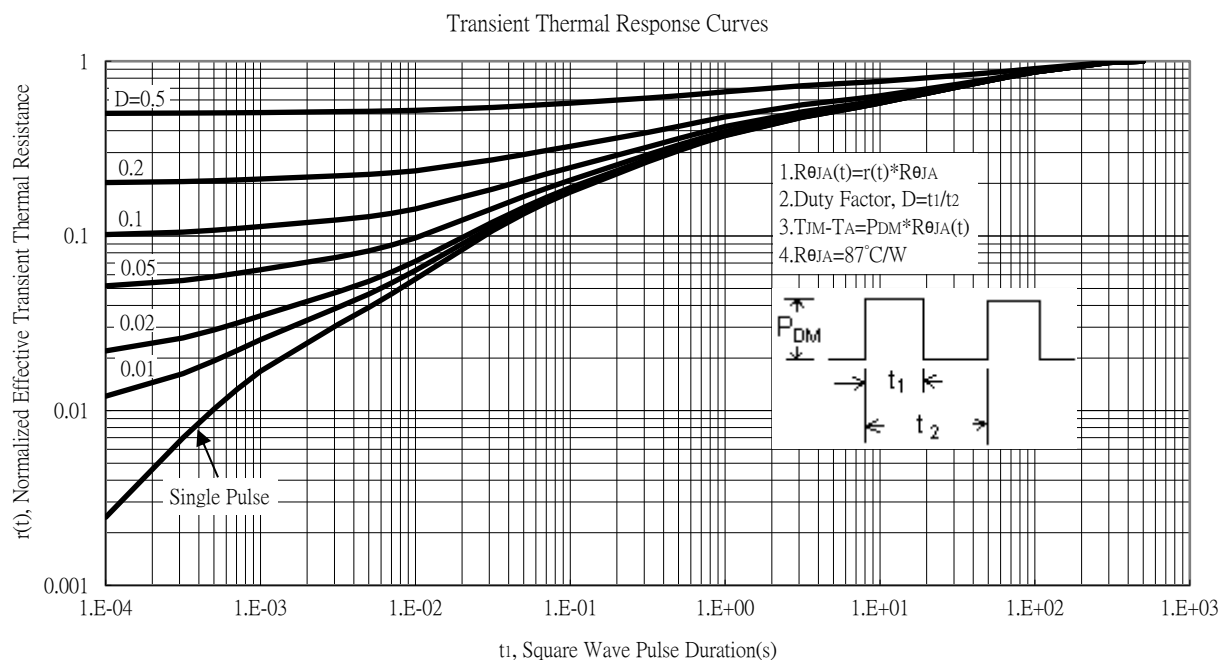
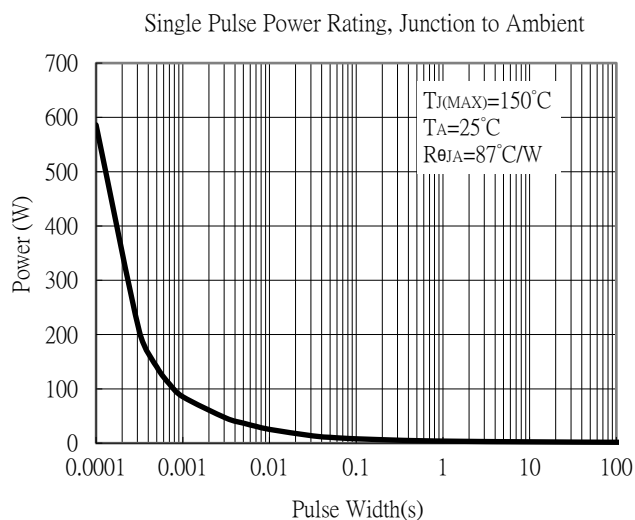
Maximum Safe Operating Area



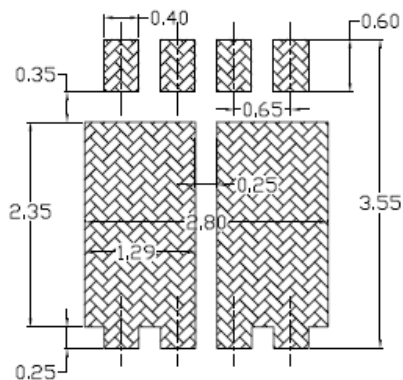
Maximum Drain Current vs Junction Temperature



Typical Characteristics (Cont.)

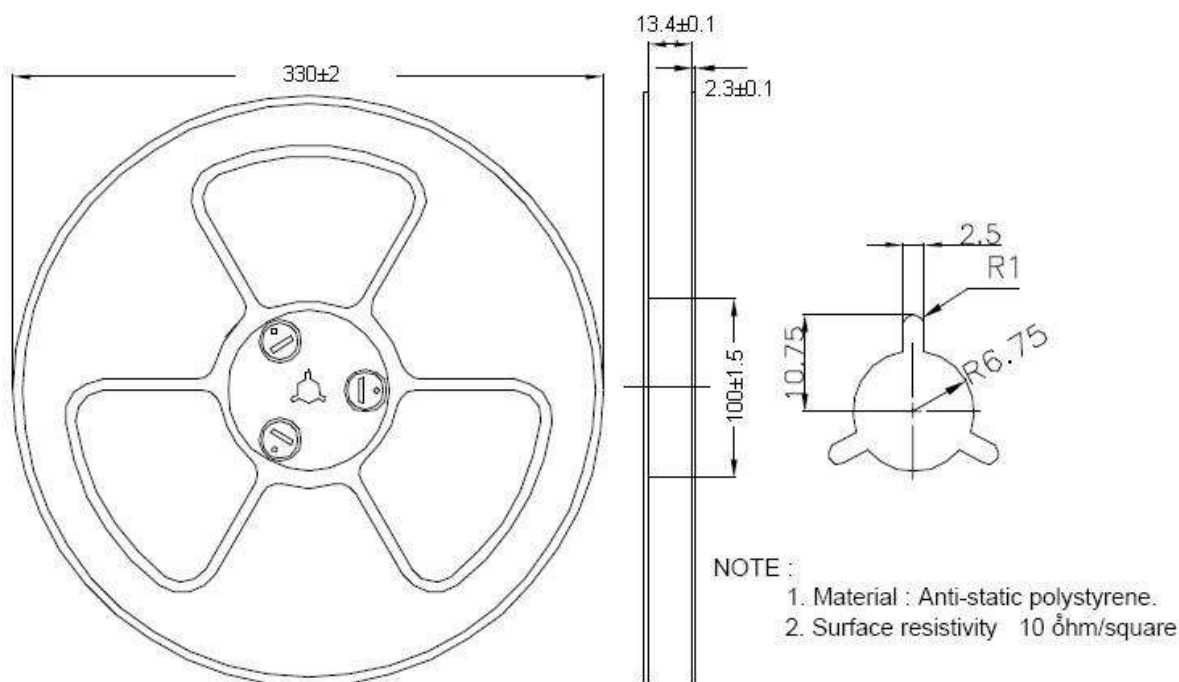


Recommended Soldering Footprint

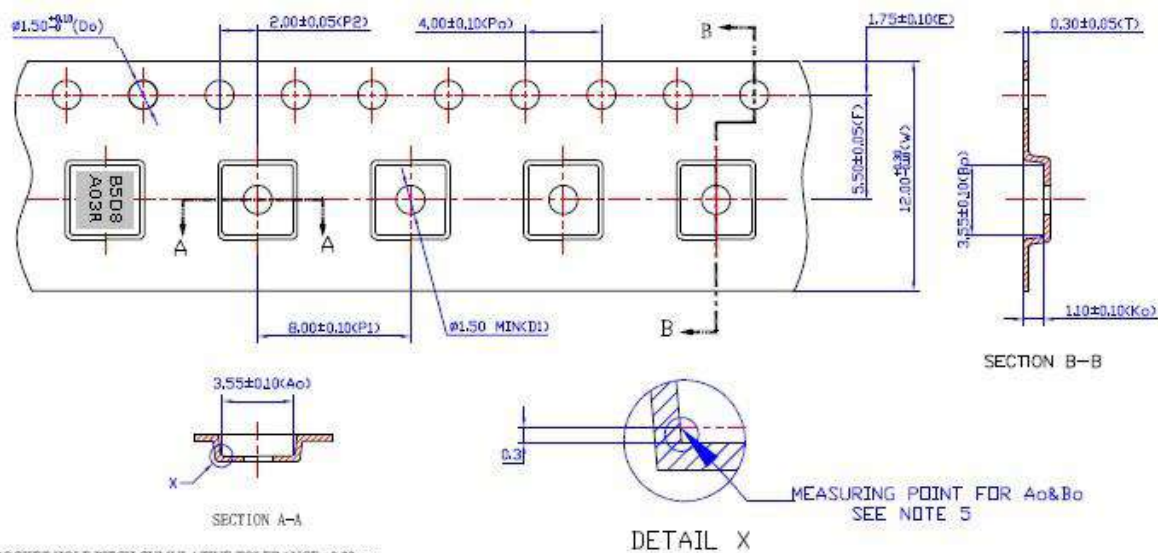


Unit : mm

Reel Dimension

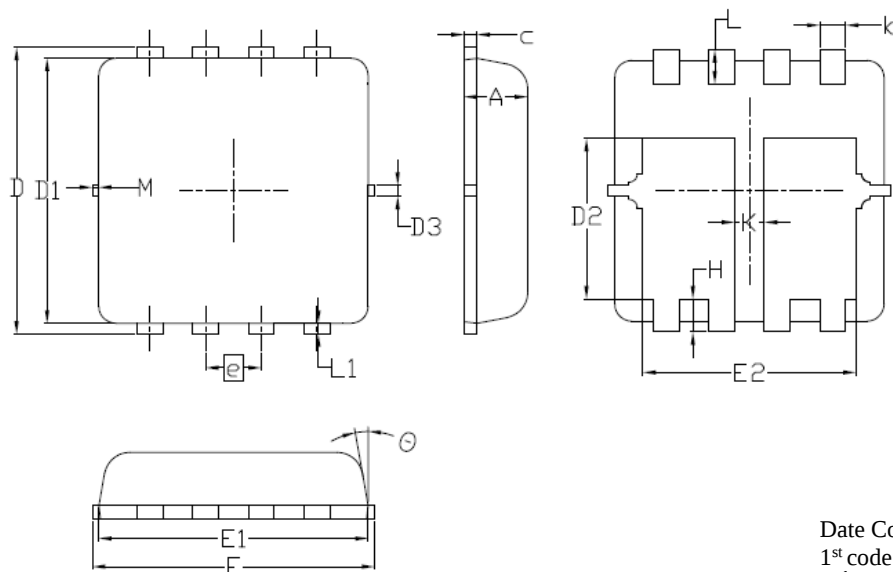


Carrier Tape Dimension



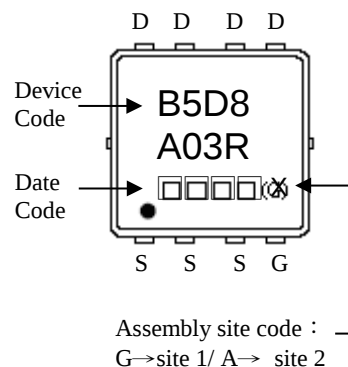
- NOTE:
1. SPROCKET HOLE PITCH CUMULATIVE TOLERANCE $\pm 0.20\text{mm}$
 2. CAMBER REFERENCE TO CARRIER TAPE INSPECTION MANUAL
 3. MATERIAL: BLACK CONDUCTIVE POLYSTYRENE
 4. ALL DIMENSIONS ARE IN MILLIMETERS (UNLESS OTHERWISE SPECIFIED)
 5. Ao AND Bo MEASURED ON A PLANE 0.30mm ABOVE THE BOTTOM OF THE POCKET
 6. Ko MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
 7. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
 8. SURFACE RESISTIVITY:
1X10E4-1X10E11 OHMS/SQ

DFN3x3 Dimension



8-Lead DFN3x3 Plastic Package

Marking:



Date Code(counting from left to right) :

1st code: year code, the last digit of Christian year

2nd code : month code, Jan→A, Feb→B, Mar→C,

Apr→D, May→E, Jun→F, Jul→G, Aug→H,

Sep→J, Oct→K, Nov→L, Dec→M

3rd and 4th codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.70	0.80	0.028	0.031	E2	2.39	2.59	0.094	0.102
b	0.25	0.35	0.010	0.014	e	0.65	BSC	0.026	BSC
c	0.10	0.25	0.004	0.010	H	0.30	0.50	0.012	0.020
D	3.25	3.45	0.128	0.136	L	0.30	0.50	0.012	0.020
D1	3.00	3.20	0.118	0.126	L1	0.13	TYP	0.005	TYP
D2	1.78	1.98	0.070	0.077	K	0.30	-	0.012	-
D3	0.13	TYP	0.005	TYP	θ	-	12°	-	12°
E	3.00	3.40	0.118	0.134	M	-	0.15	-	0.006
E1	3.00	3.20	0.118	0.126					