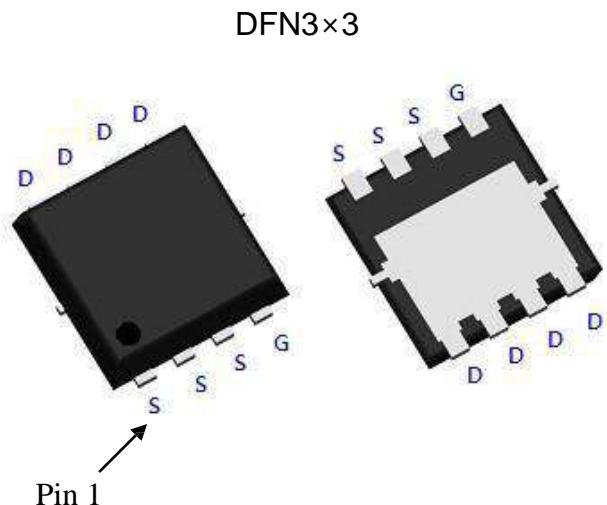


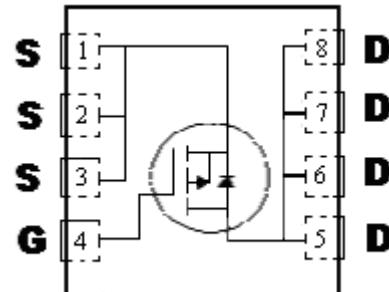
P-Channel Enhancement Mode Power MOSFET

Features :

- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Pb-free lead plating package



BV_{DSS}	-20V
Id@ Tc=25°C, Vgs=-4.5V	-49A
Id@ Ta=25°C, Vgs=-4.5V	-12A
Rdson @ Vgs=-4.5V, Id=-20A	7.7mΩ (typ.)
Rdson @ Vgs=-2.5V, Id=-20A	9.5mΩ (typ.)
Rdson @ Vgs=-1.8V, Id=-20A	12.5 mΩ (typ.)



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KSPRA7D0P02	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

Absolute Maximum Ratings ($T_a=25^\circ C$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current @ $T_c=25^\circ C$, $V_{GS}=-4.5V$	I_D	-49	A
Continuous Drain Current @ $T_c=100^\circ C$, $V_{GS}=-4.5V$		-31	
Continuous Drain Current @ $T_a=25^\circ C$, $V_{GS}=-4.5V$		-12	
Continuous Drain Current @ $T_a=70^\circ C$, $V_{GS}=-4.5V$		-9.6	
Pulsed Drain Current	I_{DM}	-196 *1	mJ
Avalanche Current @ $L=0.1mH$	I_{AS}	-46	
Avalanche Energy @ $L=1mH$, $I_D=-18A$, $V_{DD}=-15V$	E_{AS}	162 *4	
Repetitive Avalanche Energy @ $L=0.05mH$	E_{AR}	4.1 *2	
Total Power Dissipation	$T_c=25^\circ C$	41.7	W
	$T_c=100^\circ C$	16.7	
	$T_a=25^\circ C$	2.5 *3	
	$T_a=70^\circ C$	1.6 *3	
Operating Junction and Storage Temperature Range	T_j , T_{stg}	-55~+150	$^\circ C$

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	3	$^\circ C/W$
Thermal Resistance, Junction-to-ambient, max	$R_{\theta JA}$	50 *3	

Note : 1. Pulse width limited by maximum junction temperature
 2. Duty cycle $\leq 1\%$
 3. Surface mounted on 1 in²copper pad of FR-4 board, $t \leq 10s$; $125^\circ C/W$ when mounted on minimum copper pad.
 4. 100% tested by conditions of $L=0.1mH$, $I_{AS}=-10A$, $V_{GS}=-10V$, $V_{DD}=-15V$

Electrical Characteristics ($T_j=25^\circ C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	-20	-	-	V	$V_{GS}=0V$, $I_D=-250\mu A$
$V_{GS(th)}$	-0.4	-	-1.0		$V_{DS}=V_{GS}$, $I_D=-250\mu A$
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 8V$, $V_{DS}=0V$
I_{DSS}	-	-	-1	μA	$V_{DS}=-16V$, $V_{GS}=0V$
	-	-	-10		$V_{DS}=-16V$, $V_{GS}=0V$, $T_j=125^\circ C$
$R_{DS(ON)} *1$	-	7.7	10	mΩ	$V_{GS}=-4.5V$, $I_D=-20A$
	-	9.5	13.5		$V_{GS}=-2.5V$, $I_D=-20A$
	-	12.5	17.5		$V_{GS}=-1.8V$, $I_D=-20A$
$G_{FS} *1$	-	28.6	-	S	$V_{DS}=-5V$, $I_D=-3A$

Electrical Characteristics(Cont.) ($T_j=25^\circ\text{C}$, unless otherwise specified)

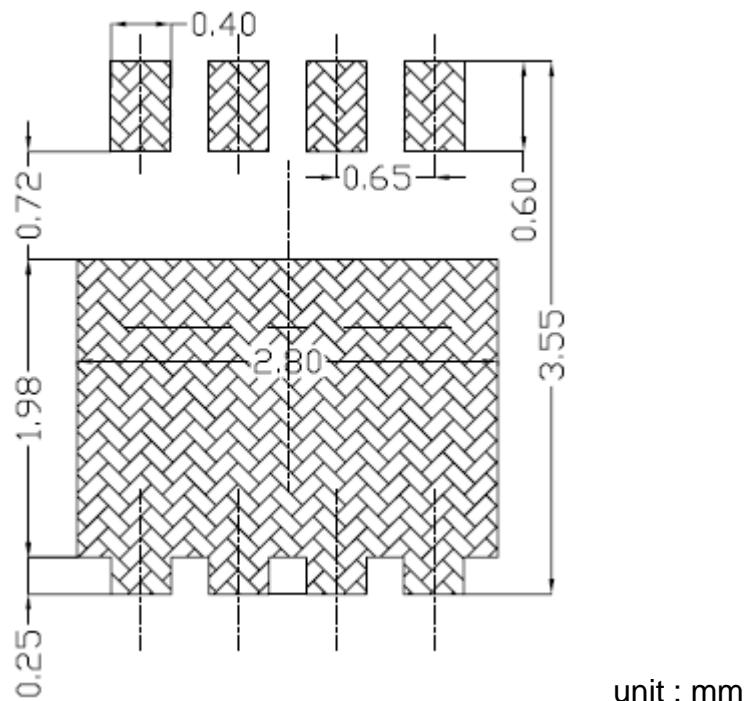
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Dynamic					
C _{iss}	-	5367	-	pF	$V_{DS}=-10\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$
C _{oss}	-	419	-		
C _{rss}	-	284	-		
Q _g *1, 2	-	63	90		
Q _{gs} *1, 2	-	10	-		
Q _{gd} *1, 2	-	7.5	-		
R _g	-	5.3	-	Ω	f=1MHz
Source-Drain Diode					
I _s *1	-	-	-49	A	
I _{SM} *3	-	-	-196		
V _{SD} *1	-	-0.7	-1	V	$I_F=-1\text{A}, V_{GS}=0\text{V}$
t _{rr}	-	15.9	-	ns	$I_F=-20\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$
Q _{rr}	-	9.4	-	nC	

Note : *1.Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

*2.Independent of operating temperature

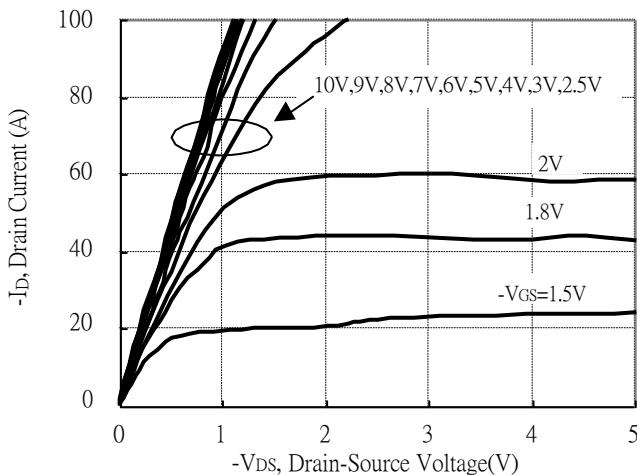
*3.Pulse width limited by maximum junction temperature.

Recommended Soldering Footprint

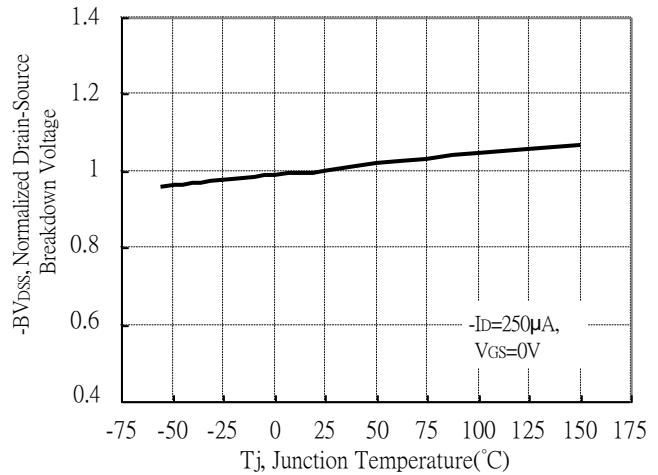


Typical Characteristics

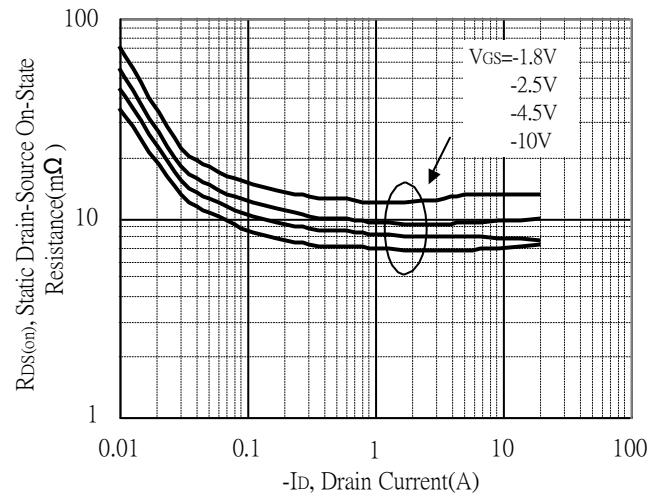
Typical Output Characteristics



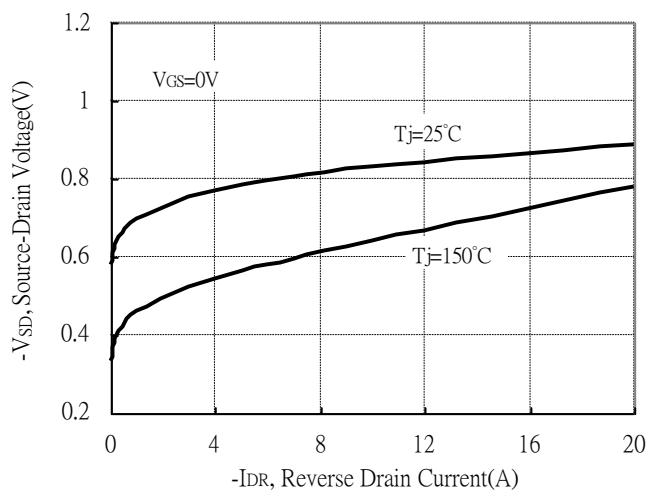
Breakdown Voltage vs Junction Temperature



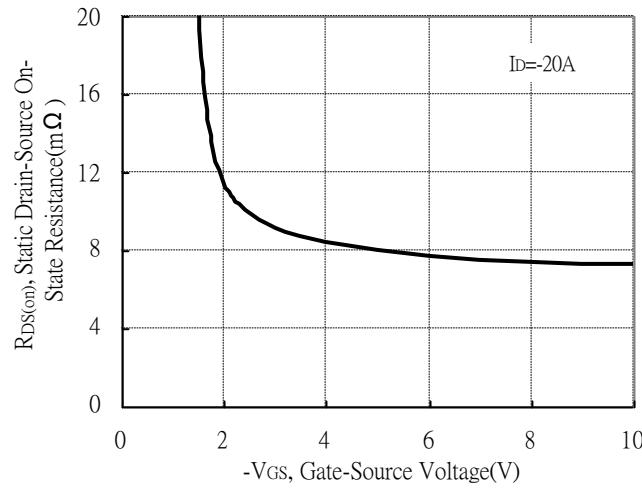
Static Drain-Source On-State resistance vs Drain Current



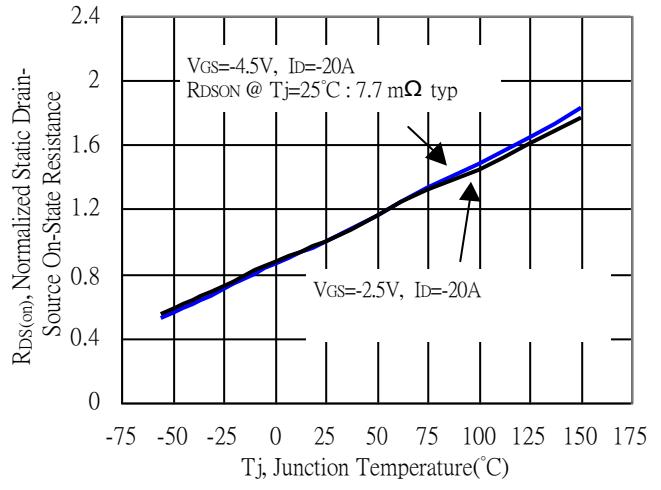
Reverse Drain Current vs Source-Drain Voltage



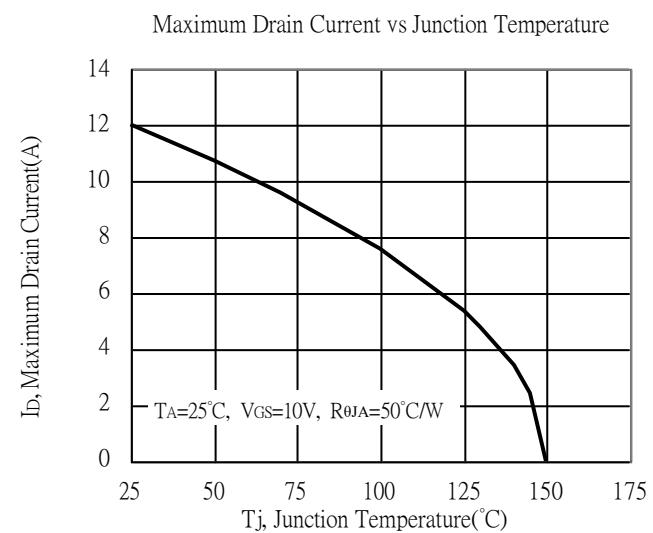
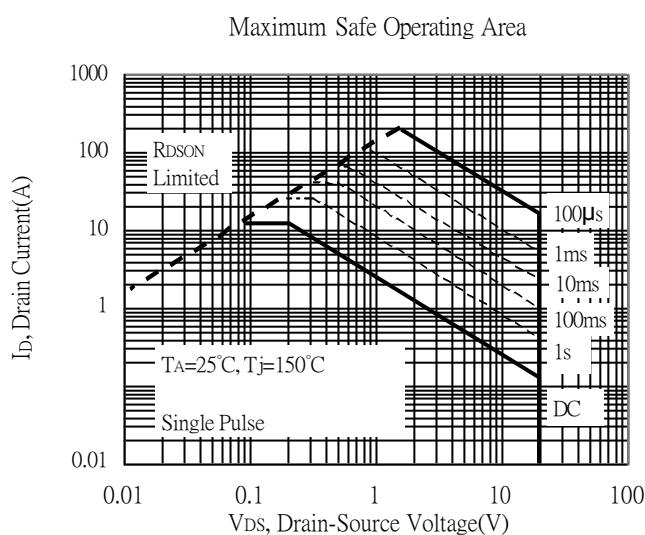
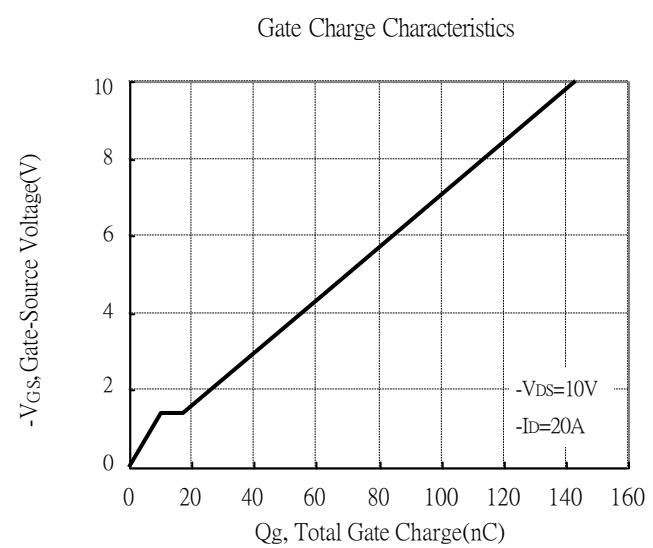
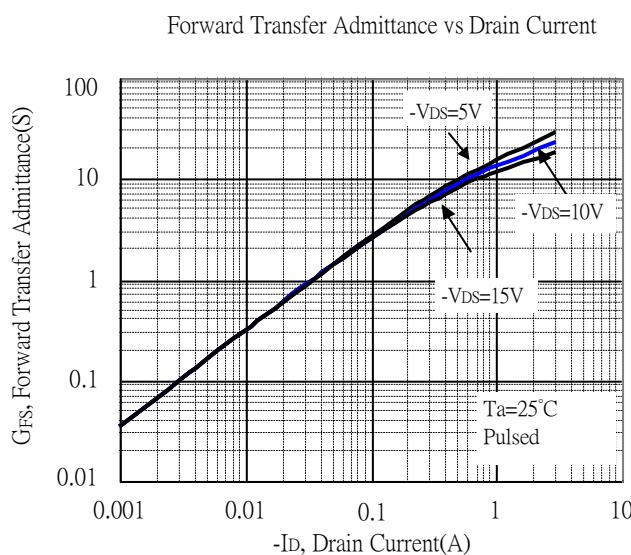
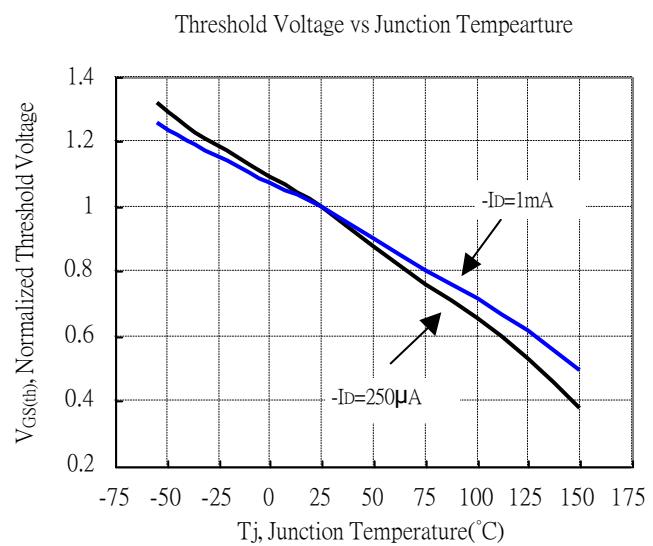
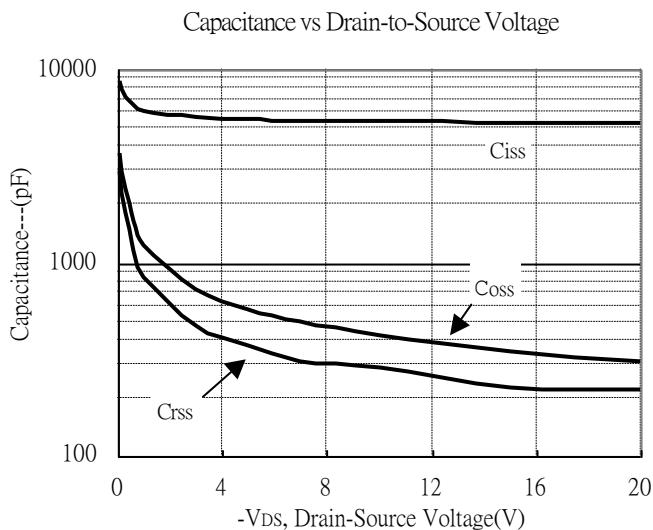
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

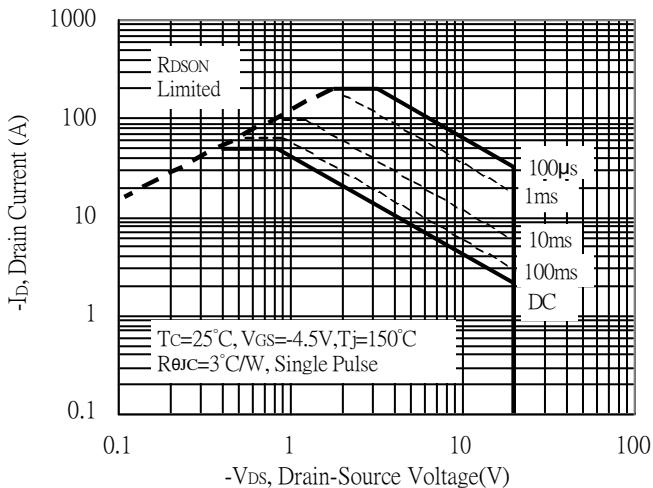


Typical Characteristics(Cont.)

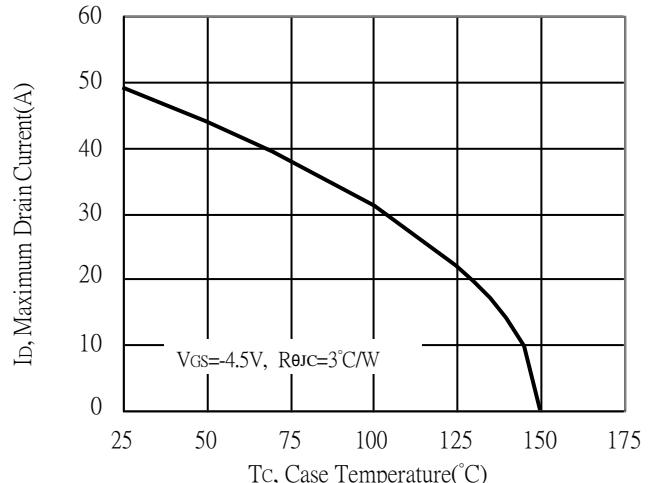


Typical Characteristics(Cont.)

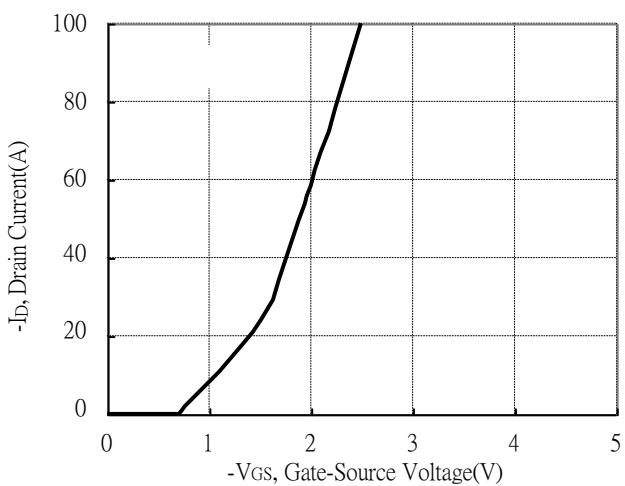
Maximum Safe Operating Area



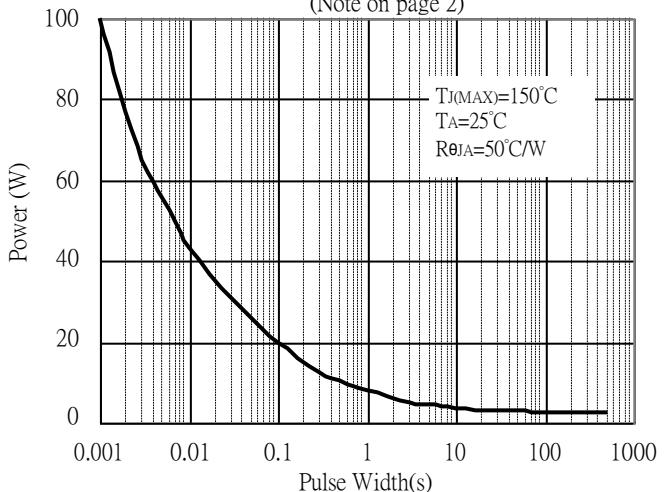
Maximum Drain Current vs Case Temperature



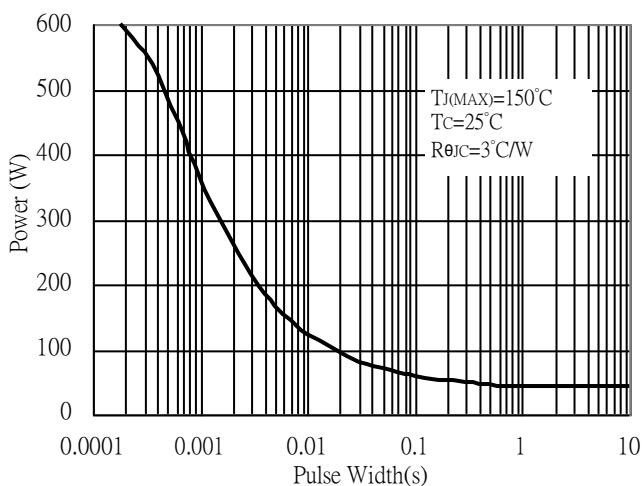
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)

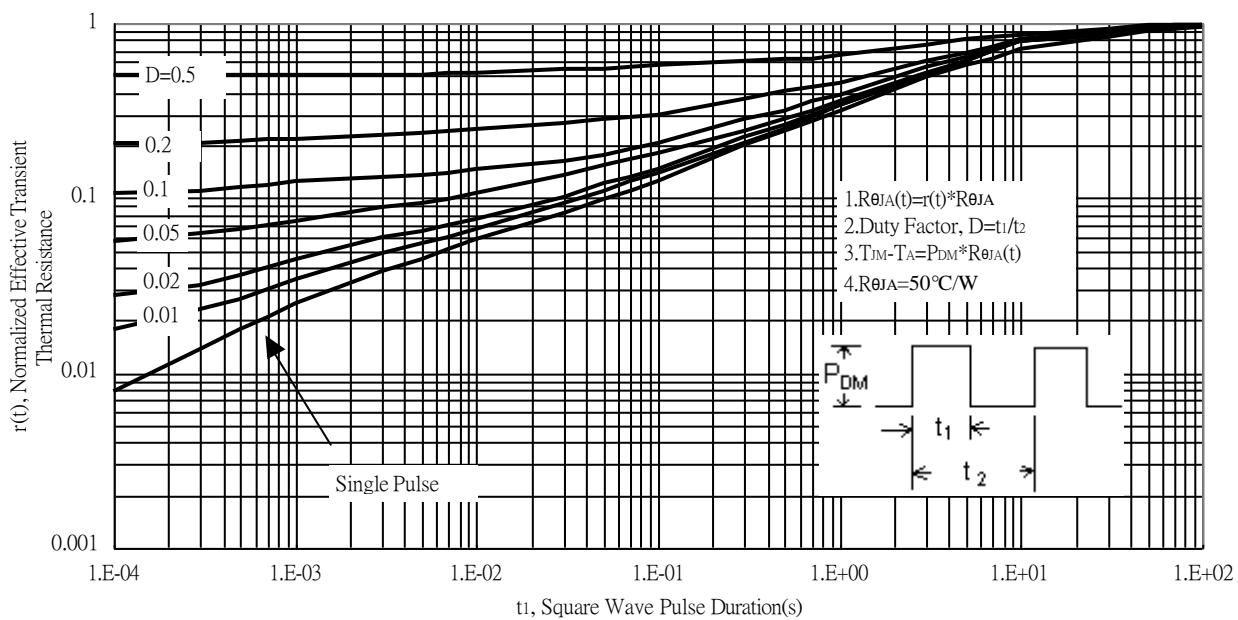


Single Pulse Maximum Power Dissipation

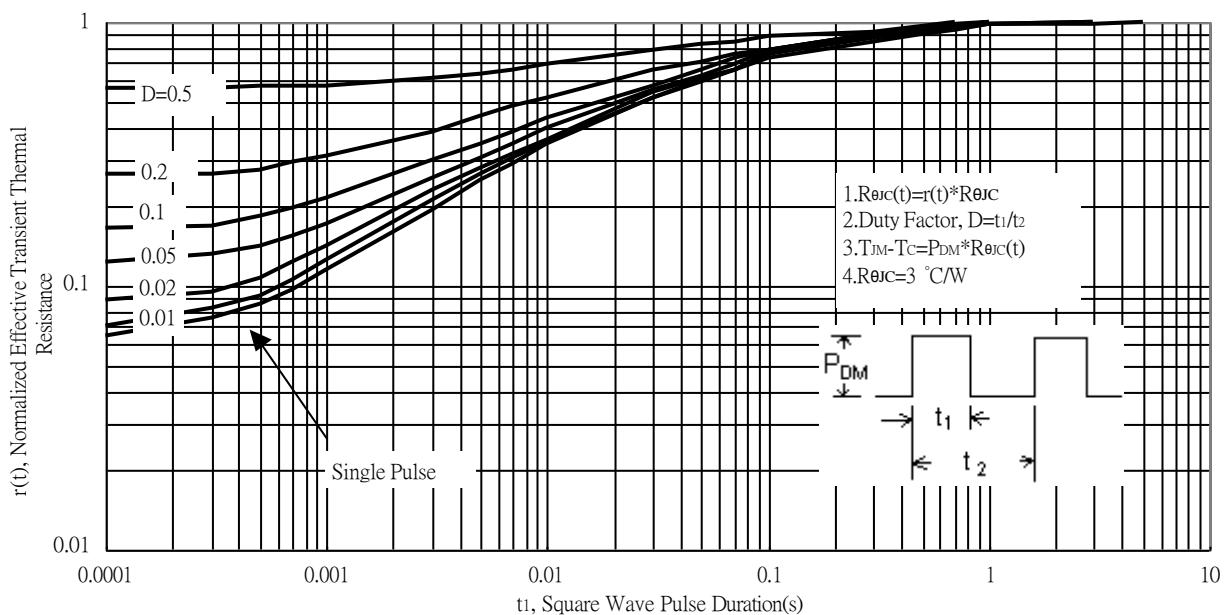


Typical Characteristics(Cont.)

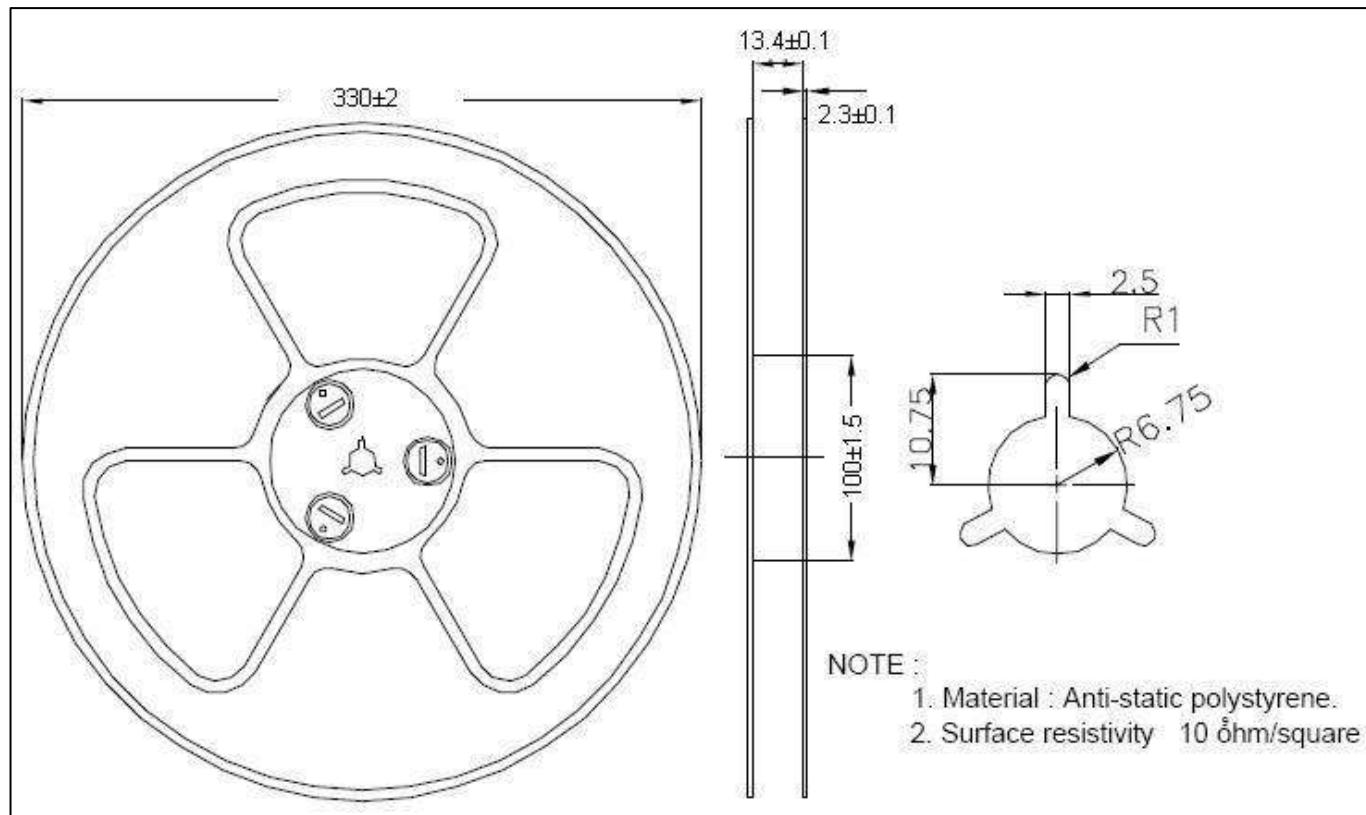
Transient Thermal Response Curves



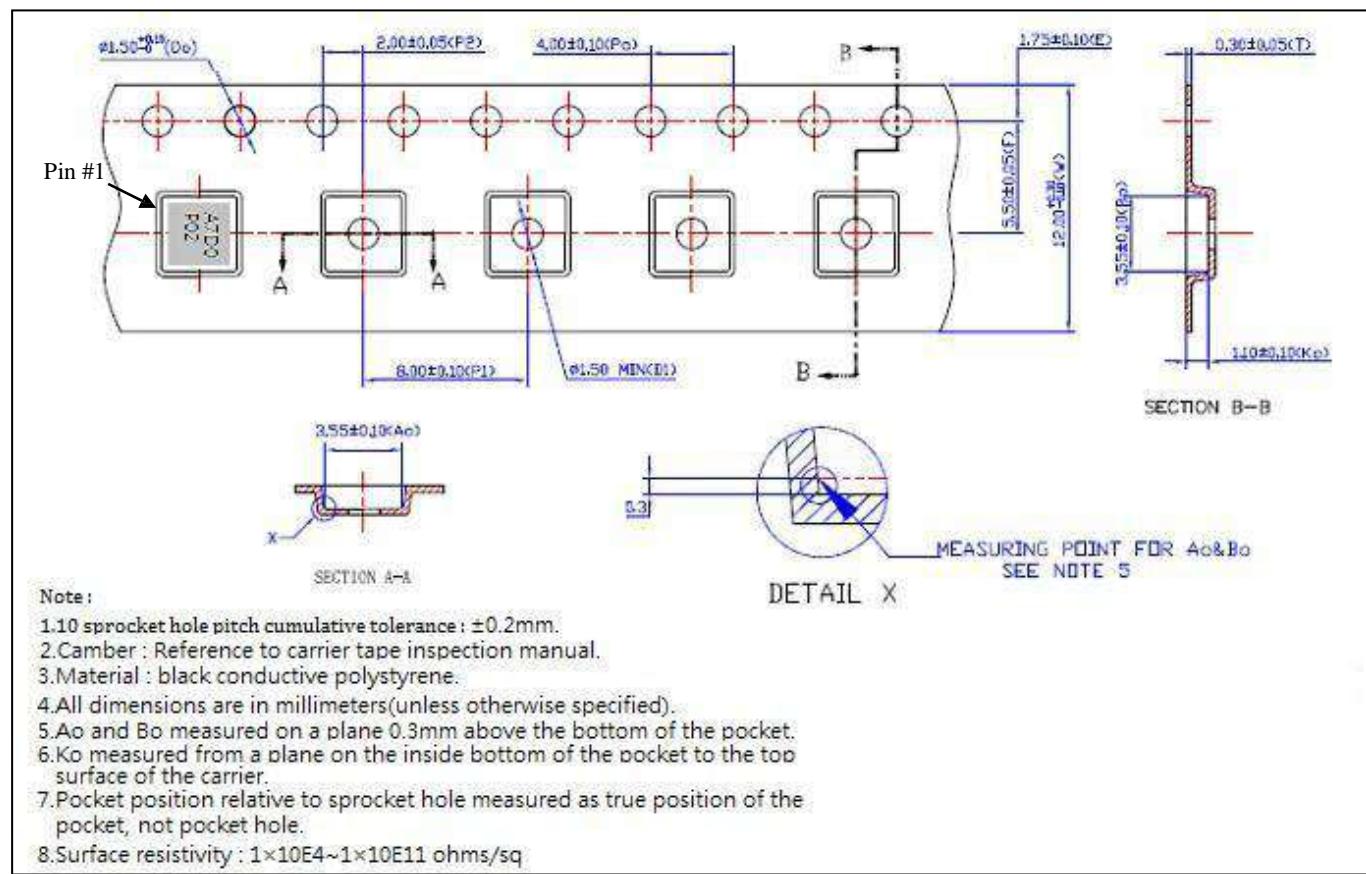
Transient Thermal Response Curves



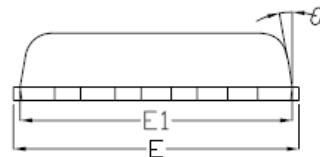
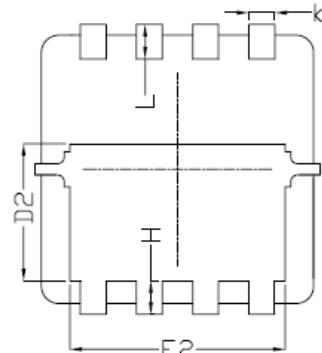
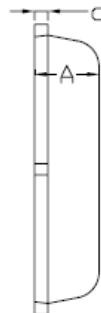
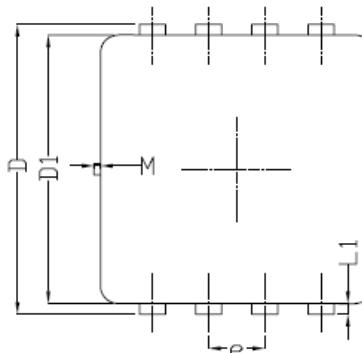
Reel Dimension



Carrier Tape Dimension

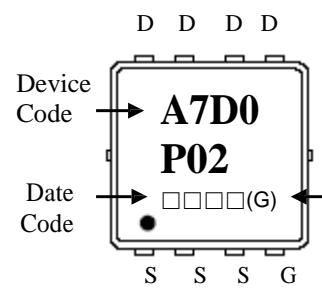


DFN3×3 Dimension



8-Lead DFN3×3 Plastic Package

Marking:



Date Code(counting from left to right) :

1st code: year code, the last digit of Christian year

2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D

May→E, Jun→F, Jul→G, Aug→H, Sep→J,

Oct→K, Nov→L, Dec→M

3rd and 4th codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.70	0.80	0.028	0.031	E1	3.00	3.20	0.118	0.126
b	0.25	0.35	0.010	0.014	E2	2.39	2.59	0.094	0.102
c	0.10	0.25	0.004	0.010	e	0.65	BSC	0.026	BSC
D	3.25	3.45	0.128	0.136	H	0.30	0.50	0.012	0.020
D1	3.00	3.20	0.118	0.126	L	0.30	0.50	0.012	0.020
D2	1.48	1.68	0.058	0.066	L1	0.13	TYP	0.005	TYP
D3	0.13 TYP		0.005	TYP	θ	8°	12°	8°	12°
E	3.20	3.40	0.126	0.134	M	-	0.15	-	0.006