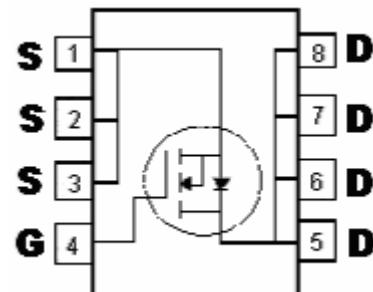
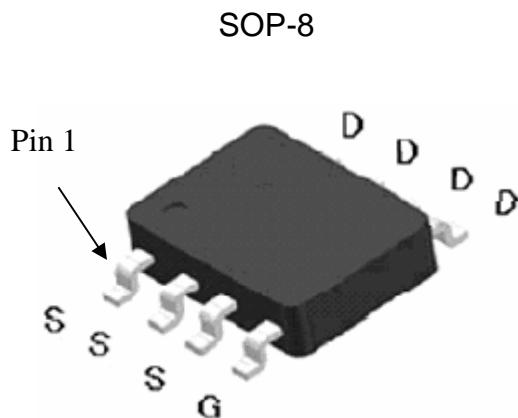


## N-Channel Logic Level Enhancement Mode MOSFET

### Features:

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Dynamic dv/dt rating
- Repetitive Avalanche Rated
- UIS 100% tested
- Pb-free Lead Plating and Halogen-free Package



G : Gate   D : Drain   S : Source

### Ordering Information

Device	Package	Shipping
KSCB55N06	SOP-8 (Pb-free lead plating & Halogen-free package)	2500 pcs / Tape & Reel

**Absolute Maximum Ratings** ( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	60	<b>V</b>
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current @ $T_c=25^\circ\text{C}$ , $V_{GS}=10\text{V}$	$I_D$	8	<b>A</b>
Continuous Drain Current @ $T_c=100^\circ\text{C}$ , $V_{GS}=10\text{V}$		5	
Continuous Drain Current @ $T_A=25^\circ\text{C}$ , $V_{GS}=10\text{V}$		6.3	
Continuous Drain Current @ $T_A=100^\circ\text{C}$ , $V_{GS}=10\text{V}$		4	
Pulsed Drain Current	$I_{DM}$	30 *1	<b>mJ</b>
Avalanche Current	$I_{AS}$	6	
Avalanche Energy @ $L=0.1\text{mH}$ , $I_D=6\text{A}$ , $R_G=25\Omega$	$E_{AS}$	1.8	
Repetitive Avalanche Energy @ $L=0.05\text{mH}$	$E_{AR}$	0.9 *2	
Total Power Dissipation	$P_D$	3.1 *3	<b>W</b>
$T_A=25^\circ\text{C}$		1.2	
Operating Junction and Storage Temperature Range	$T_j$ , $T_{stg}$	-55~+150	<b>°C</b>

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	25	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	40 *3	$^\circ\text{C}/\text{W}$

Note : 1. Pulse width limited by maximum junction temperature.

2. Duty cycle  $\leq 1\%$ .

3. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board,  $t \leq 10\text{s}$ ;  $125^\circ\text{C}/\text{W}$  when mounted on minimum copper pad.

**Characteristics ( $T_c=25^\circ\text{C}$ , unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
$BV_{DSS}$	60	-	-	<b>V</b>	$V_{GS}=0$ , $I_D=250\mu\text{A}$
$V_{GS(th)}$	1.0	1.7	3.0	<b>V</b>	$V_{DS} = V_{GS}$ , $I_D=250\mu\text{A}$
$G_{FS}$ *1	-	11	-	<b>S</b>	$V_{DS}=5\text{V}$ , $I_D=6\text{A}$
$I_{GSS}$	-	-	$\pm 100$	$\text{nA}$	$V_{GS}=\pm 20$
$I_{DSS}$	-	-	1	$\mu\text{A}$	$V_{DS}=48\text{V}$ , $V_{GS}=0$
	-	-	25		$V_{DS}=40\text{V}$ , $V_{GS}=0$ , $T_J=125^\circ\text{C}$
$R_{DS(ON)} *1$	-	35	45	$\text{m}\Omega$	$V_{GS}=10\text{V}$ , $I_D=6\text{A}$
	-	38	50	$\text{m}\Omega$	$V_{GS}=5\text{V}$ , $I_D=5\text{A}$
<b>Dynamic</b>					
$C_{iss}$	-	1160	-	<b>pF</b>	$V_{GS}=0\text{V}$ , $V_{DS}=30\text{V}$ , $f=1\text{MHz}$
$C_{oss}$	-	44	-		
$C_{rss}$	-	33	-		

### **Characteristics (T<sub>c</sub>=25°C, unless otherwise specified)**

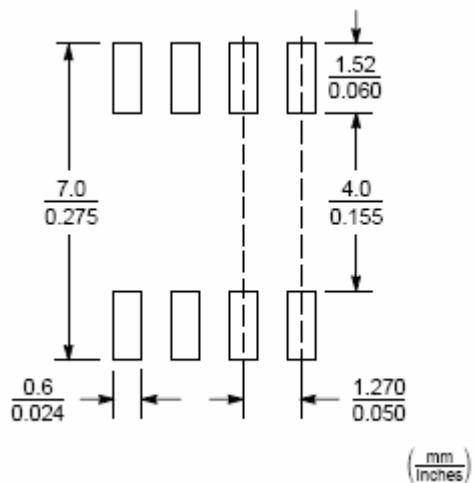
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Q <sub>g</sub> *1, 2	-	14	-	nC	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =6A
Q <sub>gs</sub> *1, 2	-	3.7	-		
Q <sub>gd</sub> *1, 2	-	4.9	-		
t <sub>d(ON)</sub> *1, 2	-	12	-	ns	V <sub>DS</sub> =30V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>GS</sub> =6Ω
t <sub>r</sub> *1, 2	-	6	-		
t <sub>d(OFF)</sub> *1, 2	-	34	-		
t <sub>f</sub> *1, 2	-	12	-		
<b>Source-Drain Diode</b>					
I <sub>S</sub> *1	-	-	2.3	A	
I <sub>SM</sub> *3	-	-	9.2		
V <sub>SD</sub> *1	-	0.77	1.2	V	I <sub>F</sub> =I <sub>S</sub> , V <sub>GS</sub> =0V

Note : \*1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

\*2.Independent of operating temperature

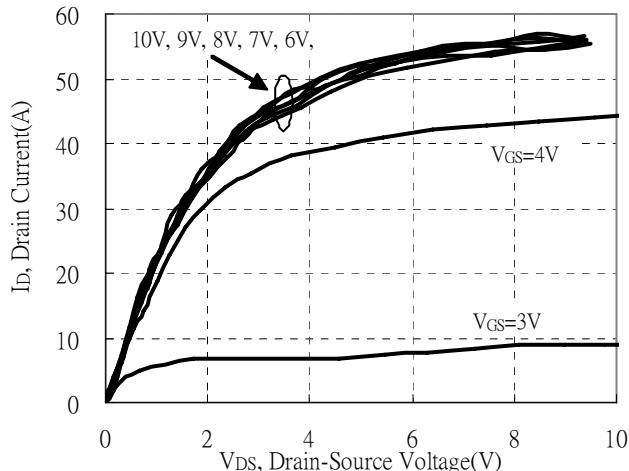
\*3.Pulse width limited by maximum junction temperature.

### **Recommended Soldering Footprint**

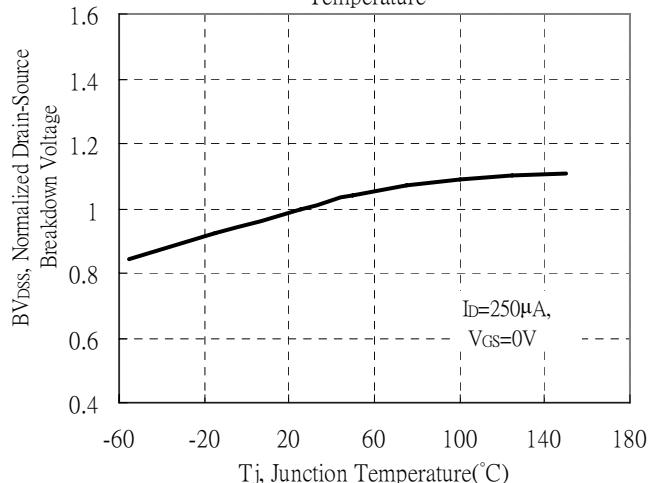


## Typical Characteristics

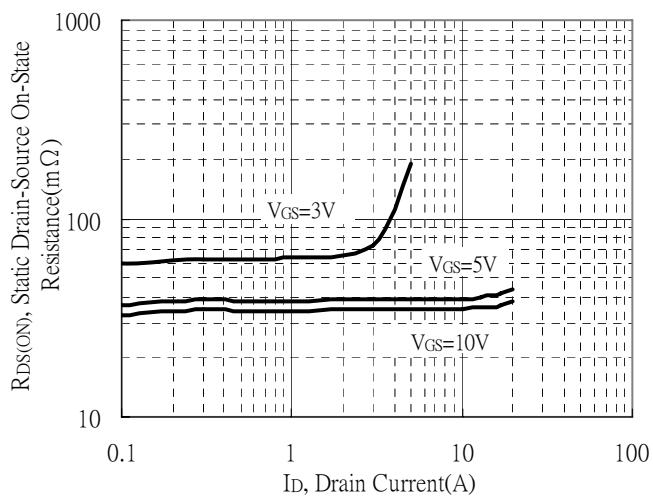
Typical Output Characteristics



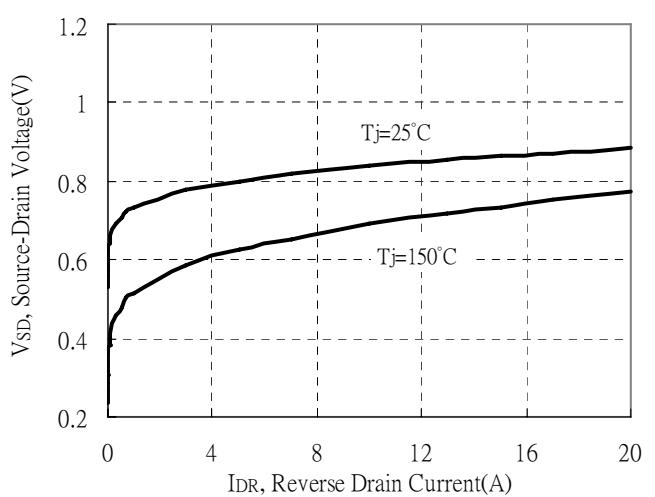
Normalized Breakdown Voltage vs Ambient Temperature



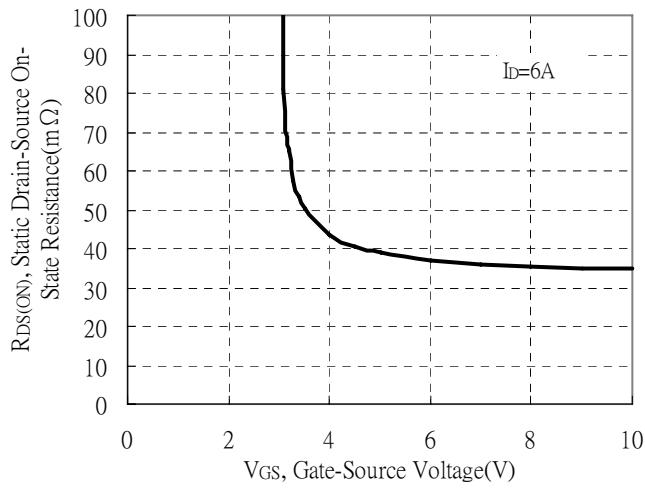
Static Drain-Source On-State resistance vs Drain Current



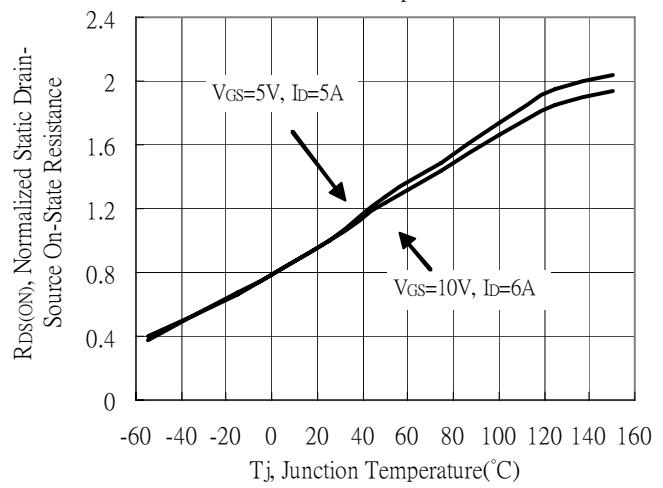
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

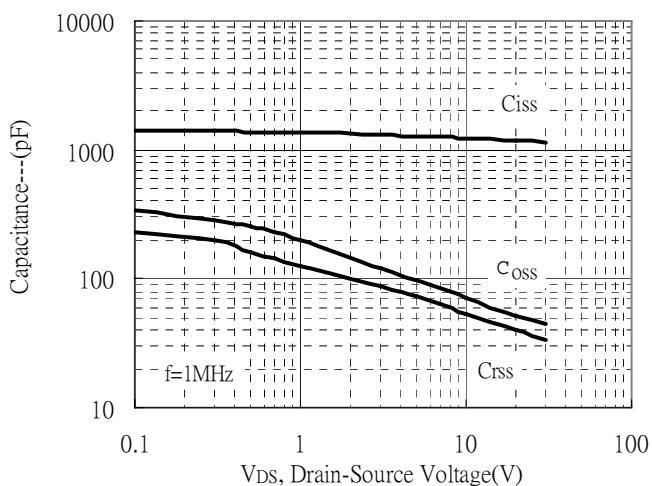


Normalized Drain-Source On-State Resistance vs Junction Temperature

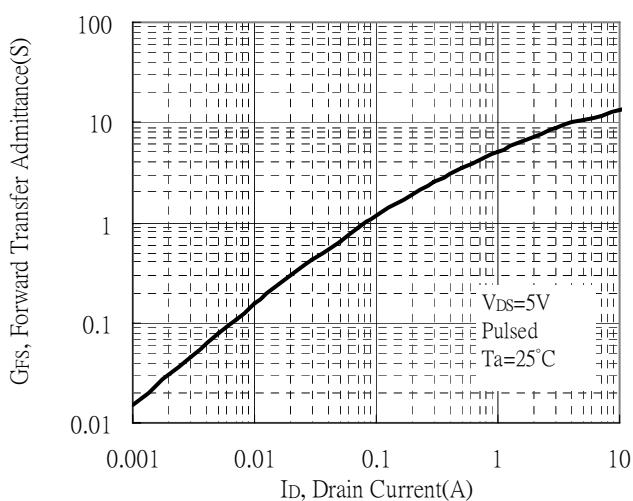


## Typical Characteristics(Cont.)

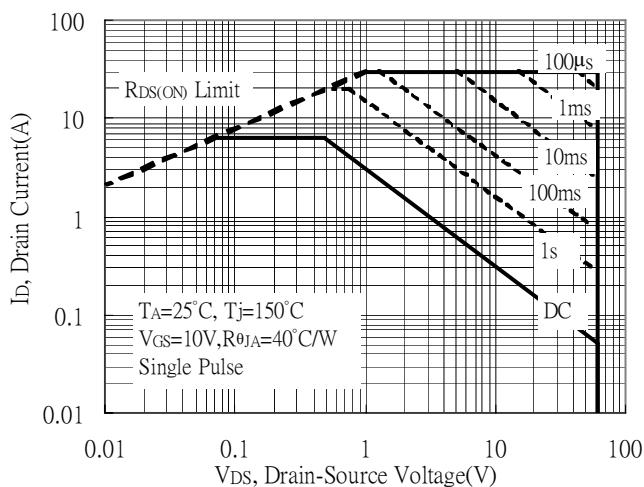
Capacitance vs Drain-to-Source Voltage



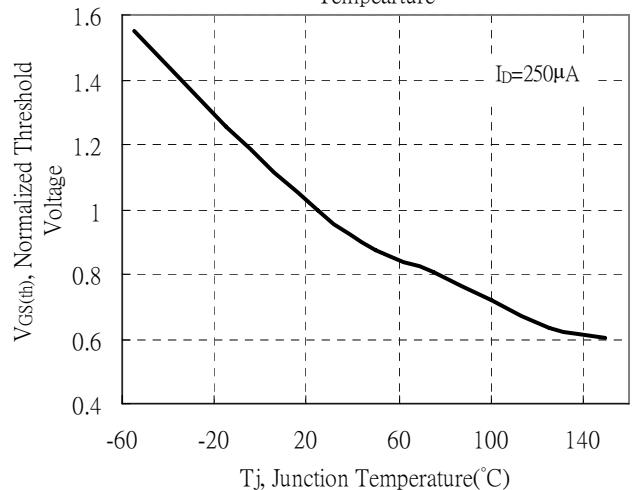
Forward Transfer Admittance vs Drain Current



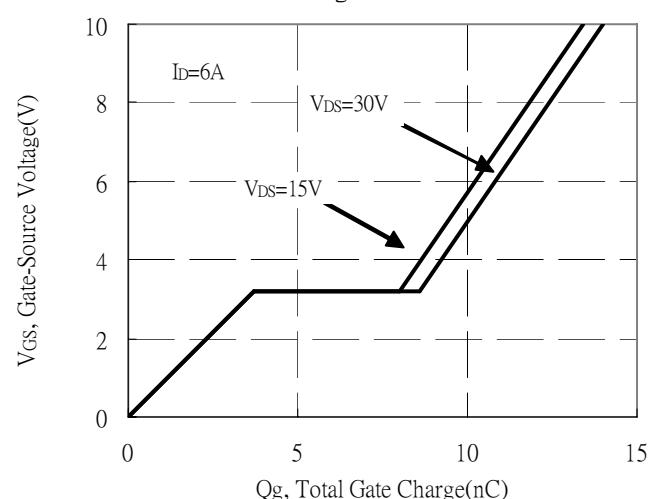
Maximum Safe Operating Area



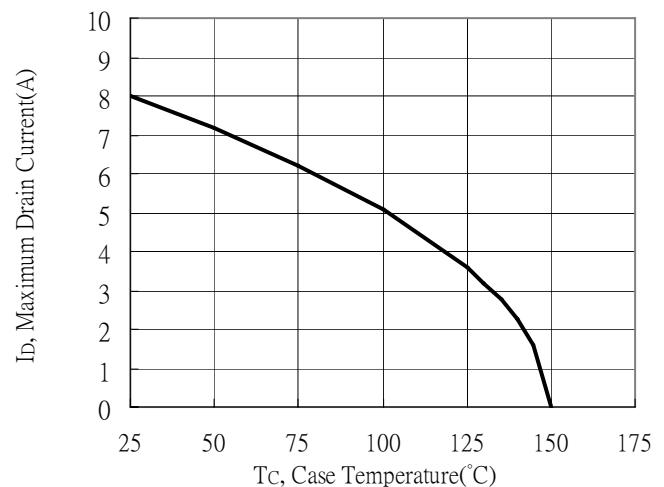
Normalized Threshold Voltage vs Junction Temperature



Gate Charge Characteristics

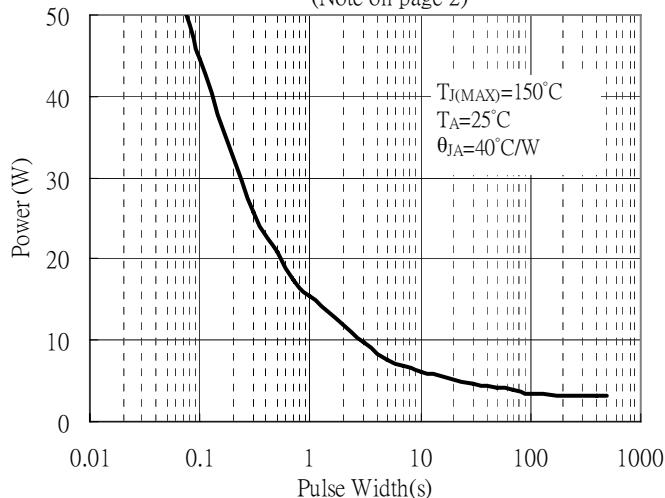


Maximum Drain Current vs Case Temperature

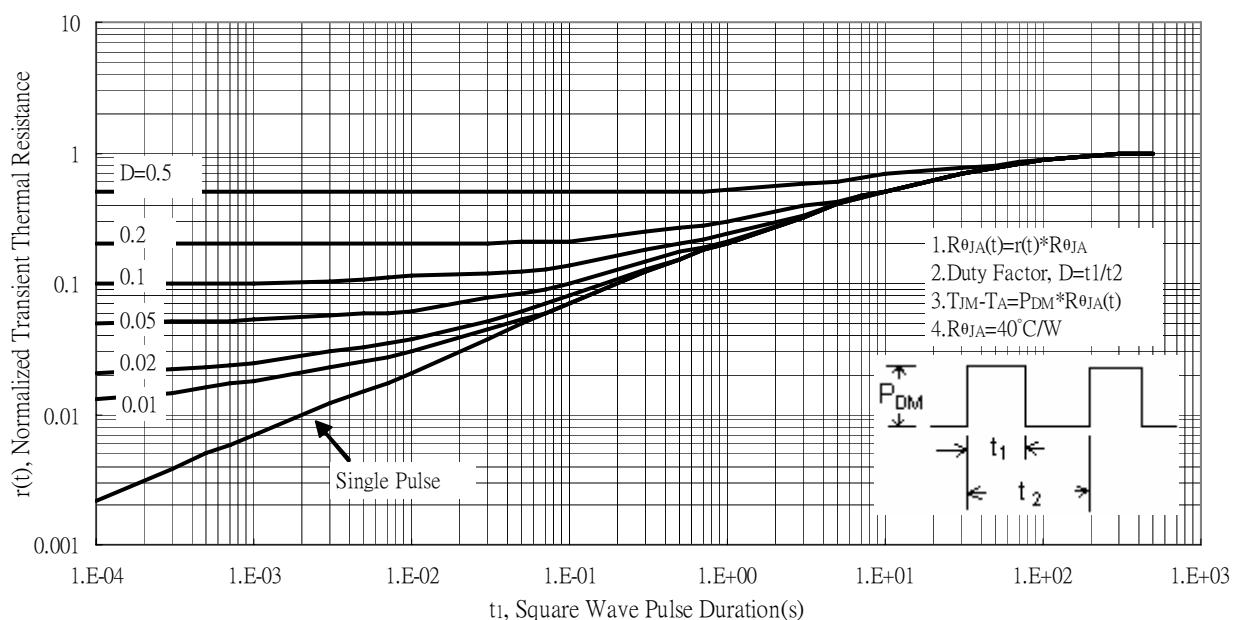


## Typical Characteristics(Cont.)

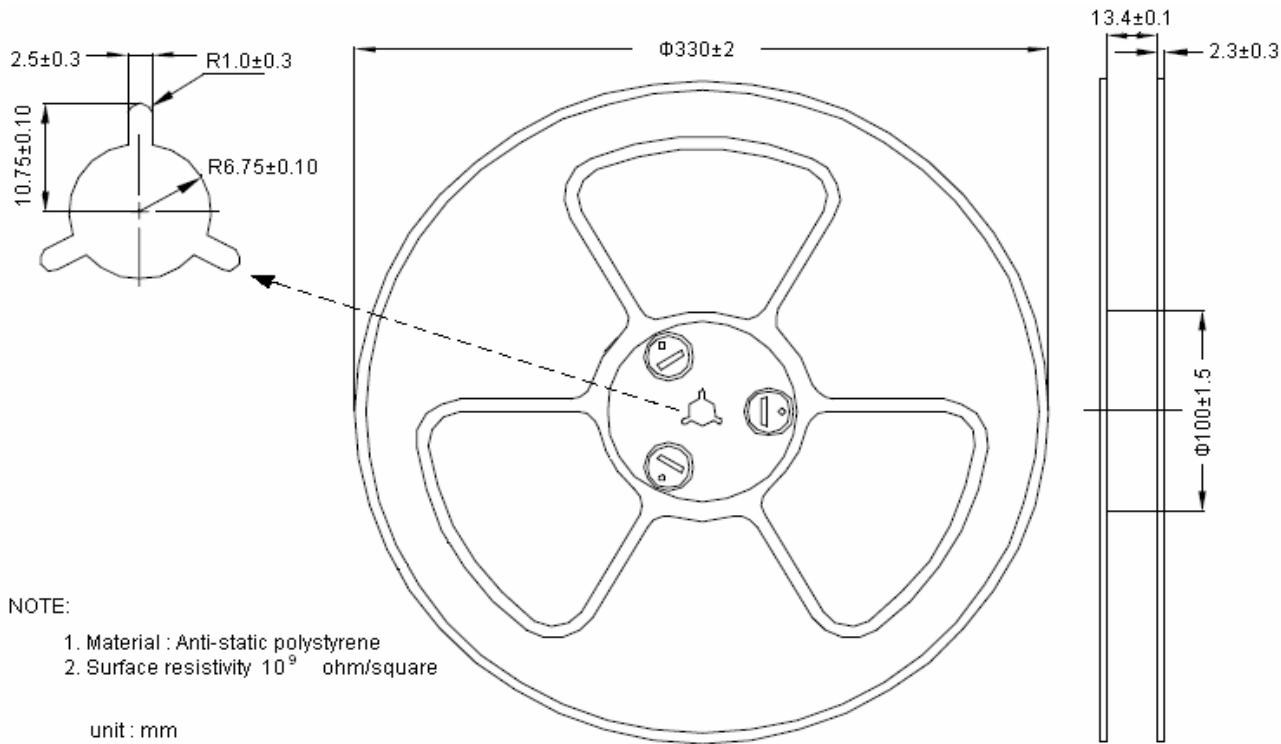
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



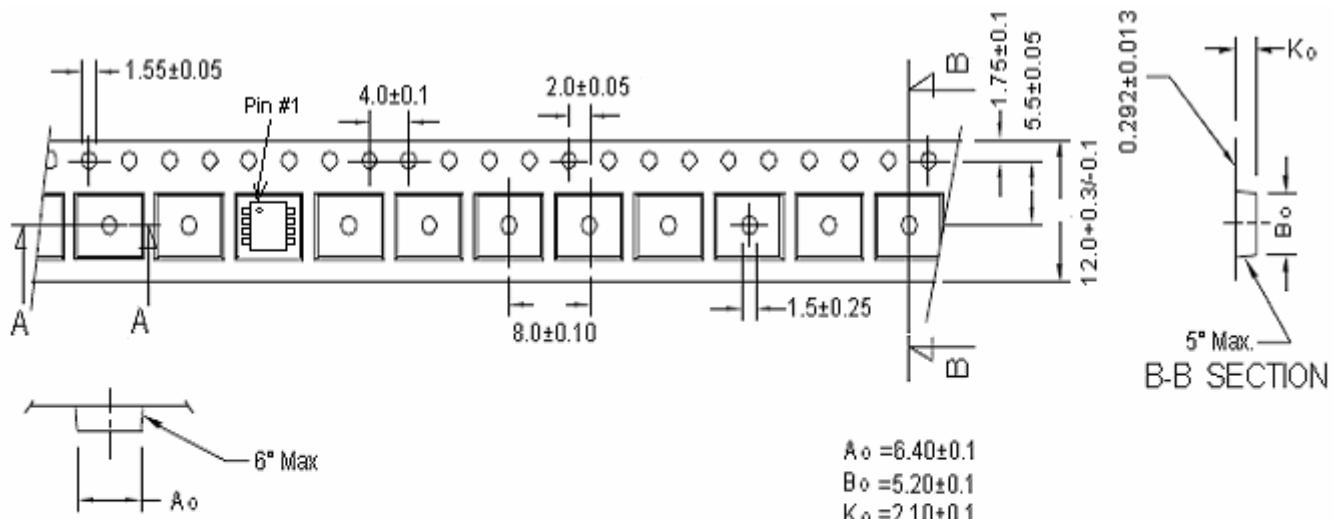
Transient Thermal Response Curves



## Reel Dimension

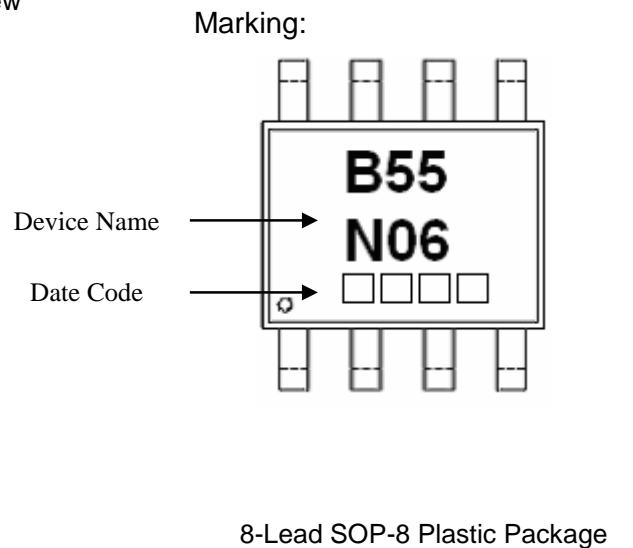
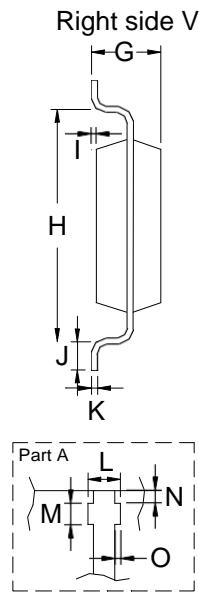
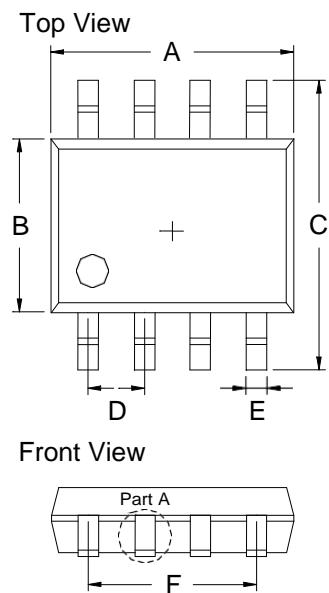


## Carrier Tape Dimension



Uni : millimeter

## SOP-8 Dimension



\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1850	0.2007	4.70	5.10	I	0.0031	0.0110	0.08	0.28
B	0.1457	0.1614	3.70	4.10	J	0.0157	0.0323	0.40	0.83
C	0.2283	0.2441	5.80	6.20	K	0.0074	0.0102	0.19	0.26
D	0.0500*		1.27*		L	0.0145	0.0204	0.37	0.52
E	0.0130	0.0201	0.33	0.51	M	0.0118	0.0197	0.30	0.50
F	0.1472	0.1527	3.74	3.88	N	0.0031	0.0051	0.08	0.13
G	0.0472	0.0638	1.20	1.62	O	0.0000	0.0059	0.00	0.15
H	0.1889	0.2007	4.80	5.10					