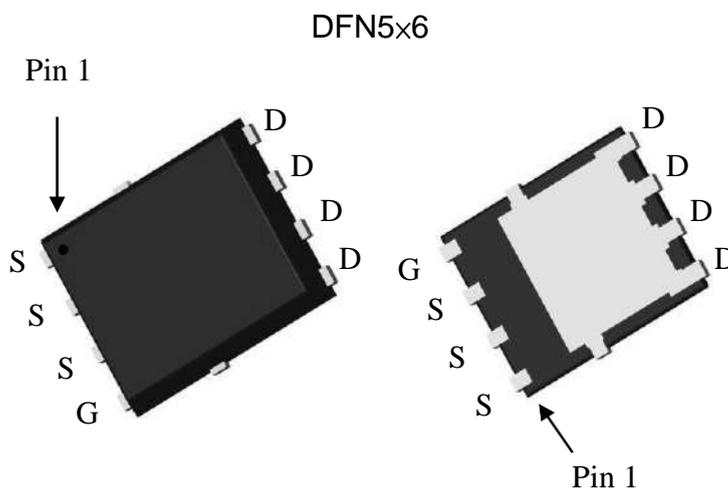


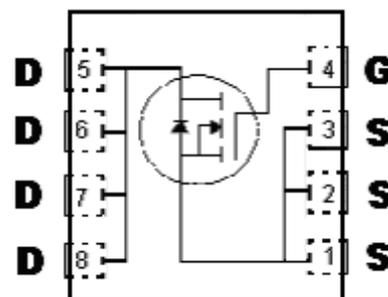
N-Channel Enhancement Mode Power MOSFET

Features:

- Simple Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Pb-free lead plating and Halogen-free package



BV_{DSS}	100V
$I_D @ V_{GS}=10V, T_C=25^\circ C$	84A
$I_D @ V_{GS}=10V, T_A=25^\circ C$	22A
$R_{DSON(TYP)}$ $V_{GS}=10V, I_D=30A$	5.3m Ω



G : Gate D : Drain S : Source

Ordering Information

Device	Package	Shipping
KPRE5D0N10BR	DFN 5 x6 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	10s	Steady State	Unit	
Drain-Source Voltage	V _{DS}	100		V	
Gate-Source Voltage	V _{GS}	±20			
Continuous Drain Current @ T _C =25°C, V _{GS} =10V (Note 1)	I _D	84		A	
Continuous Drain Current @ T _C =100°C, V _{GS} =10V (Note 1)		59.4			
Continuous Drain Current @ T _A =25°C, V _{GS} =10V (Note 2)	I _{DSM}	22	14.5		
Continuous Drain Current @ T _A =70°C, V _{GS} =10V (Note 2)		18.4	12.1		
Continuous Drain Current @ T _A =85°C, V _{GS} =10V (Note 2)		17	11.2		
Pulsed Drain Current (Note 3)		I _{DM}	336 *1		
Avalanche Current @ L=0.1mH (Note 3)	I _{AS}	80			mJ
Avalanche Energy @ L=1mH, I _D =36A, V _{DD} =50V (Note 4)	E _{AS}	648			
Repetitive Avalanche Energy @ L=0.05mH (Note 3)	E _{AR}	5 *2			
Total Power Dissipation	P _D	T _C =25°C (Note 1)	100		W
		T _C =100°C (Note 1)	50		
	P _{DSM}	T _A =25°C (Note 2)	6.8	3	
		T _A =70°C (Note 2)	4.8	2.1	
		T _A =85°C (Note 2)	4.1	1.8	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+175		°C	

Thermal Data

Parameter	Symbol	Typical	Maximum	Unit	
Thermal Resistance, Junction-to-ambient (Note 2)	R _{θJA}	t≤10s	18	22	°C/W
		Steady State	42	50	
Thermal Resistance, Junction-to-case	R _{θJC}	1.3	1.5		

Note : 1.The power dissipation P_D is based on T_{J(MAX)}=175 °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

2.The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25 °C. The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 175°C. The value in any given application depends on the user's specific board design.

3.Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=175 °C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.

4.100% tested by conditions of L=0.1mH, I_{AS}=50A, V_{GS}=10V, V_{DD}=50V

Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	100	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2	-	4		V _{DS} = V _{GS} , I _D =250μA
G _{FSS} *1	-	24.6	-	S	V _{DS} =10V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =80V, V _{GS} =0V
	-	-	25		V _{DS} =80V, V _{GS} =0V, T _j =125°C
R _{DS(ON)} *1	-	5.3	7.5	mΩ	V _{GS} =10V, I _D =30A
Dynamic					
C _{iss}	-	4772	6204	pF	V _{GS} =0V, V _{DS} =50V, f=1MHz
C _{oss}	-	457	594		
C _{rss}	-	34	51		
Q _g *1, 2	-	67.7	88	nC	V _{DS} =80V, V _{GS} =10V, I _D =30A
Q _{gs} *1, 2	-	26	-		
Q _{gd} *1, 2	-	14.6	-		
t _{d(ON)} *1, 2	-	37.6	-	ns	V _{DD} =50V, I _D =20A, V _{GS} =10V, R _G =3Ω
t _r *1, 2	-	12.2	-		
t _{d(OFF)} *1, 2	-	55.4	-		
t _f *1, 2	-	13.4	-		
R _g	-	1.4	-	Ω	f=1MHz
Source-Drain Diode					
I _S *1	-	-	84	A	
I _{SM} *3	-	-	336		
V _{SD} *1	-	0.8	1.2	V	I _S =20A, V _{GS} =0V
t _{rr}	-	52	68	ns	I _F =20A, dI _F /dt=100A/μs
Q _{rr}	-	104	-	nC	
I _{RRM}	-	3.5	-	A	

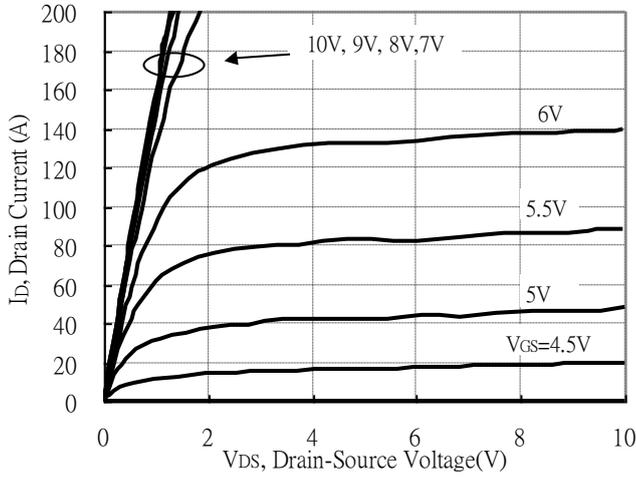
Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2.Independent of operating temperature

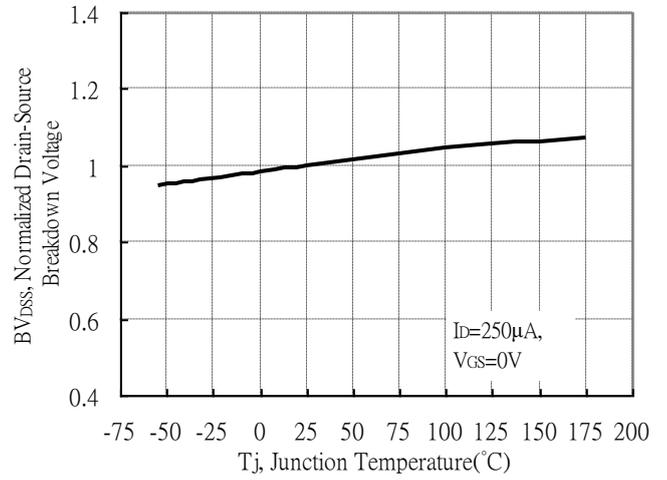
*3.Pulse width limited by maximum junction temperature.

Typical Characteristics

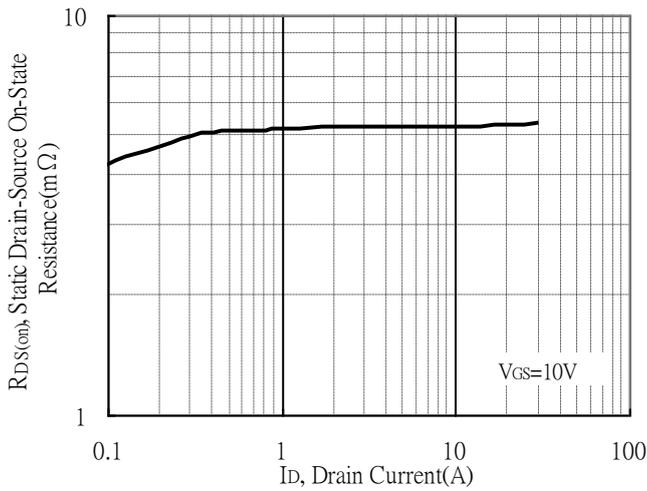
Typical Output Characteristics



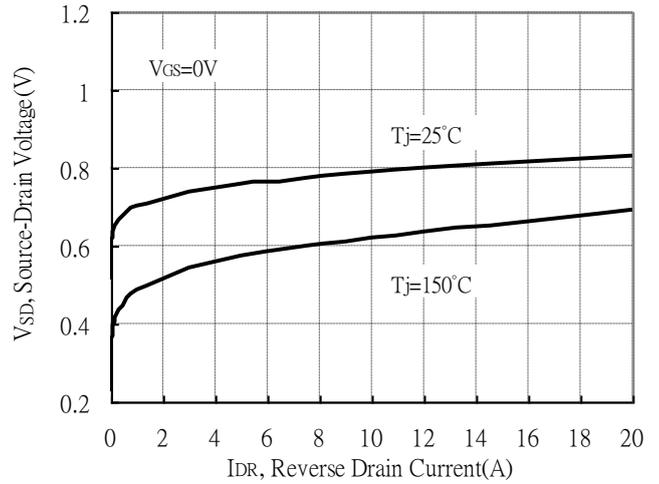
Breakdown Voltage vs Ambient Temperature



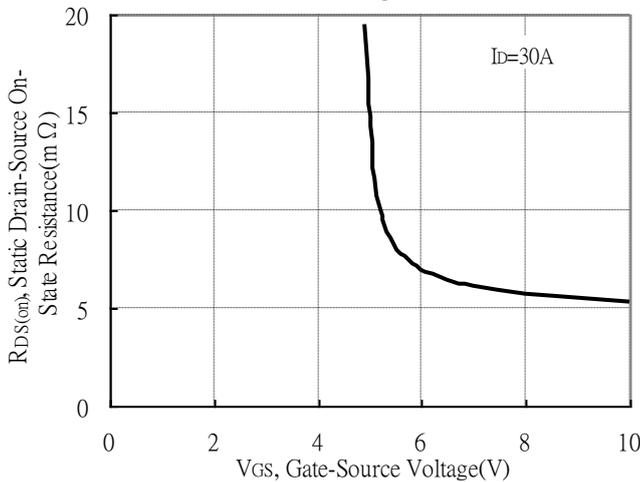
Static Drain-Source On-State resistance vs Drain Current



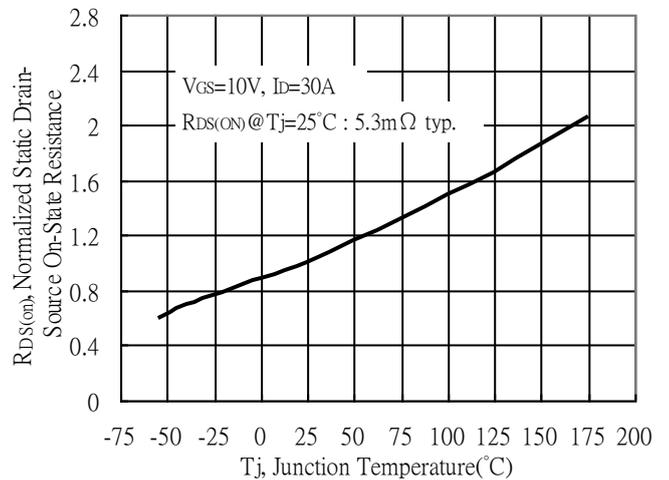
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

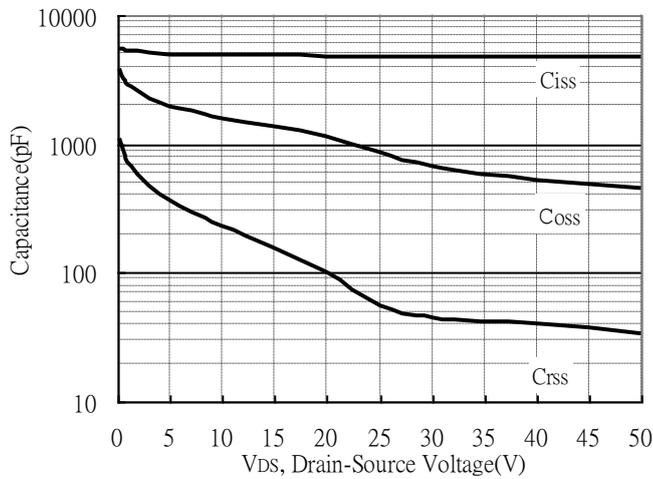


Drain-Source On-State Resistance vs Junction Temperature

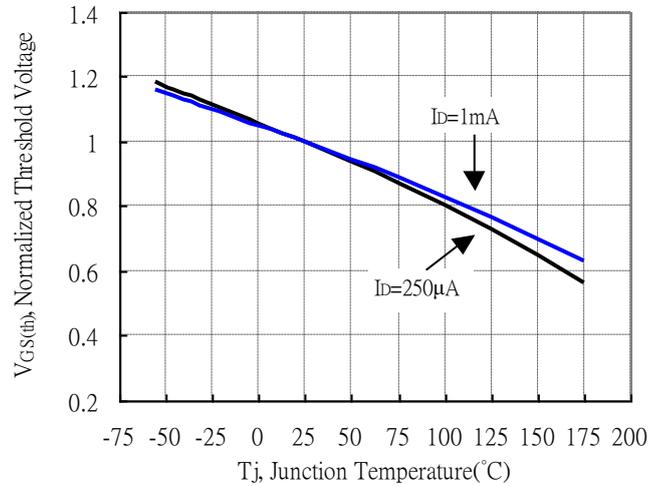


Typical Characteristics(Cont.)

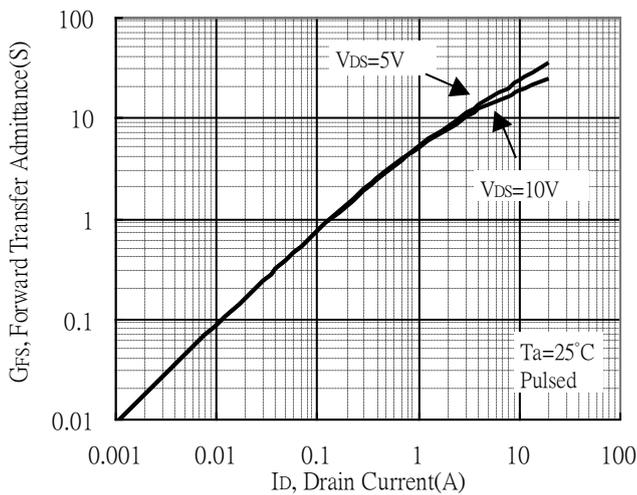
Capacitance vs Drain-to-Source Voltage



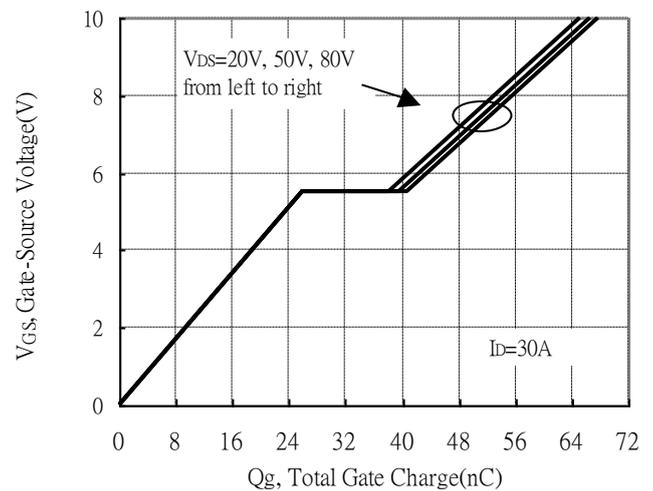
Threshold Voltage vs Junction Temperature



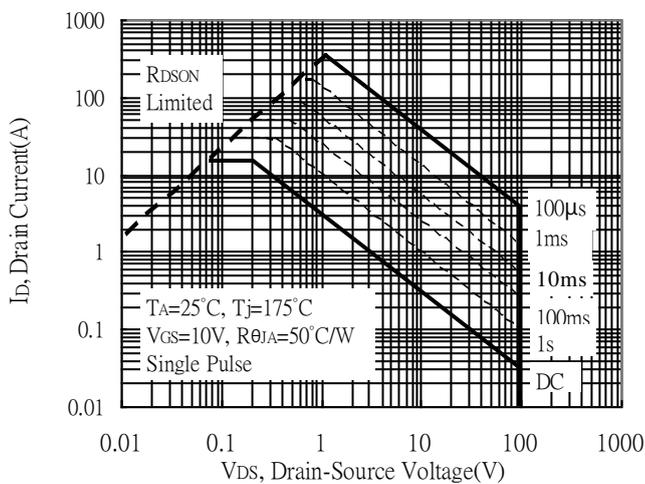
Forward Transfer Admittance vs Drain Current



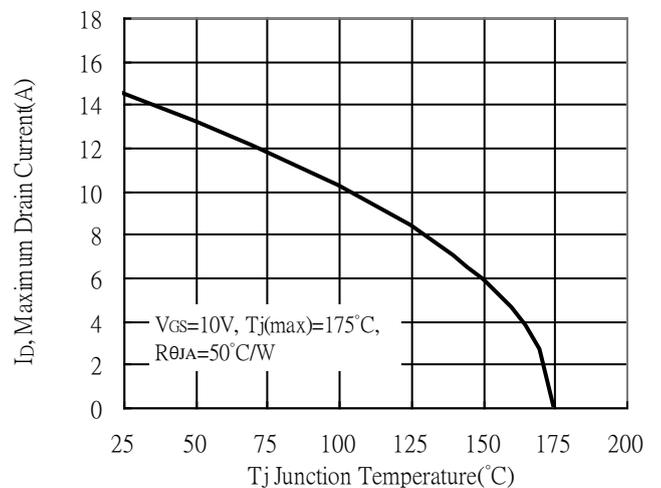
Gate Charge Characteristics



Maximum Safe Operating Area

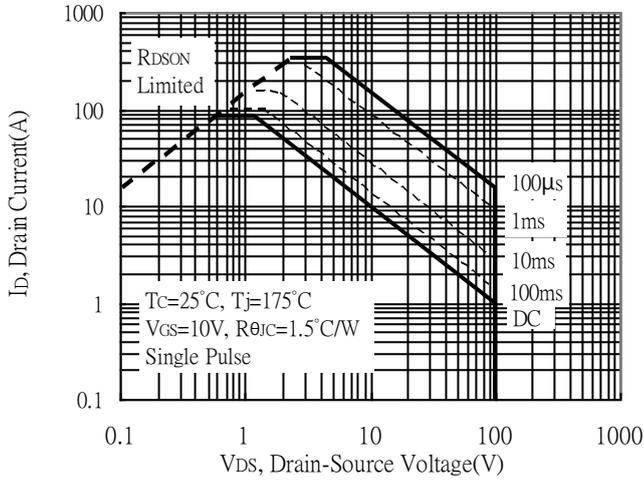


Maximum Drain Current vs Junction Temperature

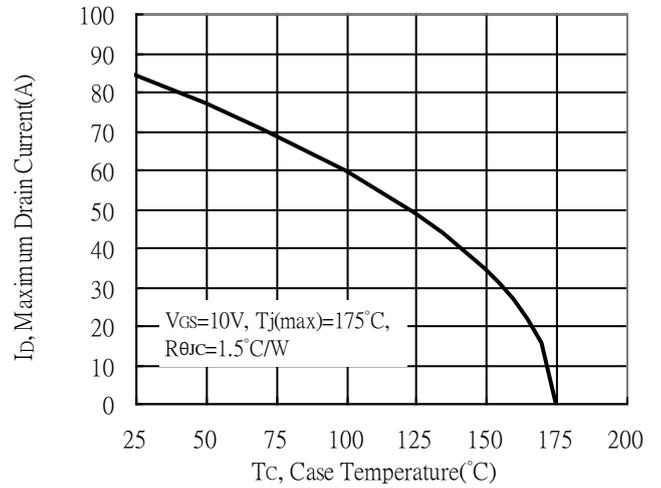


Typical Characteristics(Cont.)

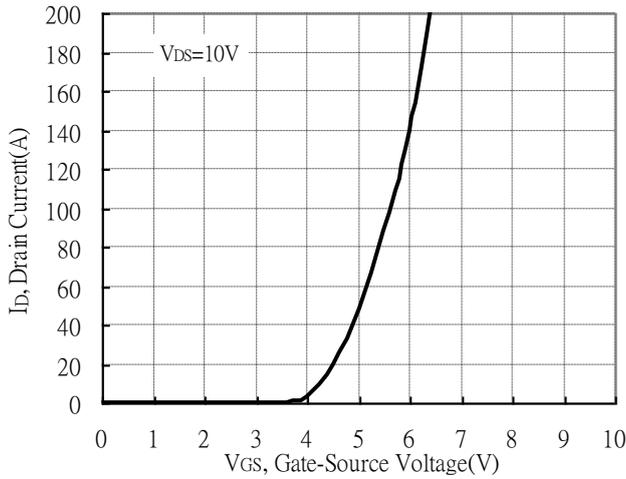
Maximum Safe Operating Area



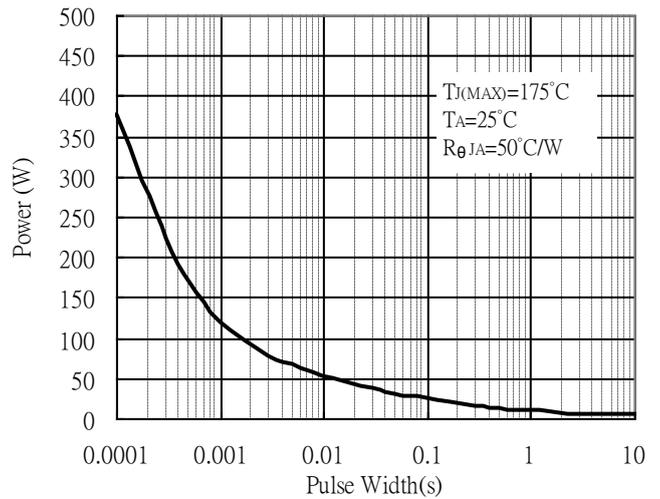
Maximum Drain Current vs Case Temperature



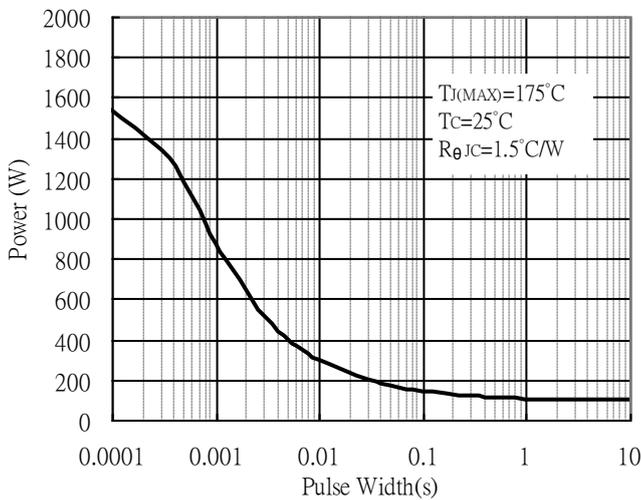
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Case

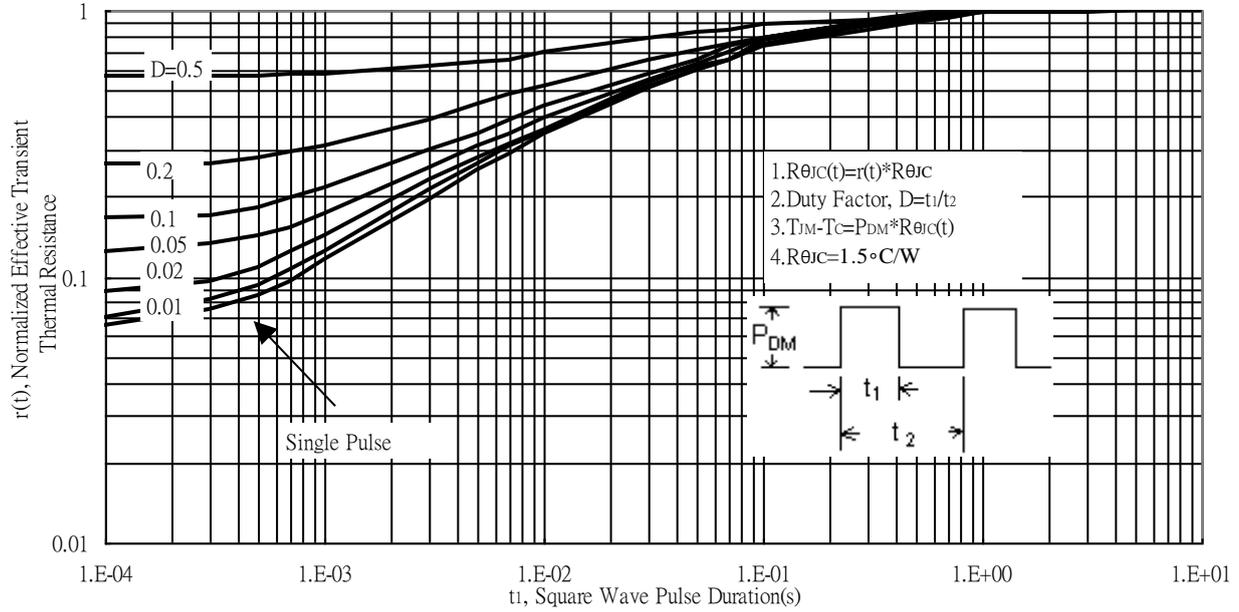


Single Pulse Power Rating, Junction to Case

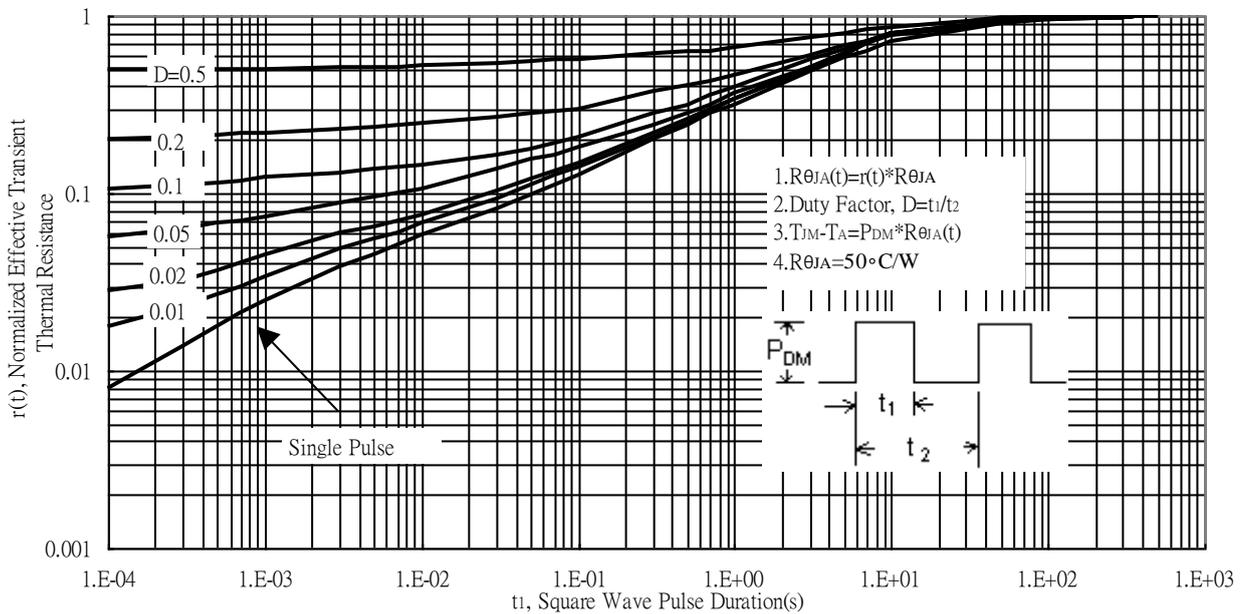


Typical Characteristics(Cont.)

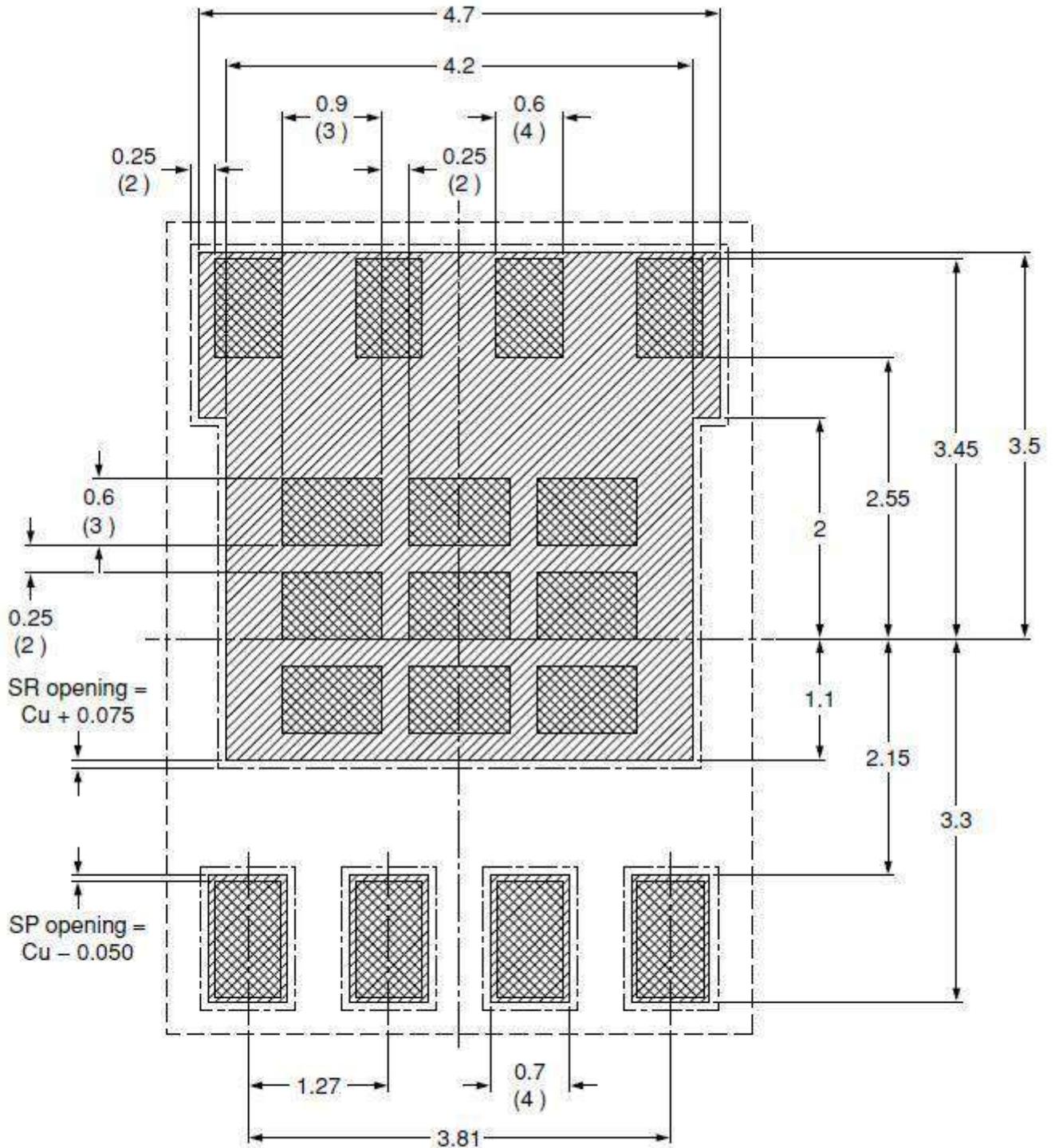
Transient Thermal Response Curves

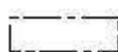
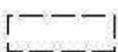


Transient Thermal Response Curves



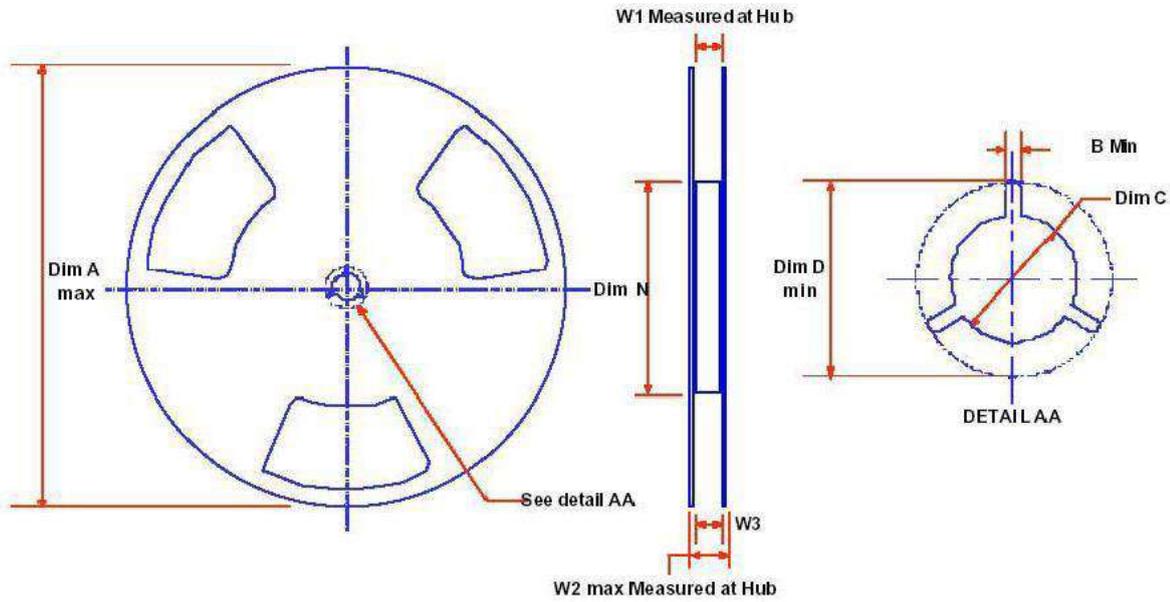
Recommended Soldering Footprint & Stencil Design



-  solder lands
-  solder paste
125 μ m stencil
-  solder resist
-  occupied area

unit : mm

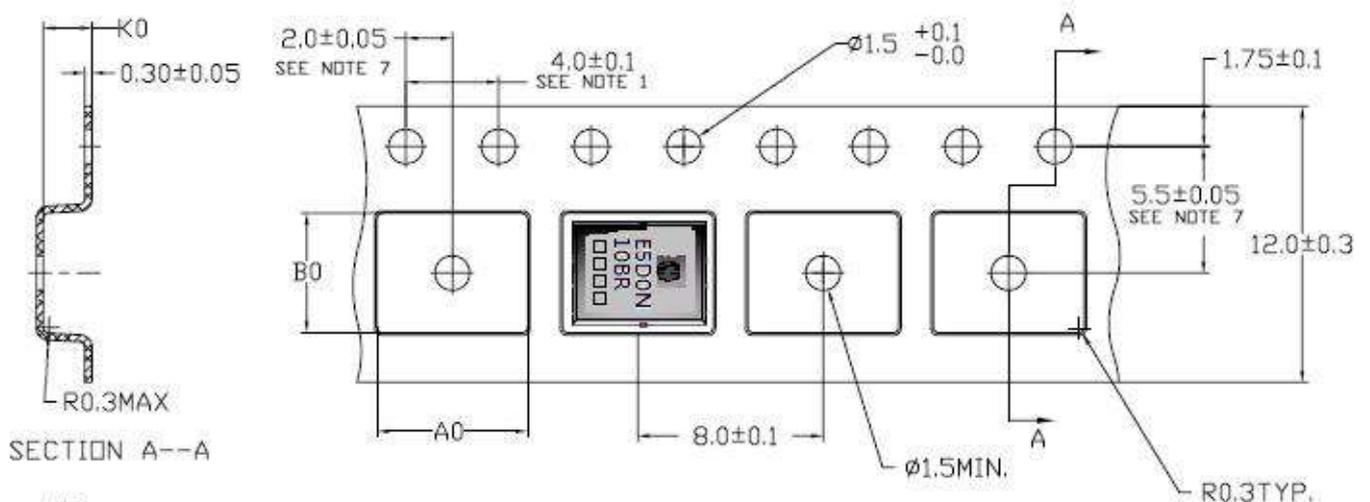
Reel Dimension



Dimensions are in inches and millimeters

Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
12mm	13" Dia (STD/L99Z)	13.00 330+/-1	0.059 1.5 Min.	0.512 13.0 Min.	0.795 20.2(ref.)	7.00 178+0/-2	0.488 +0.078/-0.000 12.4+2/0	0.724 18.4(ref.)	0.469 - 0.606 11.9 - 15.4

Carrier Tape Dimension

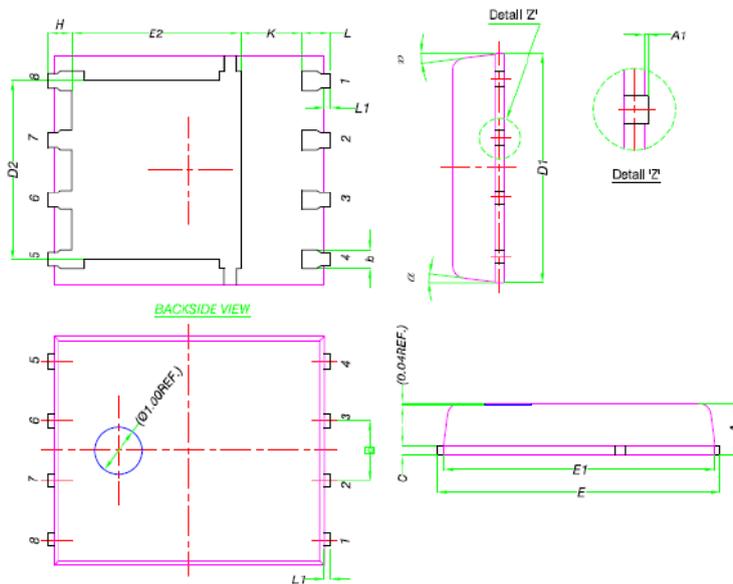


NOTE:

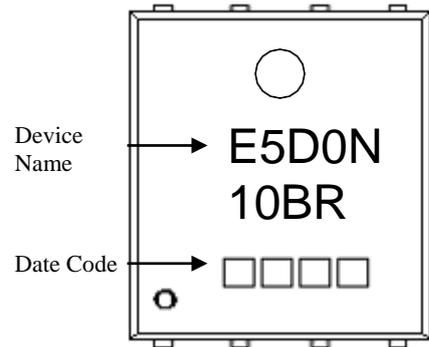
1. 10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE ±0.2
2. CAMBER NOT TO EXCEED 1mm IN 100mm, NONCUMULATIVE OVER 250mm.
3. MATERIAL: BLACK STATIC DISSIPATIVE PS.(POLYSTYRENE)
4. ALL DIMENSIONS ARE IN MILLIMETERS (UNLESS OTHERWISE SPECIFIED)
5. A0 AND B0 MEASURED ON A PLANE 0.3mm ABOVE THE BOTTOM OF THE POCKET
6. K0 MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
7. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE.
8. SURFACE RESISTIVITY
 $1 \times 10^4 - 1 \times 10^6 \Omega \text{ INHMS/SQ.}$

A0=6.5±0.1
 B0=5.3±0.1
 K0=1.4±0.1

DFN5×6 Dimension



Marking :



8-Lead DFN5x6 Plastic Package

Date Code(counting from left to right) :

1st code: year code, the last digit of Christian year

2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J, Oct→K, Nov→L, Dec→M

3rd and 4th codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.90	1.10	0.035	0.043	E2	3.38	3.78	0.133	0.149
A1	0.00	0.05	0.000	0.002	e	1.27	BSC	0.050	BSC
b	0.33	0.51	0.013	0.020	H	0.41	0.61	0.016	0.024
C	0.20	0.30	0.008	0.012	K	1.10	-	0.043	-
D1	4.80	5.00	0.189	0.197	L	0.51	0.71	0.020	0.028
D2	3.61	3.96	0.142	0.156	L1	0.06	0.20	0.002	0.008
E	5.90	6.10	0.232	0.240	θ	8°	12°	8°	12°
E1	5.70	5.80	0.224	0.228					