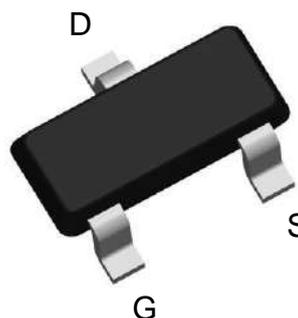


## P-Channel Enhancement Mode MOSFET

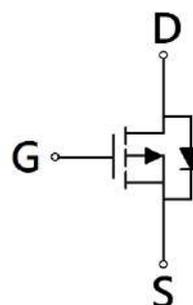
### Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

SOT-23



$BV_{DSS}$	-30V
$I_D @ V_{GS} = -10V, T_A = 25^\circ C$	-4.5A
$R_{DS(ON) \text{ typ. } @ V_{GS} = -10V, I_D = -4A}$	29m $\Omega$
$R_{DS(ON) \text{ typ. } @ V_{GS} = -4.5V, I_D = -3.5A}$	42m $\Omega$



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KNSB035P0	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

### Absolute Maximum Ratings (T<sub>A</sub>=25°C)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-30	V	
Gate-Source Voltage	V <sub>GS</sub>	±20		
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =25°C	I <sub>D</sub>	-4.5	A	
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =100°C		-3.6		
Pulsed Drain Current	I <sub>DM</sub>	-18		
Continuous Body Diode Forward Current @ T <sub>A</sub> =25°C	I <sub>S</sub>	-1		
Avalanche Current @ L=0.1mH	I <sub>AS</sub>	-12		
Avalanche Energy @ L=0.5mH	E <sub>AS</sub>	12	mJ	
Total Power Dissipation	P <sub>D</sub>	T <sub>A</sub> =25°C	1.4	W
		T <sub>A</sub> =70°C	0.9	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C	

### Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	R <sub>θJA</sub>	90	°C/W

Note:

\*a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR -4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

\*b. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.

**Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-1	-	-2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
G <sub>FS</sub>	-	6.5	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-4A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V
R <sub>DS(ON)</sub>	-	29	38	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A
	-	42	59		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.5A
<b>Dynamic</b>					
C <sub>iss</sub>	-	925	-	pF	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	110	-		
C <sub>rss</sub>	-	90	-		
R <sub>g</sub>	-	22	-	Ω	f=1MHz
Q <sub>g</sub> *1, 2	-	21	-	nC	V <sub>DS</sub> =-15V, I <sub>D</sub> =-4A, V <sub>GS</sub> =-10V
Q <sub>gs</sub> *1, 2	-	3.6	-		
Q <sub>gd</sub> *1, 2	-	4.5	-		
t <sub>d(ON)</sub> *1, 2	-	6.8	-	ns	V <sub>DS</sub> =-15V, I <sub>D</sub> =-4A, V <sub>GS</sub> =-10V, R <sub>GS</sub> =6Ω
t <sub>r</sub> *1, 2	-	17	-		
t <sub>d(OFF)</sub> *1, 2	-	94	-		
t <sub>f</sub> *1, 2	-	48	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *1	-	-0.83	-1.2	V	I <sub>S</sub> =-4A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	9.5	-	ns	I <sub>F</sub> =-4A, dI <sub>F</sub> /dt=100A/μs
Q <sub>rr</sub>	-	4.5	-	nC	

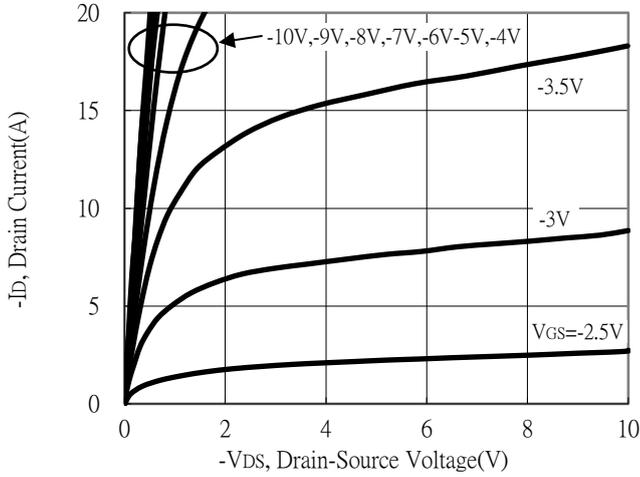
Note:

\*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

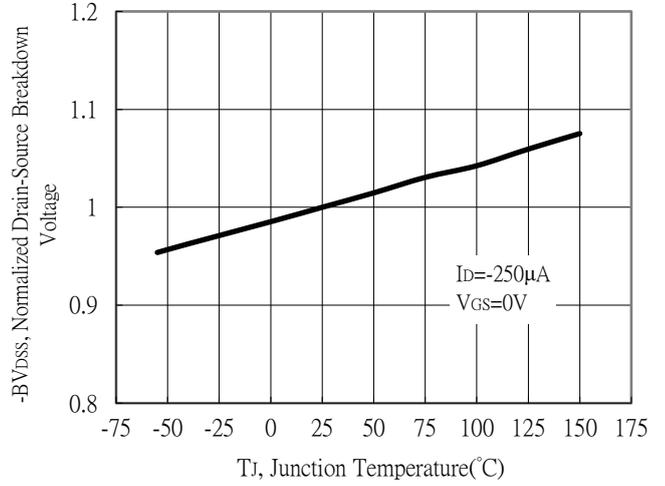
\*2. Independent of operating temperature

### Typical Characteristics

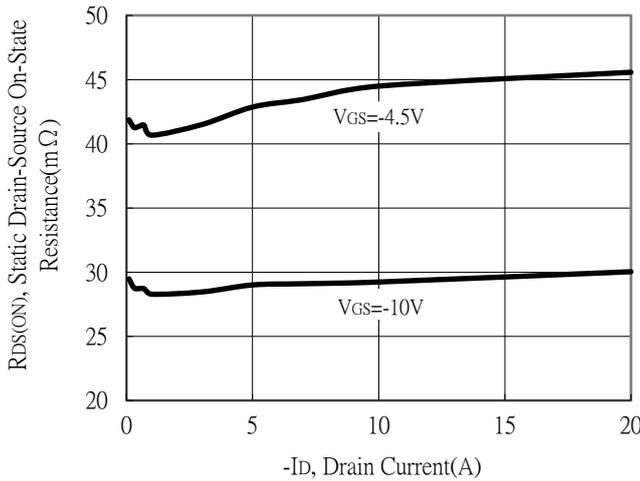
Typical Output Characteristics



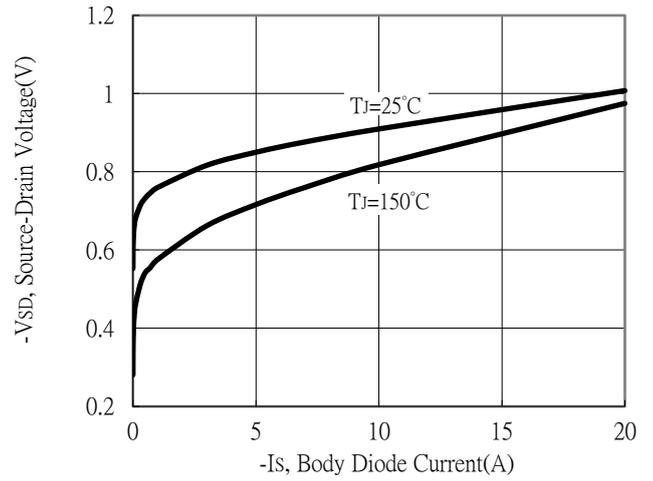
Breakdown Voltage vs Ambient Temperature



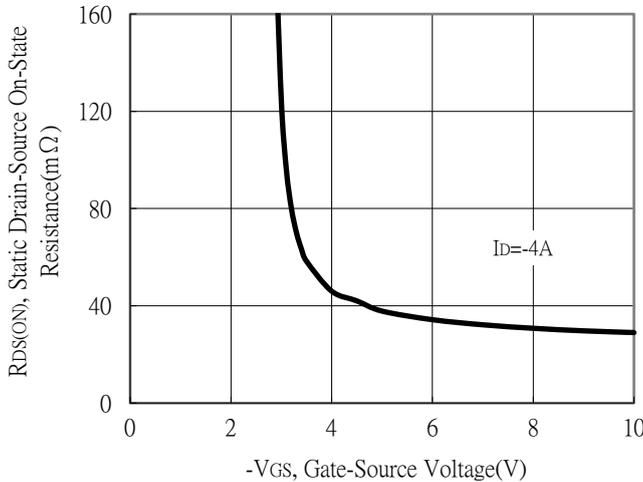
Static Drain-Source On-State resistance vs Drain Current



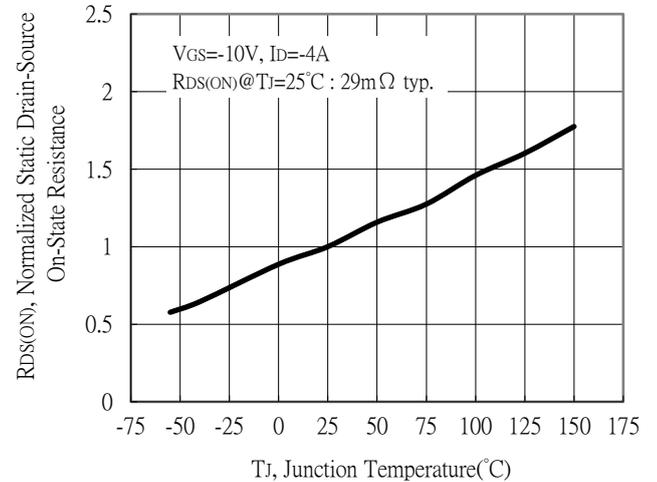
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

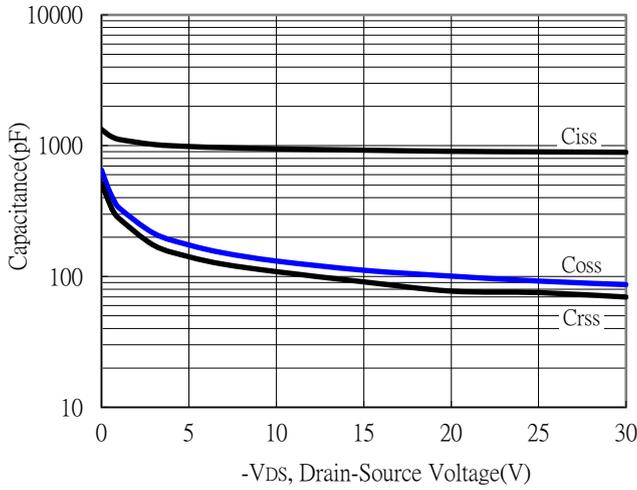


Drain-Source On-State Resistance vs Junction Temperature

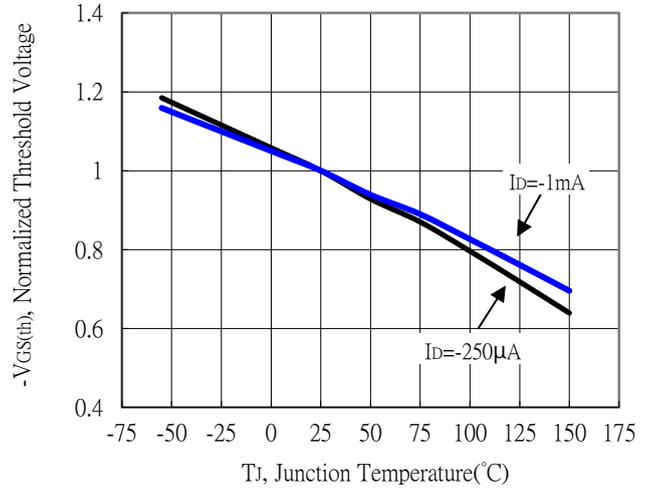


**Typical Characteristics (Cont.)**

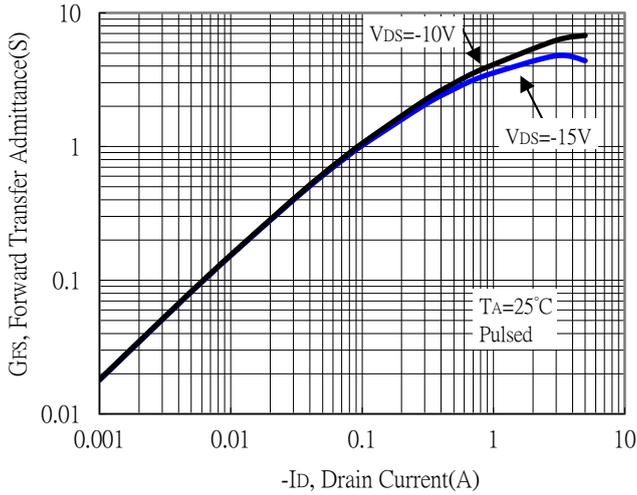
Capacitance vs Drain-to-Source Voltage



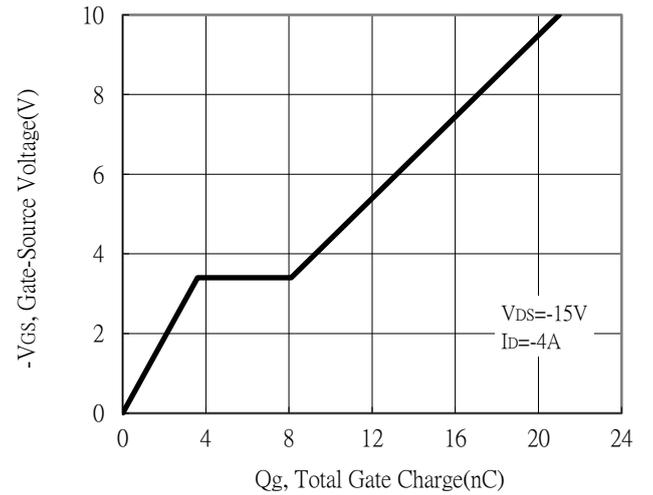
Threshold Voltage vs Junction Temperature



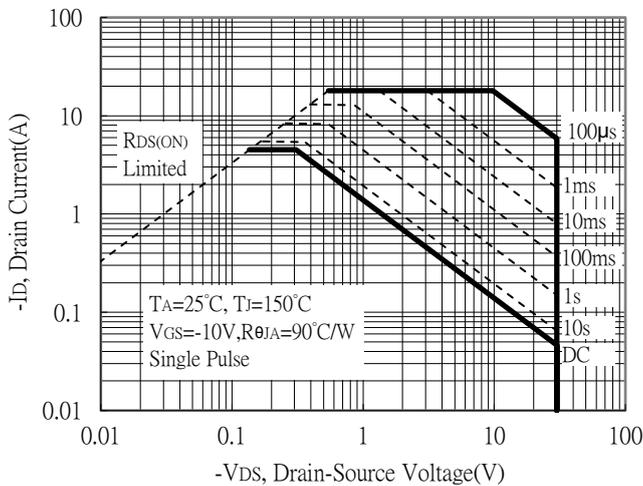
Forward Transfer Admittance vs Drain Current



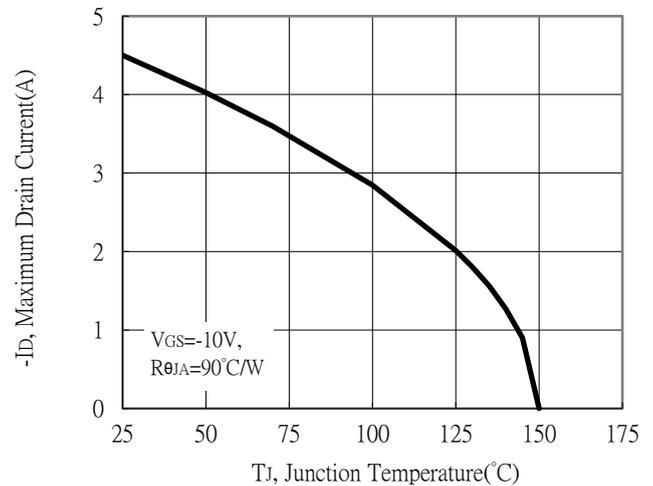
Gate Charge Characteristics



Maximum Safe Operating Area

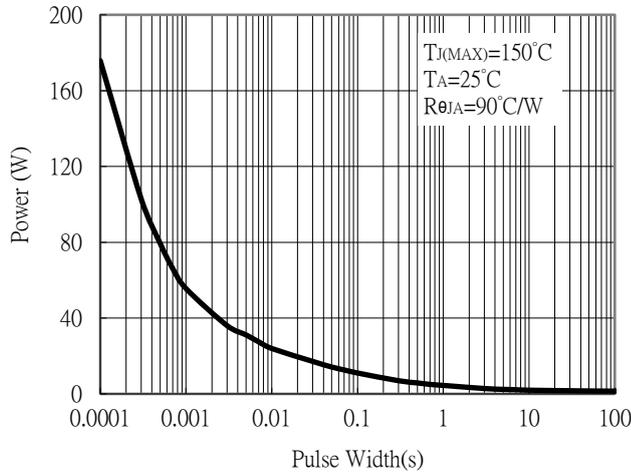


Maximum Drain Current vs Junction Temperature

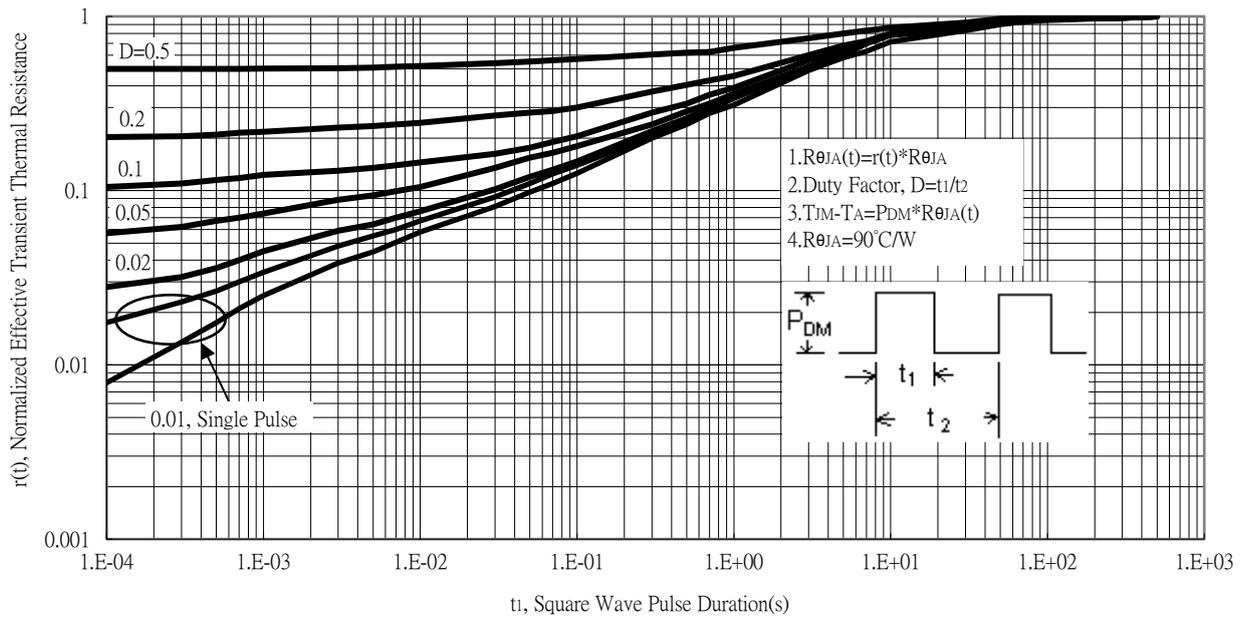


### Typical Characteristics (Cont.)

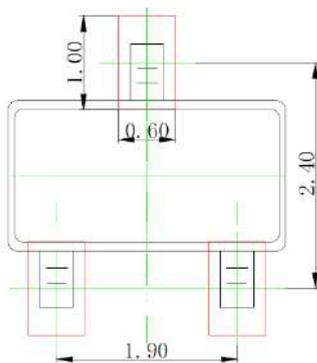
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves

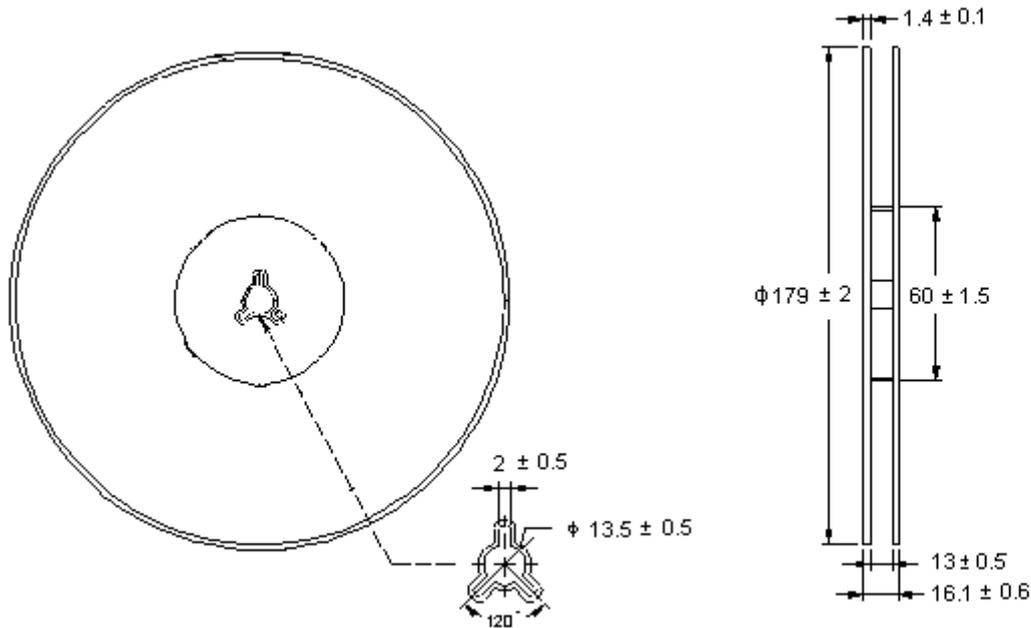


### Recommended Soldering Footprint



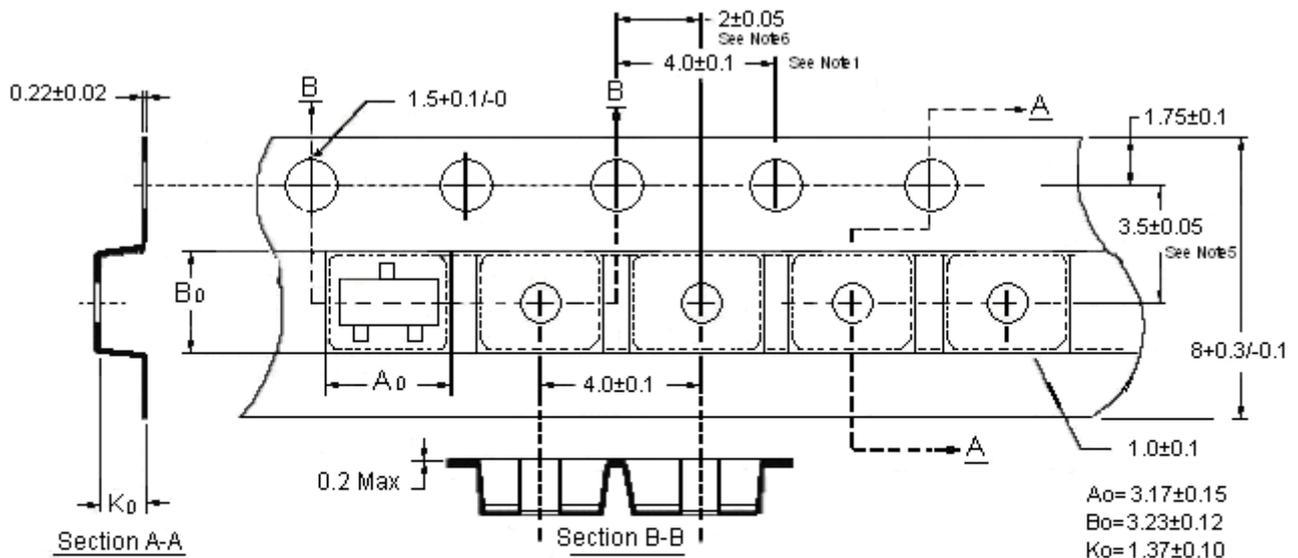
Unit : mm

## Reel Dimension



Unit: millimeter

## Carrier Tape Dimension

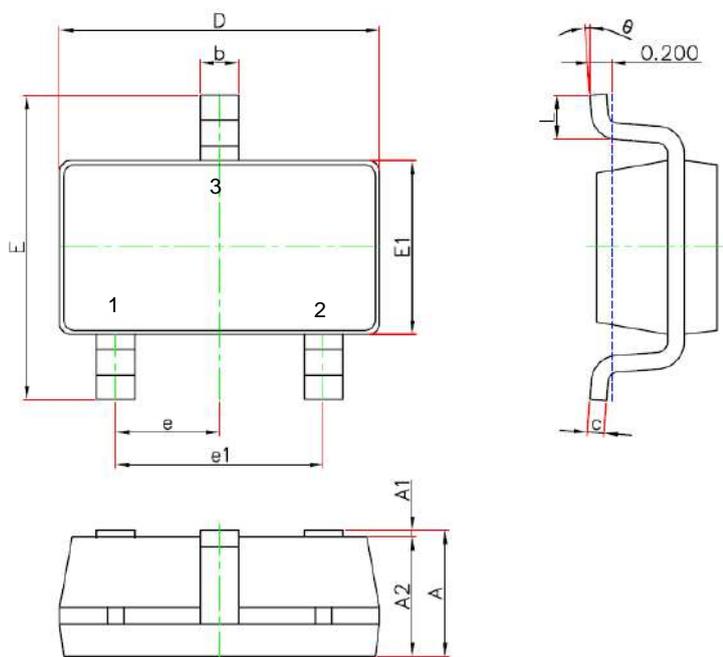


### Notes:

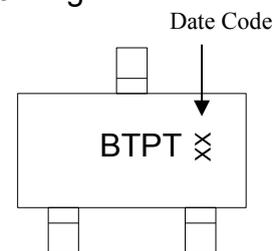
1. 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$ .
2. Camber not to exceed 1mm in 100mm.
3. Material : conductive Black Polystyrene.
4.  $A_0$  &  $B_0$  measured on a plane 0.3mm above the bottom of the pocket.
5.  $K_0$  measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Unit : millimeter

**SOT-23 Dimension**



**Marking:**



Style: Pin 1.Gate 2.Source 3.Drain

Date Code: Year+Month  
 Year: 3→2003, 4→2004  
 Month: 1→1, 2→2, . . .  
 9→9, A→10, B→11, C→12

3-Lead SOT-23 Plastic  
 Surface Mounted Package

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.041	0.049	1.05	1.25	E1	0.059	0.067	1.50	1.70
A1	0.000	0.004	0.00	0.10	E	0.104	0.116	2.65	2.95
A2	0.041	0.045	1.05	1.15	e	0.037 BSC		0.95 BSC	
b	0.012	0.020	0.30	0.50	e1	0.071	0.079	1.80	2.00
c	0.004	0.008	0.10	0.20	L	0.012	0.024	0.30	0.60
D	0.111	0.119	2.82	3.02	θ	0°	8°	0°	8°