

N-Ch 30V Fast Switching MOSFETs

Features:

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



Description:

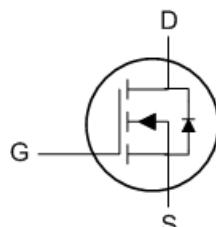
SOP8 Pin Configuration

The KSCS3018B is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The KSCS3018B meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Product Summary

BVDSS	RDS(on)	ID
30V	3mΩ	24A



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _c =25°C	Continuous Drain Current ¹	24	A
I _D @T _c =100°C	Continuous Drain Current ¹	15	A
I _{DM}	Pulsed Drain Current ²	175	A
EAS	Single Pulse Avalanche Energy ³	80	mJ
I _{AS}	Avalanche Current	40	A
P _D @T _c =25°C	Total Power Dissipation ⁴	3.1	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹ (t≤10S)	---	40	°C/W
	Thermal Resistance Junction-ambient ¹ (Steady State)	---	75	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10\text{V}$, $I_D=20\text{A}$	---	2.4	3	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=15\text{A}$	---	3	3.8	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	uA
		$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=55\text{ }^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=20\text{A}$	---	105	---	S
R_g	Gate Resistance	$V_{DS}=0\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	1.5	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=15\text{V}$, $V_{GS}=10\text{V}$, $I_D=15\text{A}$	---	56.9	---	nC
Q_{gs}	Gate-Source Charge		---	13.8	---	
Q_{gd}	Gate-Drain Charge		---	23.5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=15\text{V}$, $V_{GS}=10\text{V}$, $R_G=3.3\Omega$, $I_D=1\text{A}$	---	20.1	---	ns
T_r	Rise Time		---	6.3	---	
$T_{d(off)}$	Turn-Off Delay Time		---	124.6	---	
T_f	Fall Time		---	15.8	---	
C_{iss}	Input Capacitance	$V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	4345	---	pF
C_{oss}	Output Capacitance		---	340	---	
C_{rss}	Reverse Transfer Capacitance		---	225	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ¹	$V_G=V_D=0\text{V}$, Force Current	---	---	24	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=40\text{A}$
- 4.The power dissipation is limited by $150\text{ }^{\circ}\text{C}$ junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

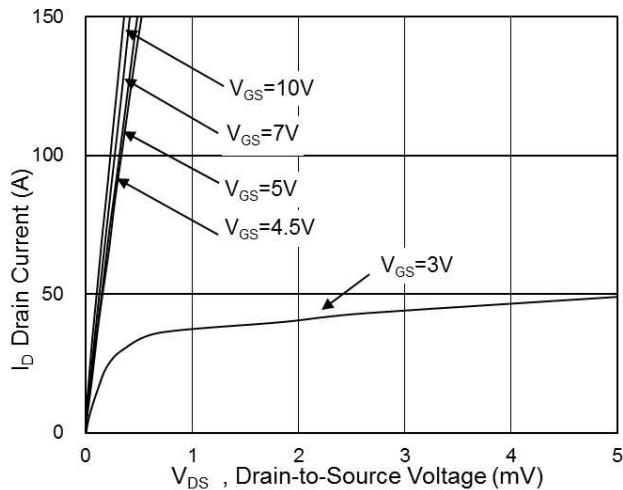


Fig.1 Typical Output Characteristics

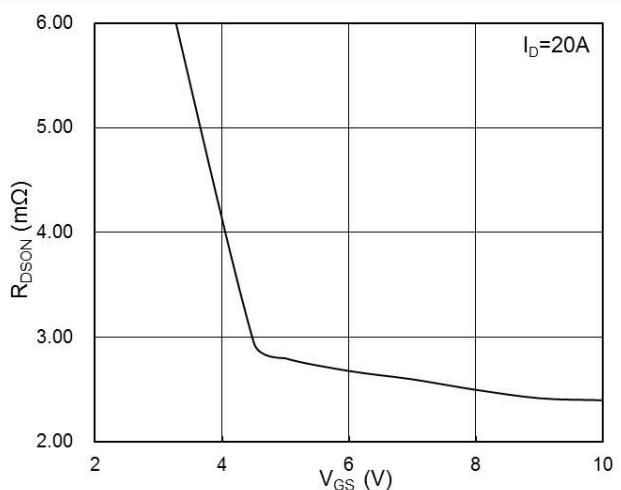


Fig.2 On-Resistance vs. Gate-Source Voltage

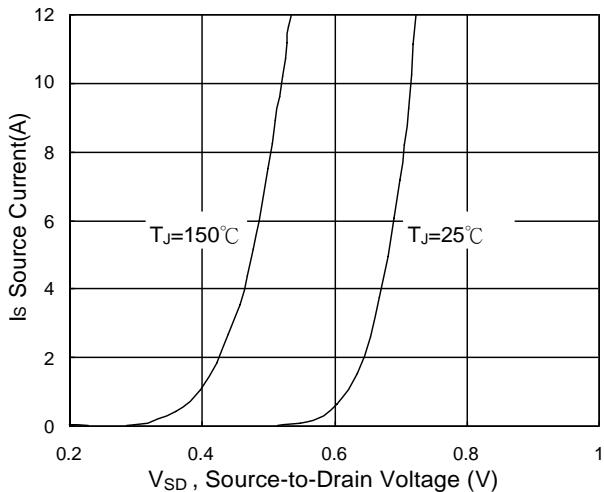


Fig.3 Forward Characteristics of Reverse

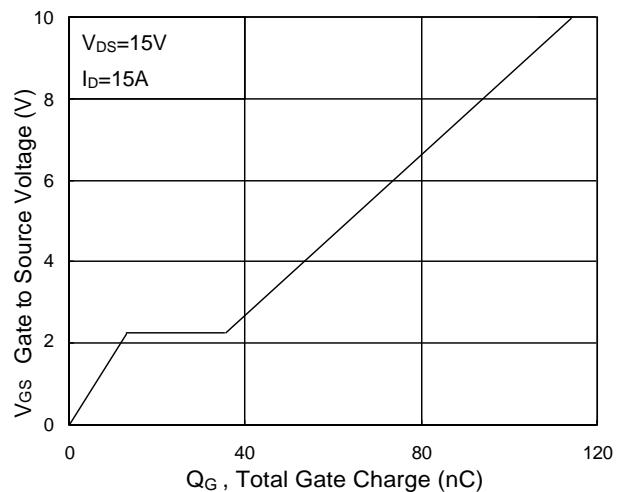


Fig.4 Gate-Charge Characteristics

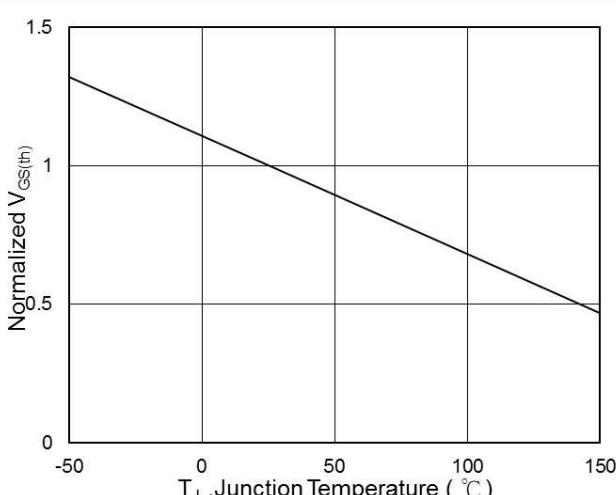


Fig.5 Normalized V_{Gs(th)} vs. T_J

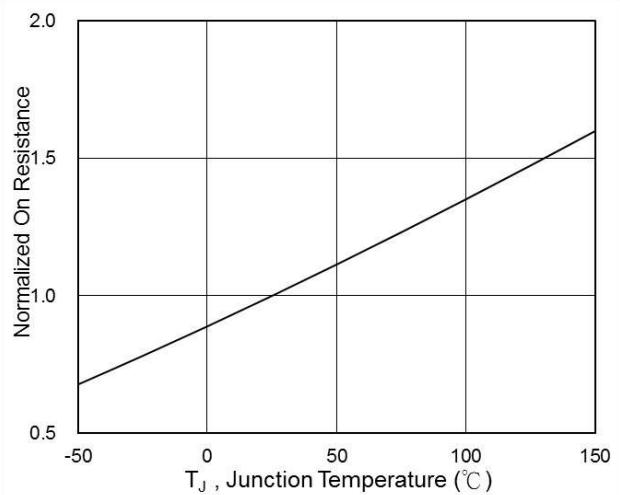


Fig.6 Normalized R_{DS(on)} vs. T_J

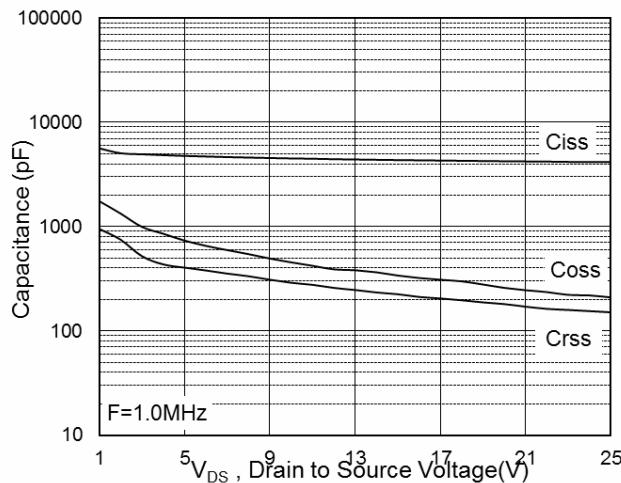


Fig.7 Capacitance

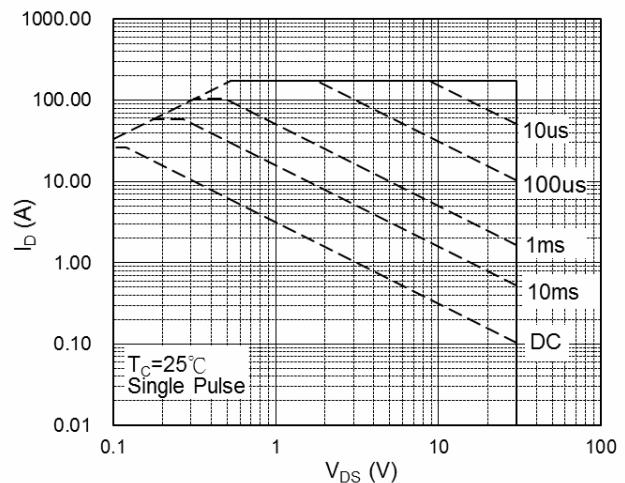


Fig.8 Safe Operating Area

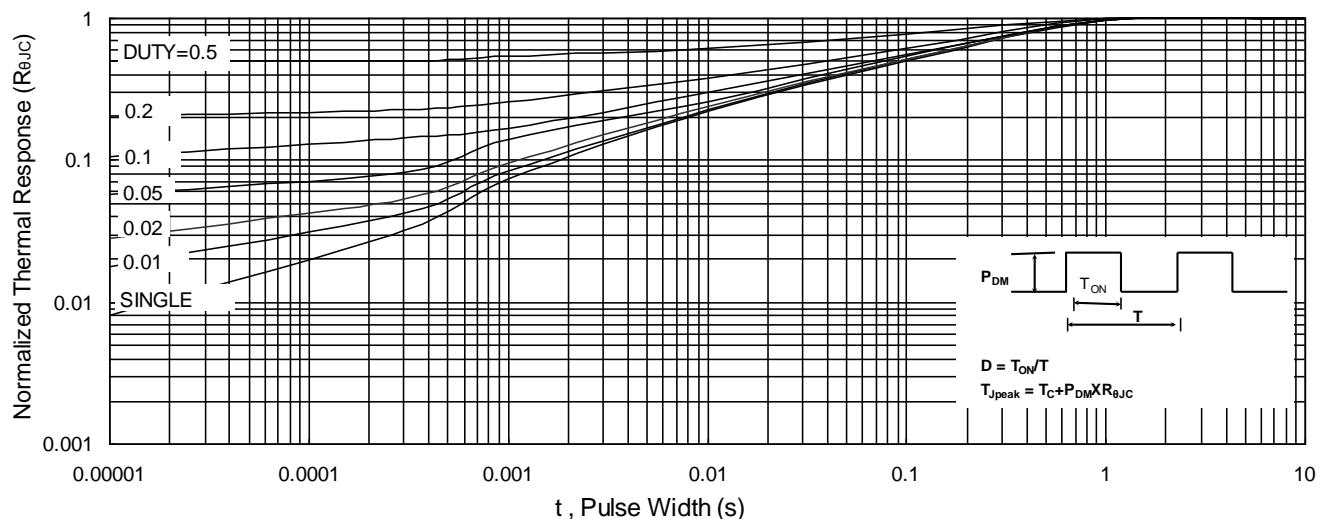


Fig.9 Normalized Maximum Transient Thermal Impedance

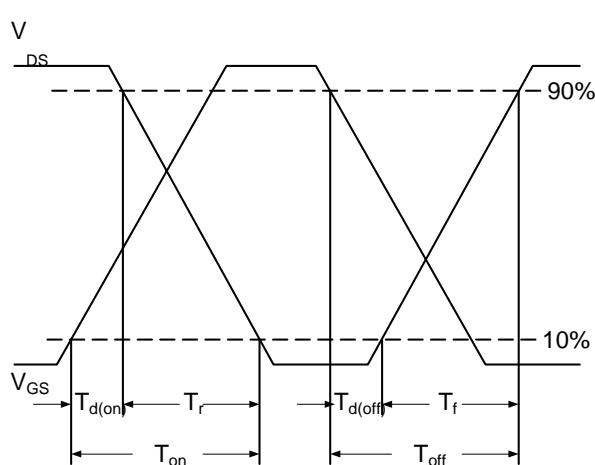


Fig.10 Switching Time Waveform

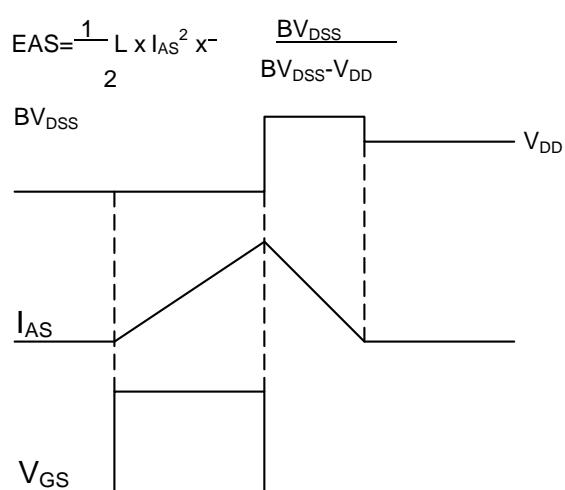


Fig.11 Unclamped Inductive Switching Waveform