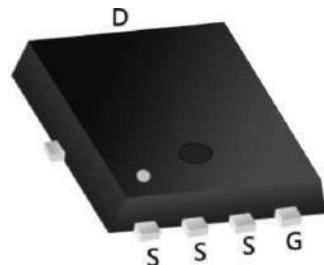


N-Ch 80V Fast Switching MOSFETs

Features:

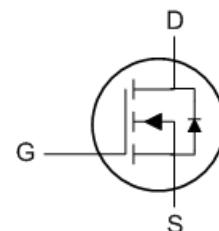
- ★ Advanced Trench MOS Technology
- ★ 100% EAS Guaranteed
- ★ Fast Switching Speed
- ★ Green Device Available



Application:

PRPAK5X6 Pin Configuration

- ★ High Frequency Switching and Synchronous Rectification.
- ★ DC/DC Converter.
- ★ Motor Drivers.



Product Summary

BVDSS	RDS(on)	ID
80V	8.7mΩ	60A

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	80	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	60	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	38	A
I _{DM}	Pulsed Drain Current ²	200	A
EAS	Single Pulse Avalanche Energy ³	45	mJ
I _{AS}	Avalanche Current	30	A
P _D @T _C =25°C	Total Power Dissipation ⁴	52	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}		---	75	°C/W
R _{θJC}	Thermal Resistance Junction-case ¹	---	2.4	°C/W

Electrical Characteristics ($T_J=25^\circ C$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	80	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=10A$	---	7.2	8.7	$m\Omega$
		$V_{GS}=4.5V, I_D=10A$	---	10.5	13	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	---	2.3	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=64V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	μA
		$V_{DS}=64V, V_{GS}=0V, T_J=55^\circ C$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	1.5	---	Ω
Q_g	Total Gate Charge (10V)	$V_{DS}=40V, V_{GS}=10V, I_D=10A$	---	29	---	nC
Q_{gs}	Gate-Source Charge		---	7.7	---	
Q_{gd}	Gate-Drain Charge		---	5.3	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=40V, V_{GS}=10V, R_G=3.3\Omega, I_D=10A$	---	6.2	---	ns
T_r	Rise Time		---	19	---	
$T_{d(off)}$	Turn-Off Delay Time		---	9.4	---	
T_f	Fall Time		---	36	---	
C_{iss}	Input Capacitance	$V_{DS}=40V, V_{GS}=0V, f=1MHz$	---	1738	---	pF
C_{oss}	Output Capacitance		---	317	---	
C_{rss}	Reverse Transfer Capacitance		---	12	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	30	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_s=1A, T_J=25^\circ C$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10A, dI/dt=100A/\mu s, T_J=25^\circ C$	---	35	---	nS
Q_{rr}	Reverse Recovery Charge		---	62	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=50V, V_{GS}=10V, L=0.1mH, I_{AS}=30A$
- 4.The power dissipation is limited by $150^\circ C$ junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

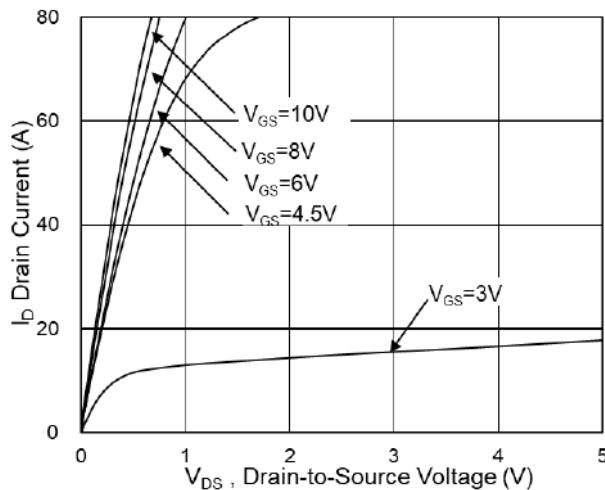


Fig.1 Typical Output Characteristics

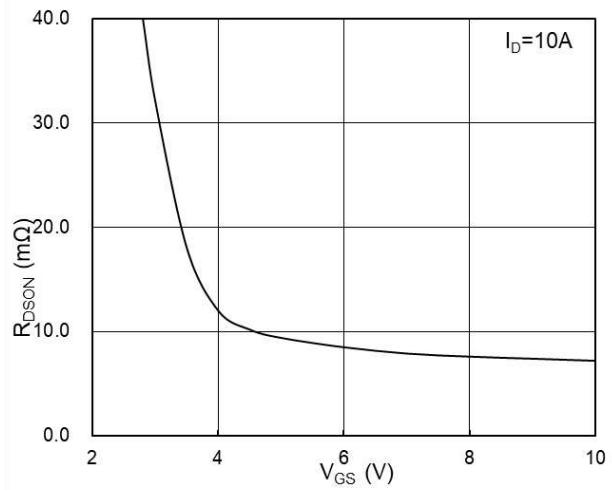


Fig.2 On-Resistance vs G-S Voltage

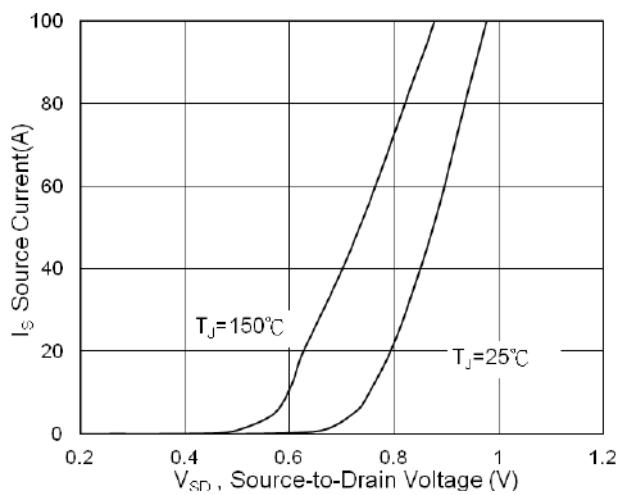


Fig.3 Source Drain Forward Characteristics

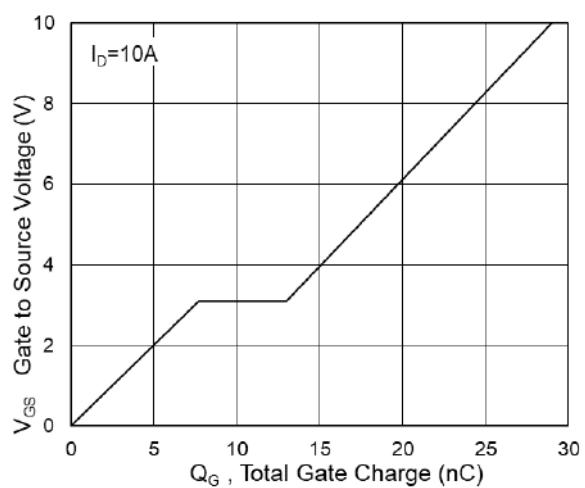


Fig.4 Gate-Charge Characteristics

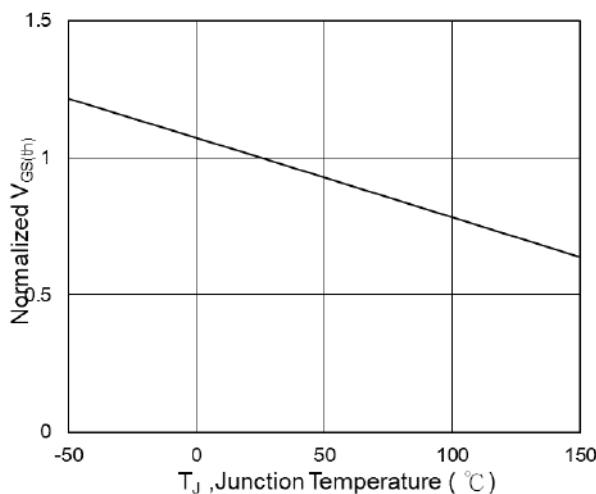


Fig.5 Normalized $V_{GS(th)}$ vs T_J

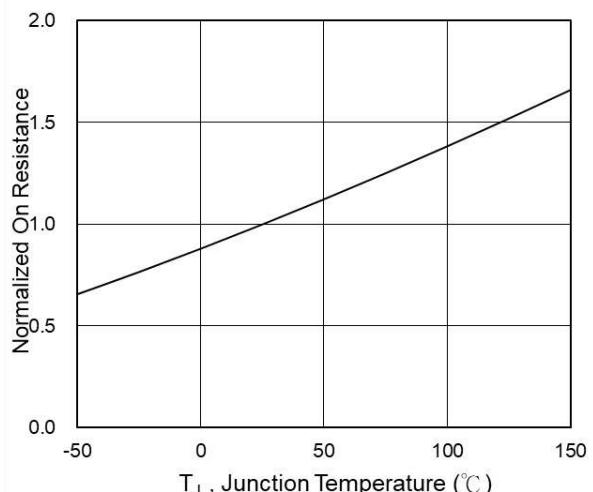


Fig.6 Normalized $R_{DS(on)}$ vs T_J

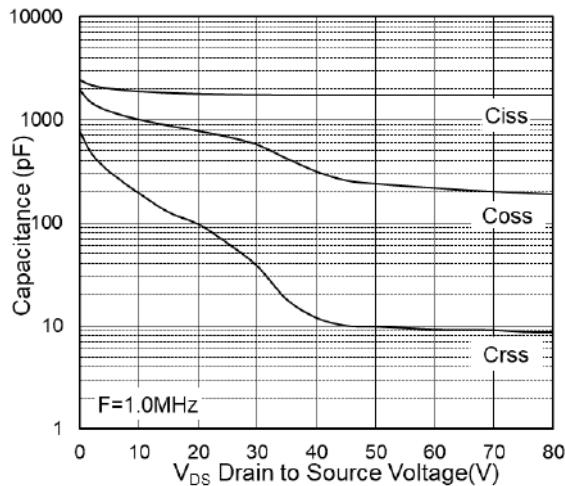


Fig.7 Capacitance

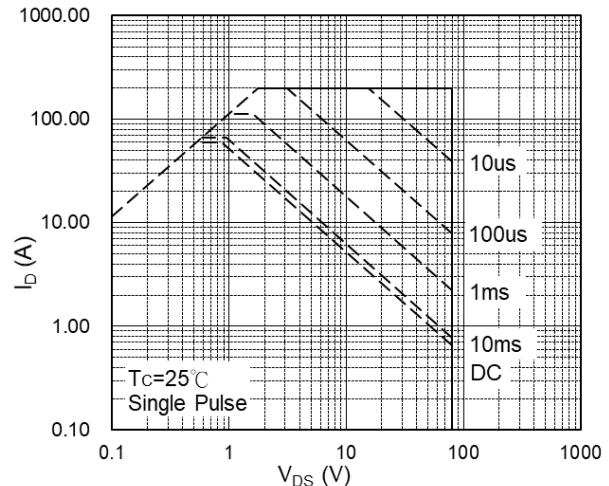


Fig.8 Safe Operating Area

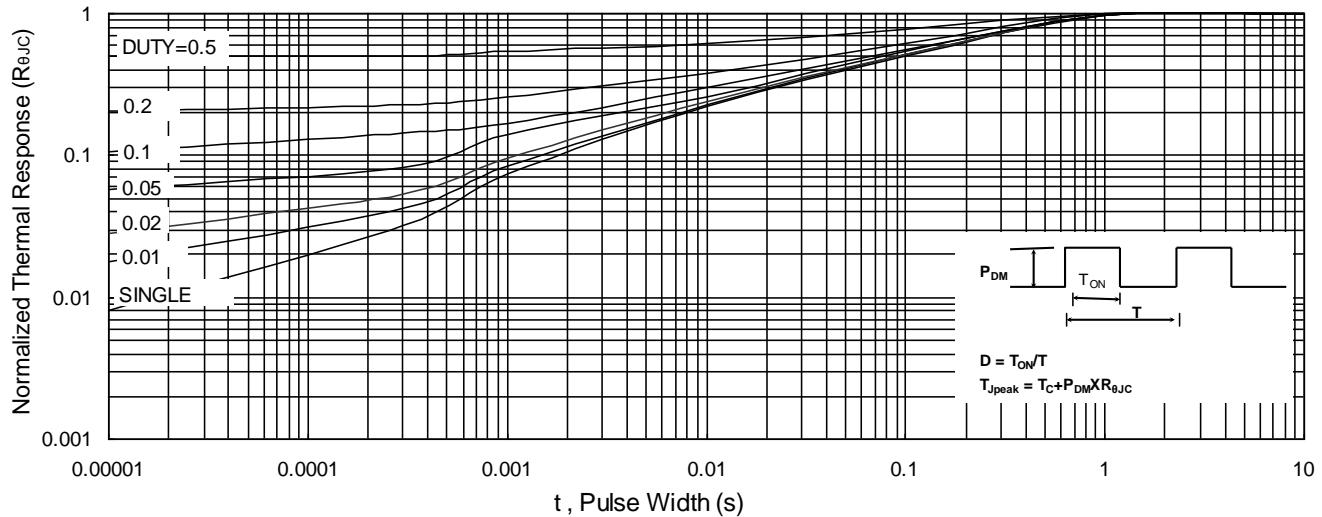


Fig.9 Normalized Maximum Transient Thermal Impedance

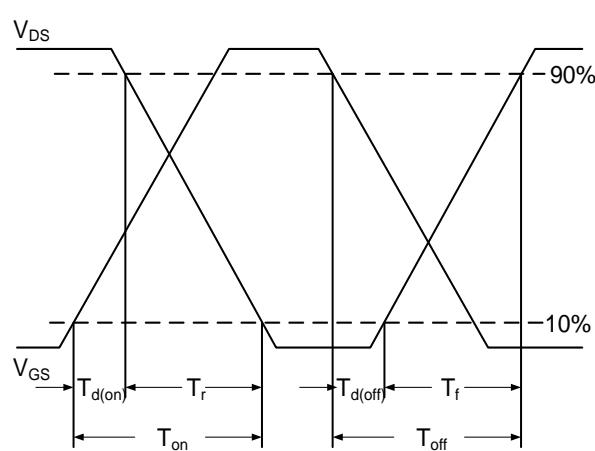


Fig.10 Switching Time Waveform

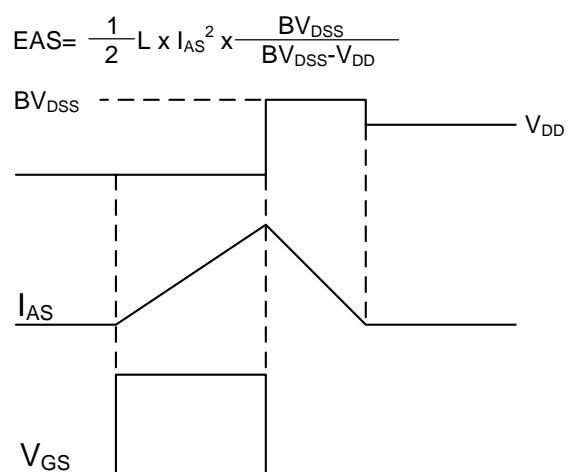


Fig.11 Unclamped Inductive Switching Waveform