

P-Ch 30V Fast Switching MOSFETs

Features:

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



Description:

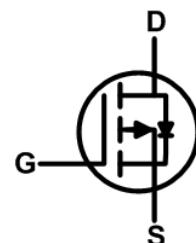
The KNSC3101 is the high cell density trenched P-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

SOT23S Pin Configuration

The KNSC3101 meet the RoHS and Green Product requirement, with full function reliability approved.

Product Summary

BVDSS	RDS _{ON}	ID
-30V	52mΩ	-3.5A



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		10s	Steady State	
V _{DS}	Drain-Source Voltage	-30		V
V _{GS}	Gate-Source Voltage	±20		V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-4.0	-3.5	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -10V ¹	-3.3	-3.0	A
I _{DM}	Pulsed Drain Current ²	-17		A
P _D @T _A =25°C	Total Power Dissipation ³	1.32	1	W
P _D @T _A =70°C	Total Power Dissipation ³	0.84	0.64	W
T _{STG}	Storage Temperature Range	-55 to 150		°C
T _J	Operating Junction Temperature Range	-55 to 150		°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	125	°C/W
R _{θJA}	Thermal Resistance Junction-Ambient ¹ (t ≤ 10s)	---	95	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	80	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
$\text{R}_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-3\text{A}$	---	42	52	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-2\text{A}$	---	75	90	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	uA
		$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	-5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-3\text{A}$	---	11	---	S
Q_{g}	Total Gate Charge (-4.5V)	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-3\text{A}$	---	6.4	9.0	nC
Q_{gs}	Gate-Source Charge		---	2.3	3.2	
Q_{gd}	Gate-Drain Charge		---	1.9	2.7	
$\text{T}_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_{\text{G}}=3.3\Omega$, $I_{\text{D}}=-3\text{A}$	---	2.8	5.6	ns
T_r	Rise Time		---	8.4	15.1	
$\text{T}_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	39	78.0	
T_f	Fall Time		---	6	12.0	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	583	816	pF
C_{oss}	Output Capacitance		---	100	140	
C_{rss}	Reverse Transfer Capacitance		---	80	112	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_{s}	Continuous Source Current ^{1,4}	$V_{\text{G}}=V_{\text{D}}=0\text{V}$, Force Current	---	---	-3	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $\text{I}_{\text{s}}=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_{D} and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

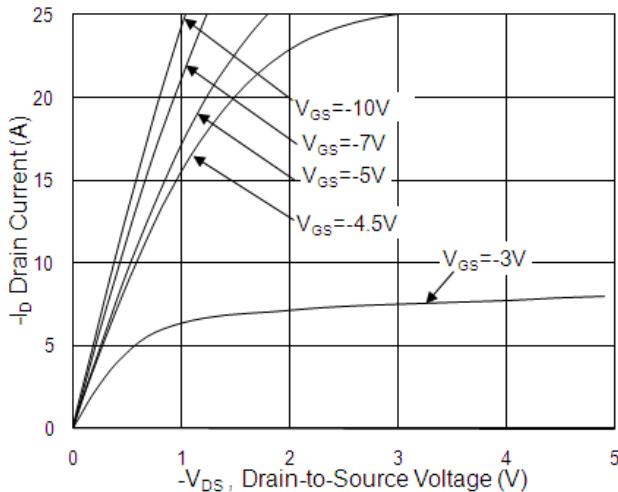


Fig.1 Typical Output Characteristics

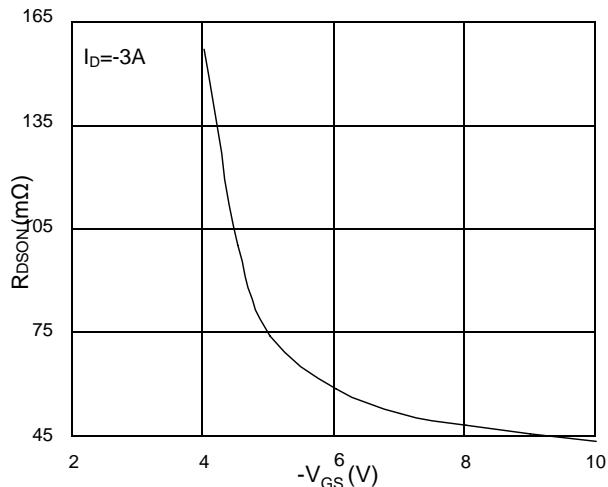


Fig.2 On-Resistance vs G-S Voltage

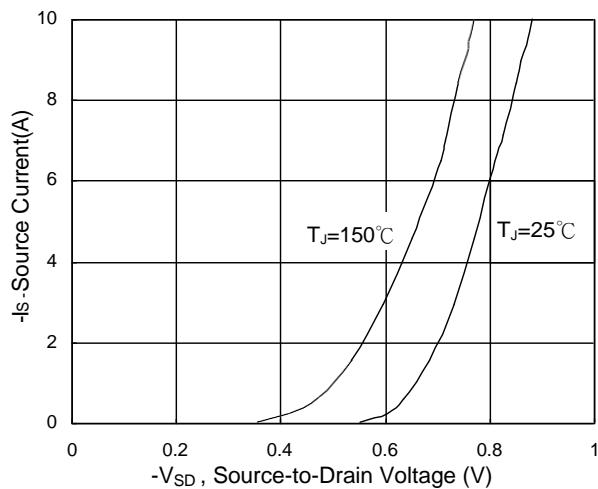


Fig.3 Source Drain Forward Characteristics

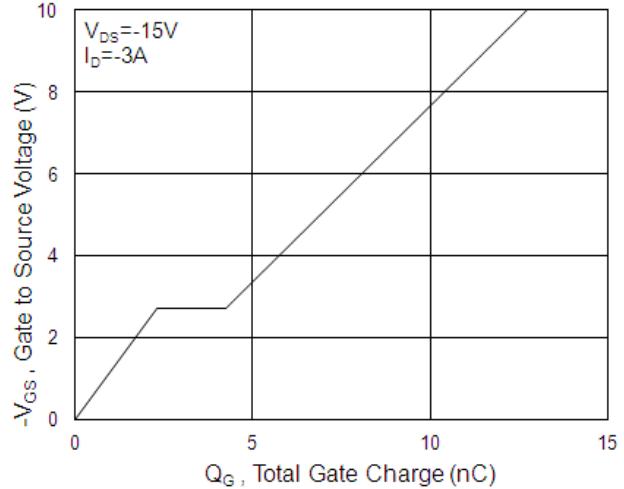


Fig.4 Gate-Charge Characteristics

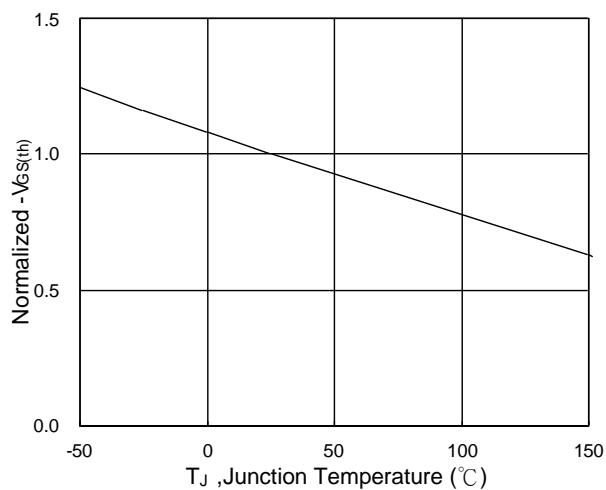


Fig.5 Normalized $V_{GS(th)}$ vs T_J

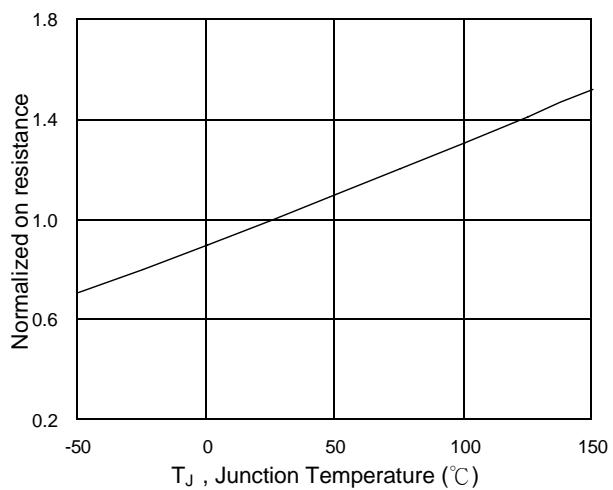


Fig.6 Normalized $R_{DS(on)}$ vs T_J

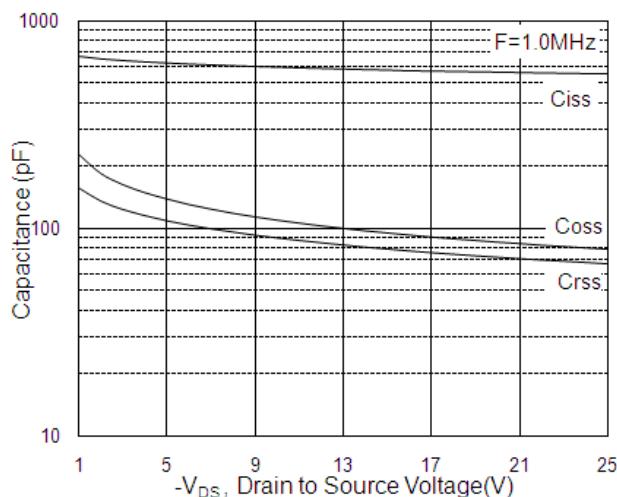


Fig.7 Capacitance

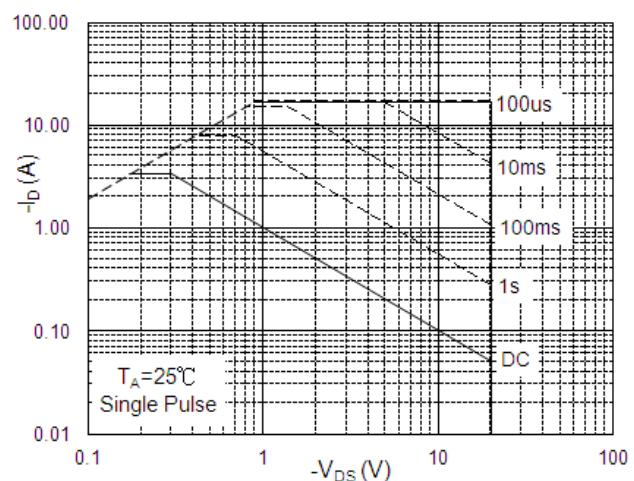


Fig.8 Safe Operating Area

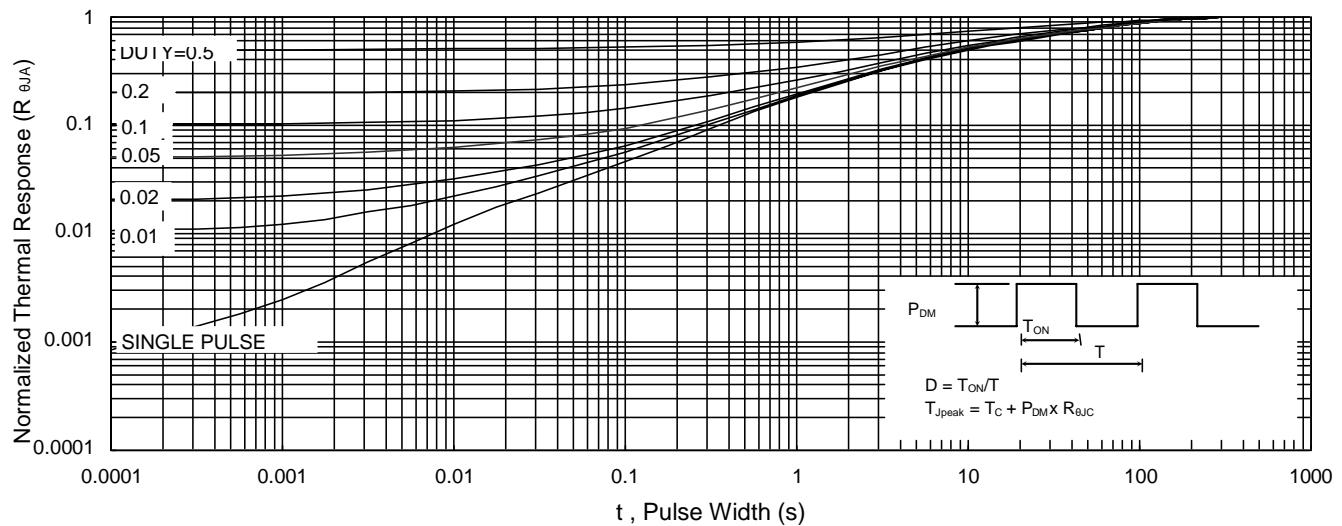


Fig.9 Normalized Maximum Transient Thermal Impedance

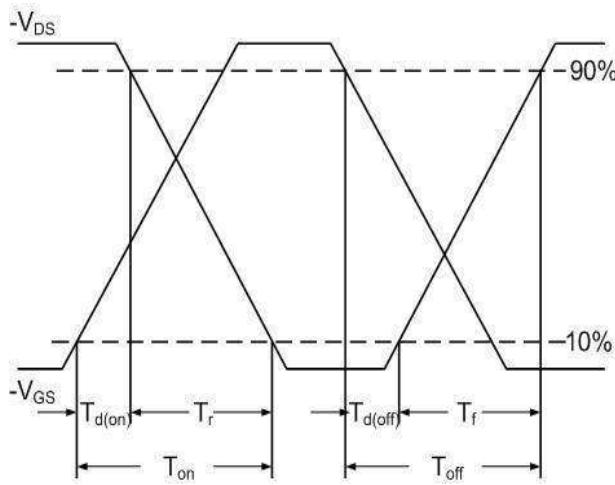


Fig.10 Switching Time Waveform

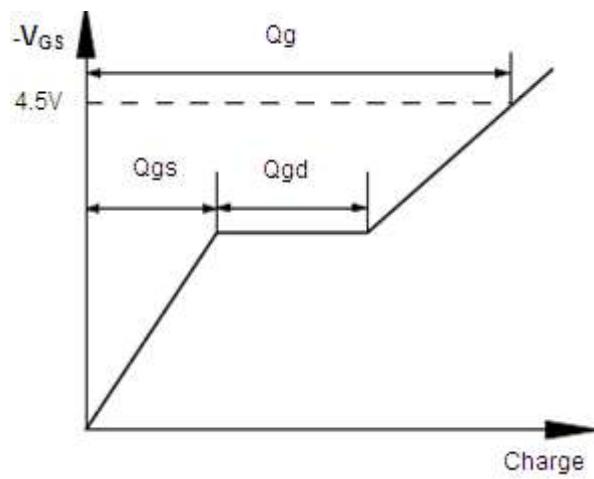
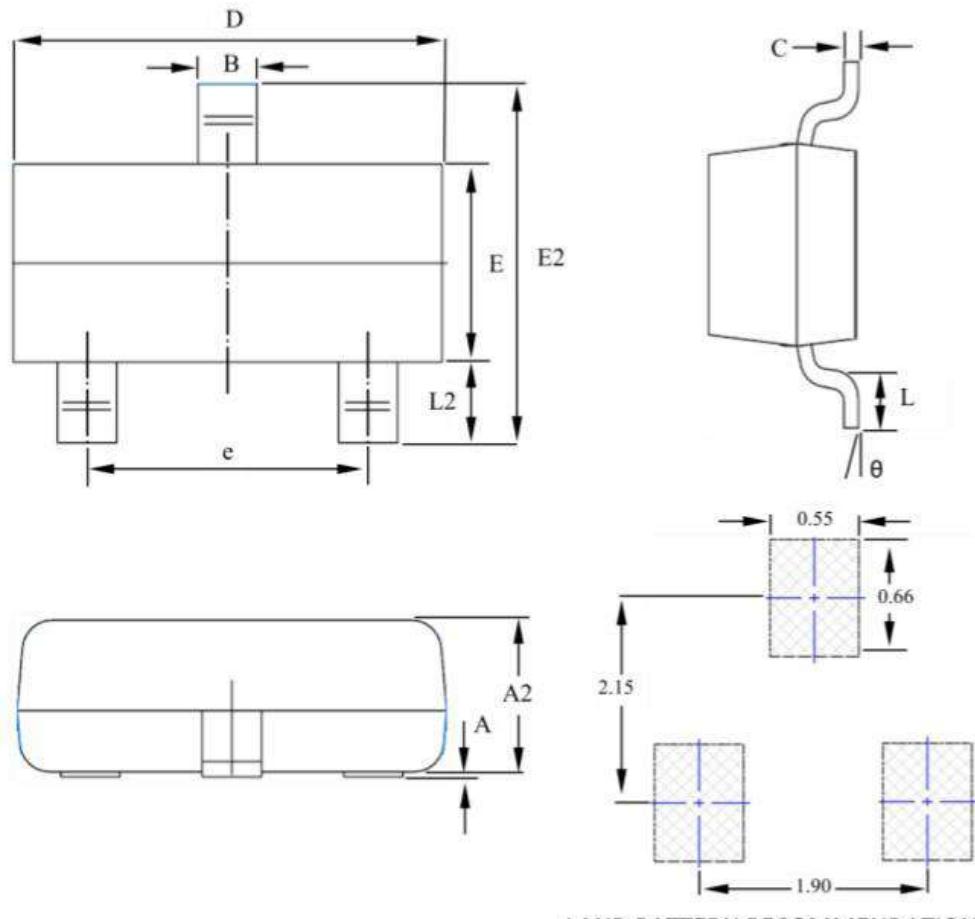


Fig.11 Gate Charge Waveform

SOT23S Package Outline Dimensions



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.00	--	0.10	0.000	--	0.004
A2	0.90	--	1.10	0.035	--	0.041
B	0.30	--	0.50	0.012	--	0.020
C	0.08	--	0.15	0.003	--	0.006
D	2.80	--	3.00	0.110	--	0.118
E	1.20	--	1.40	0.047	--	0.055
E2	2.25	--	2.55	0.089	--	0.100
L	0.30	--	0.50	0.012	--	0.020
L2	0.50	--	0.60	0.020	--	0.024
θ	0°	--	8°	0°	--	8°
e	1.80	--	2.00	0.071	--	0.079