

Single N-Channel MOSFET

Features:

- ★ Advanced Trench MOS Technology
- ★ 100% EAS Guaranteed
- ★ Green Device Available

Description:

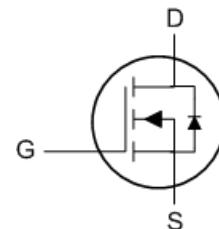
- ★ Power Management
- ★ DC-DC Converter

TO252 Pin Configuration



Product Summary

| BVDSS | RDS(on) | ID |
|-------|---------|-----|
| 100V | 14mΩ | 64A |



Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|---------------------------------------|--|------------|-------|
| V _{DS} | Drain-Source Voltage | 100 | V |
| V _{GS} | Gate-Source Voltage | ±20 | V |
| I _D @T _C =25°C | Continuous Drain Current, V _{GS} @ 10V ¹ | 64 | A |
| I _D @T _C =100°C | Continuous Drain Current, V _{GS} @ 10V ¹ | 40 | A |
| I _{DM} | Pulsed Drain Current ² | 180 | A |
| EAS | Single Pulse Avalanche Energy ³ | 101 | mJ |
| I _{AS} | Avalanche Current | 45 | A |
| P _D @T _C =25°C | Total Power Dissipation ⁴ | 89 | W |
| T _{STG} | Storage Temperature Range | -55 to 150 | °C |
| T _J | Operating Junction Temperature Range | -55 to 150 | °C |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|------------------|--|------|------|------|
| R _{θJA} | Thermal Resistance Junction-Ambient ¹ | --- | 62 | °C/W |
| R _{θJC} | Thermal Resistance Junction-Case ¹ | --- | 1.4 | °C/W |

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|----------------------------|--|--|------|------|-----------|------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$ | 100 | --- | --- | V |
| $\text{R}_{\text{DS(ON)}}$ | Static Drain-Source On-Resistance ² | $V_{\text{GS}}=10\text{V}$, $I_D=10\text{A}$ | --- | --- | 14 | $\text{m}\Omega$ |
| | Static Drain-Source On-Resistance ² | $V_{\text{GS}}=4.5\text{V}$, $I_D=10\text{A}$ | --- | --- | 17 | |
| $V_{\text{GS(th)}}$ | Gate Threshold Voltage | $V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$ | 1.2 | --- | 2.4 | V |
| I_{DSS} | Drain-Source Leakage Current | $V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$ | --- | --- | 1 | uA |
| | | $V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$ | --- | --- | 5 | |
| I_{GSS} | Gate-Source Leakage Current | $V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$ | --- | --- | ± 100 | nA |
| g_{fs} | Forward Transconductance | $V_{\text{DS}}=5\text{V}$, $I_D=10\text{A}$ | --- | 43 | --- | S |
| R_g | Gate Resistance | $V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$ | --- | 0.7 | --- | Ω |
| Q_g | Total Gate Charge (10V) | $V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$ | --- | 75 | --- | nC |
| Q_{gs} | Gate-Source Charge | | --- | 15.5 | --- | |
| Q_{gd} | Gate-Drain Charge | | --- | 20.3 | --- | |
| $T_{\text{d(on)}}$ | Turn-On Delay Time | $V_{\text{DD}}=40\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_g=3.3\Omega$, $I_D=20\text{A}$ | --- | 18.5 | --- | ns |
| T_r | Rise Time | | --- | 8.8 | --- | |
| $T_{\text{d(off)}}$ | Turn-Off Delay Time | | --- | 58.8 | --- | |
| T_f | Fall Time | | --- | 15.8 | --- | |
| C_{iss} | Input Capacitance | $V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$ | --- | 4708 | --- | pF |
| C_{oss} | Output Capacitance | | --- | 326 | --- | |
| C_{rss} | Reverse Transfer Capacitance | | --- | 247 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|--|---|------|------|------|-------------|
| I_s | Continuous Source Current ^{1,5} | $V_G=V_D=0\text{V}$, Force Current | --- | --- | 70 | A |
| V_{SD} | Diode Forward Voltage ² | $V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$ | --- | --- | 1.2 | V |
| t_{rr} | Reverse Recovery Time | $I_F=20\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$ | --- | 24 | --- | nS |
| | Reverse Recovery Charge | | --- | 28 | --- | nC |

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=45\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

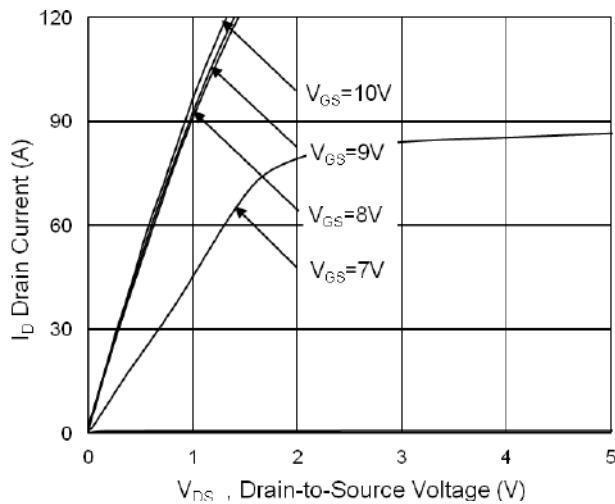


Fig.1 Typical Output Characteristics

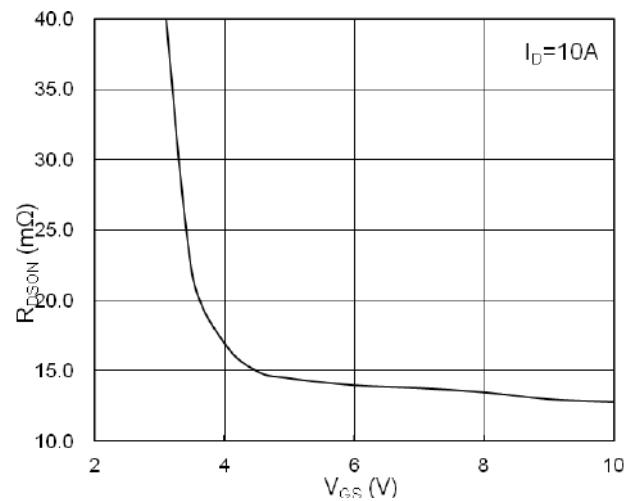


Fig.2 On-Resistance vs G-S Voltage

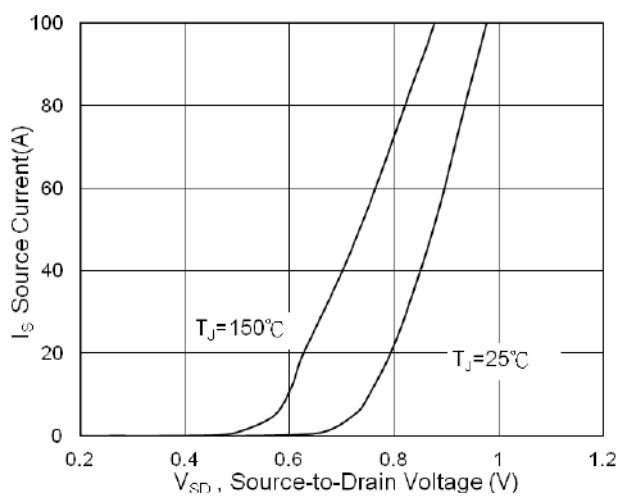


Fig.3 Source Drain Forward Characteristics

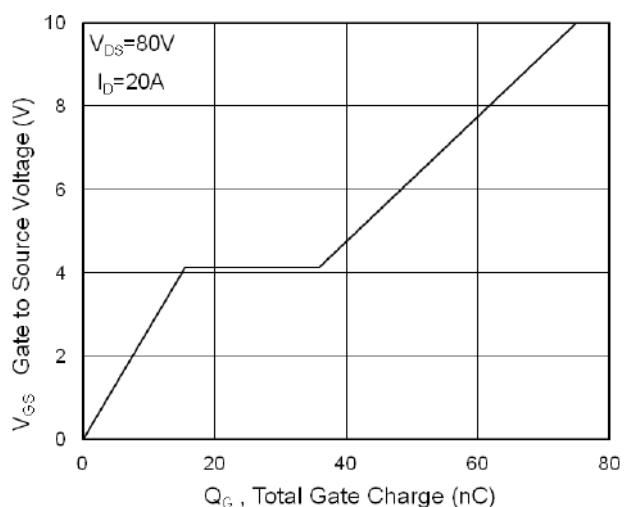


Fig.4 Gate-Charge Characteristics

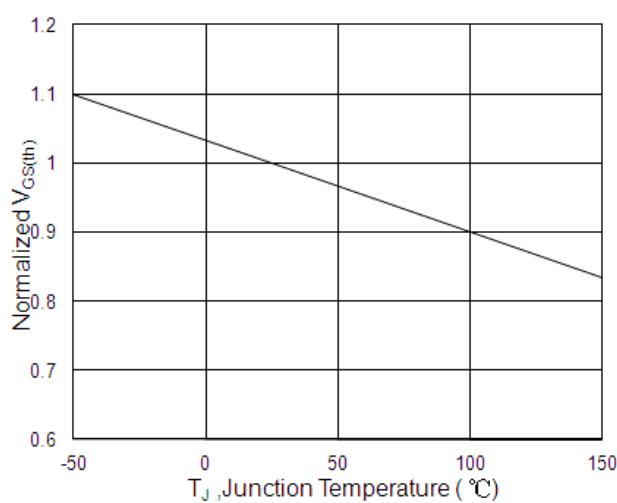


Fig.5 Normalized V_{GS(th)} vs T_J

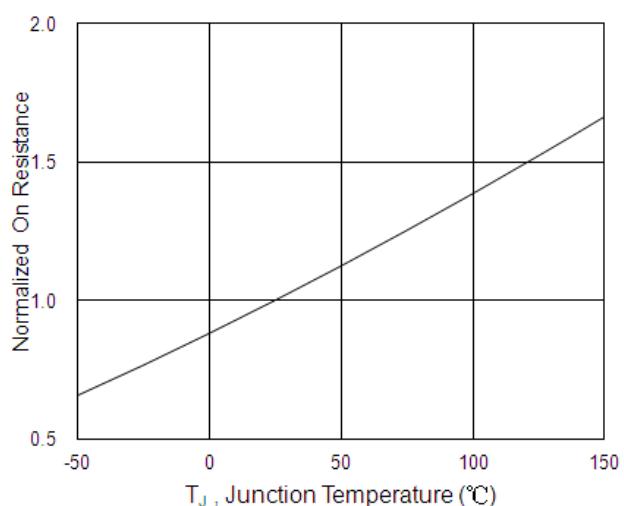


Fig.6 Normalized R_{DS(on)} vs T_J

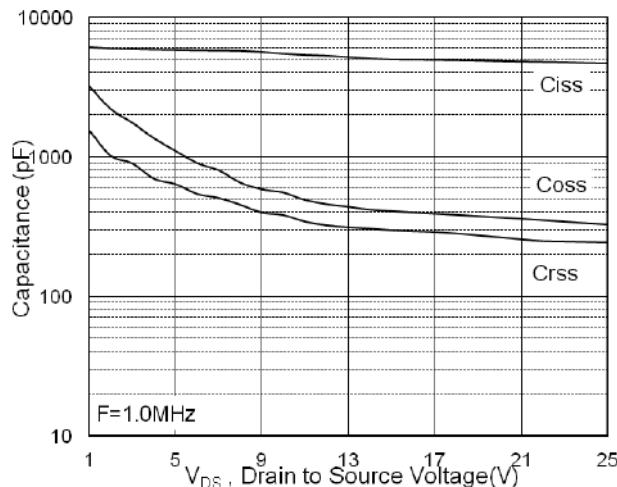


Fig.7 Capacitance

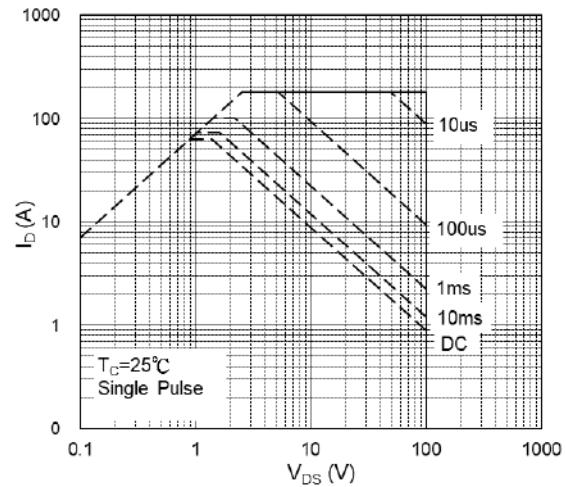


Fig.8 Safe Operating Area

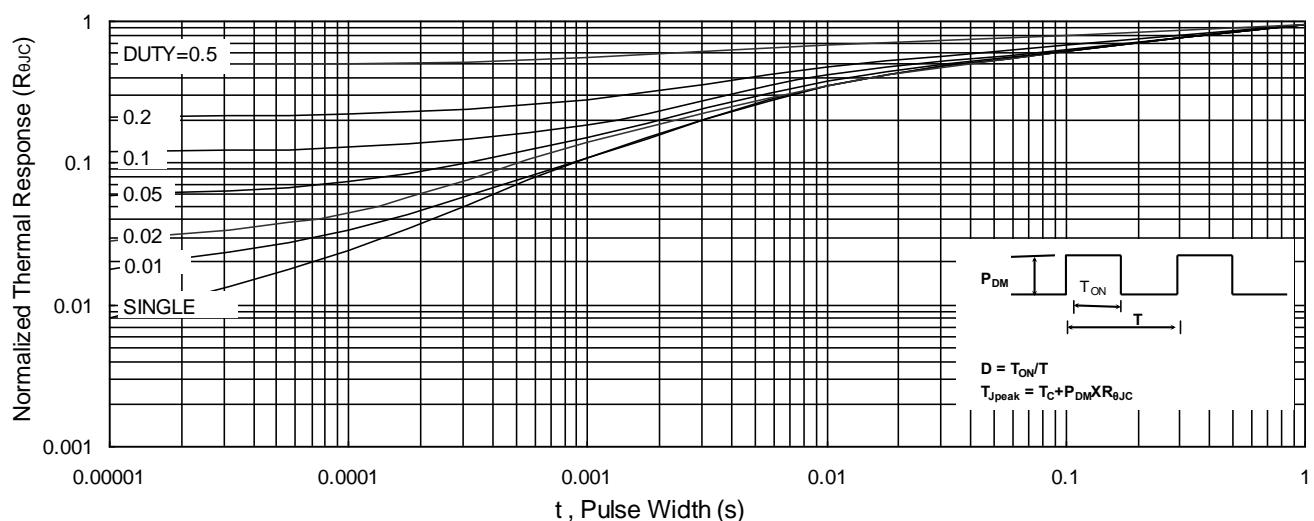


Fig.9 Normalized Maximum Transient Thermal Impedance

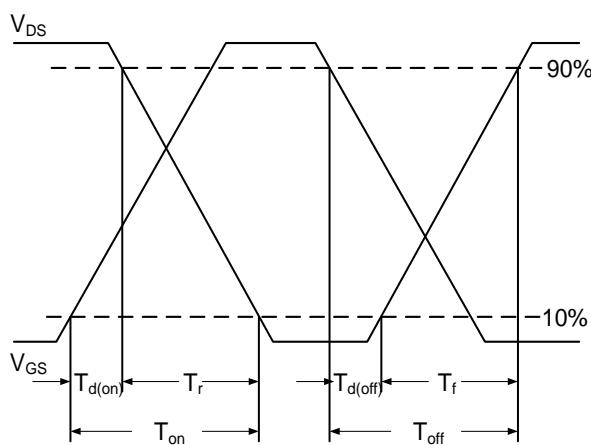


Fig.10 Switching Time Waveform

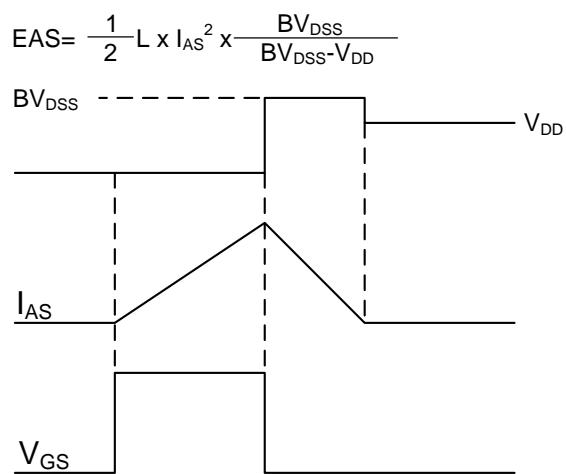


Fig.11 Unclamped Inductive Switching Waveform