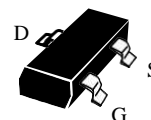


N-Channel High Density Trench MOSFET

Features:

- Super high dense cell trench design for low $R_{DS(on)}$.
- Rugged and reliable.
- Surface Mount package.

SOT-23-3L

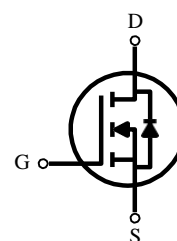


DEVICE MARKING

KW3400GE = X0DV

PRODUCT SUMMARY

V_{DSS}	I_D	$R_{DS(on)}$ (m Ω) Max
30V	5.8A	27 @ $V_{GS} = 10V$
	5.0A	32 @ $V_{GS} = 4.5V$
	4.0A	52 @ $V_{GS} = 2.5V$



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous ^a @ $T_A = 25^\circ C$ -Pulse ^b	I_D	5.8	A
	I_{DM}	24	A
Drain-Source Diode Forward Current ^a	I_S	2.5	A
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ C$	1.25
		$T_A = 75^\circ C$	0.75
Operating Junction and Storage Temperature Range	T_J, T_{STG}	- 55 to 150	$^\circ C$

Thermal Resistance, Junction-to-Ambient ^a	R_{thJA}	100	$^\circ C/W$
--	------------	-----	--------------

Note :

a. Surface Mounted on FR4 Board , $t \leq 10sec$.

b. Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS (T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V , I _D = 250uA	30			V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V , V _{GS} = 0V			1	uA	
Gate-Body Leakage	I _{GSS}	V _{GS} = ±12V , V _{DS} = 0V			±100	nA	
ON CHARACTERISTICS^b							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	0.7	0.9	1.4	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10V , I _D = 5.8A		25	27	Ω	
		V _{GS} = 4.5V , I _D = 5.0A		29	32		
		V _{GS} = 2.5V , I _D = 4.0A		42	52		
Forward Transconductance	g _{fs}	V _{DS} = 5V , I _D = 5A		10		S	
DRAIN-SOURCE DIODE CHARACTERISTICS^b							
Diode Forward Voltage	V _{SD}	V _{GS} = 0V , I _S = 1.0A			1.2	V	
DYNAMIC CHARACTERISTICS^c							
Input Capacitance	C _{ISS}	V _{DS} = 15V , V _{GS} = 0V f = 1.0MHz		551		pF	
Output Capacitance	C _{OSS}			54		pF	
Reverse Transfer Capacitance	C _{RSS}			46		pF	
SWITCHING CHARACTERISTICS^c							
Turn-On Delay Time	t _{D(ON)}	V _{DD} = 15V , I _D = 1A V _{GEN} = 10V R _L = 15 Ω R _{GEN} = 6 Ω		6.5		ns	
Rise Time	t _r			2		ns	
Turn-Off Delay Time	t _{D(OFF)}				28.6		ns
Fall Time	t _f				2.6		ns
Total Gate Charge	Q _g	V _{DS} = 15V I _D = 3A V _{GS} = 10V		13.2		nC	
Gate-Source Charge	Q _{gs}			2.2		nC	
Gate-Drain Charge	Q _{gd}			1.7		nC	

Note :

b. Pulse : Pulse width ≤ 300us , Duty Cycle ≤ 2% .

c. Guaranteed by design , not subject to production testing .

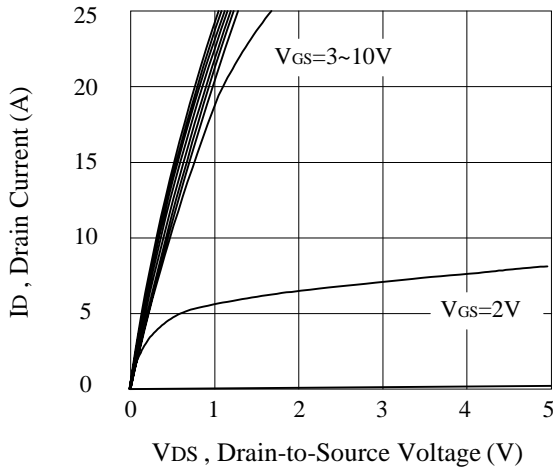


Figure 1. Output Characteristics

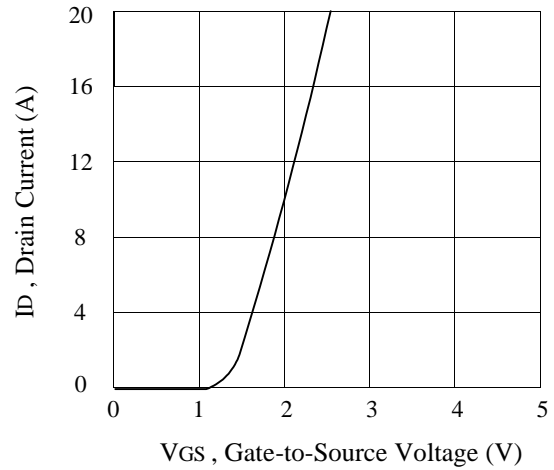


Figure 2. Transfer Characteristics

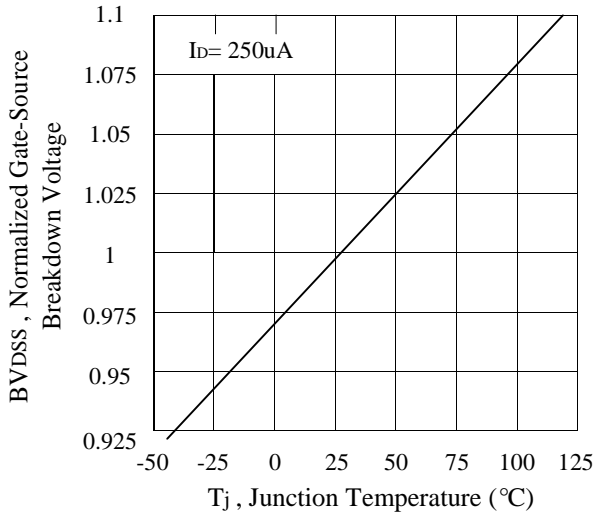


Figure 3. Breakdown Voltage Variation with Temperature

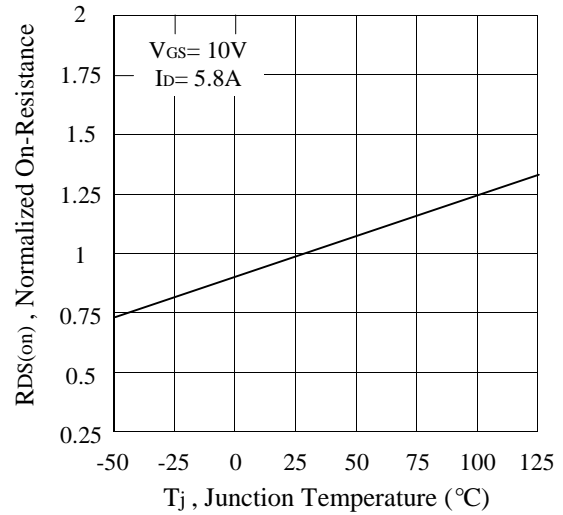


Figure 4. On-Resistance Variation with Temperature

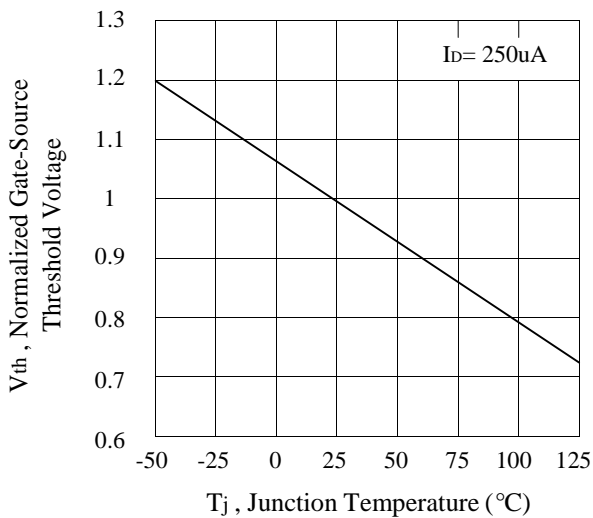


Figure 5. Gate Threshold Variation with Temperature

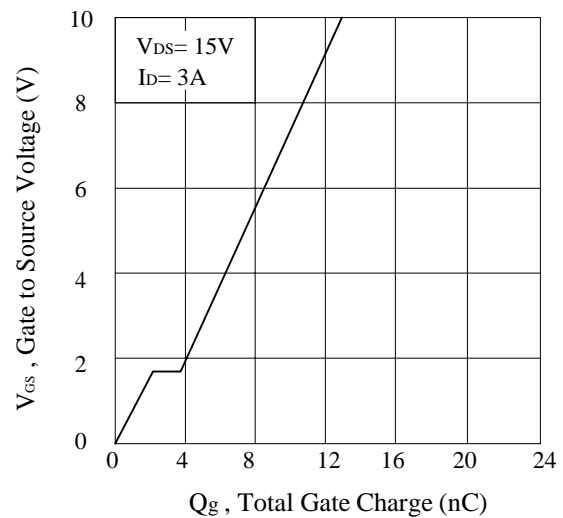


Figure 6. Gate Charge

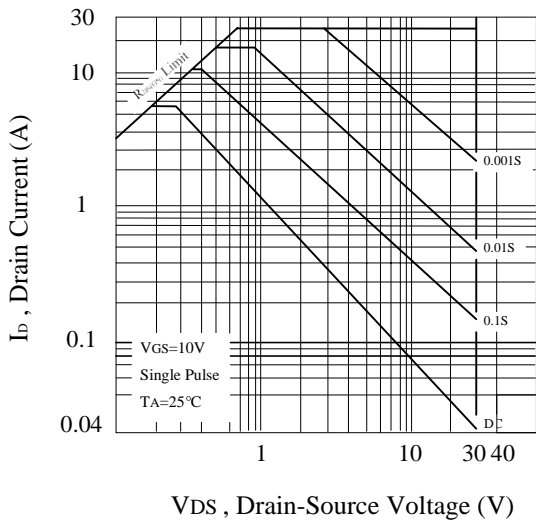


Figure 7. Maximum Safe Operating Area

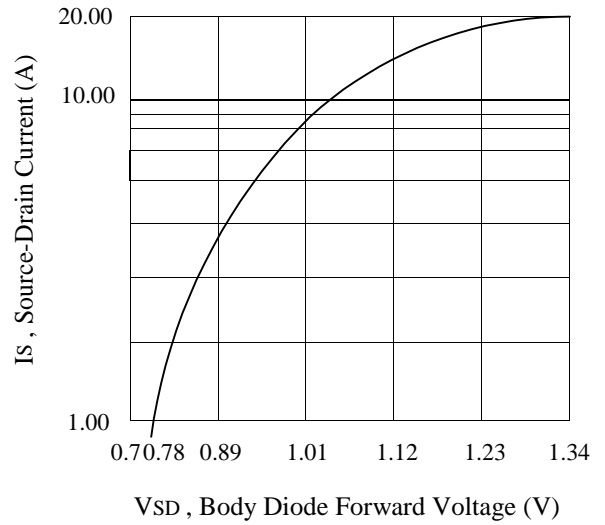


Figure 8. Body Diode Forward Voltage Variation with Source Current

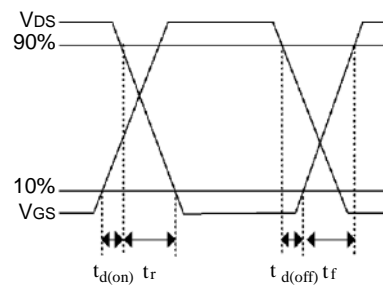
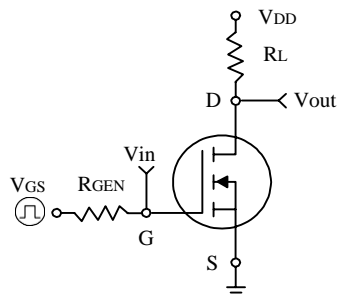


Figure 9. Switching Test Circuit and Switching Waveforms

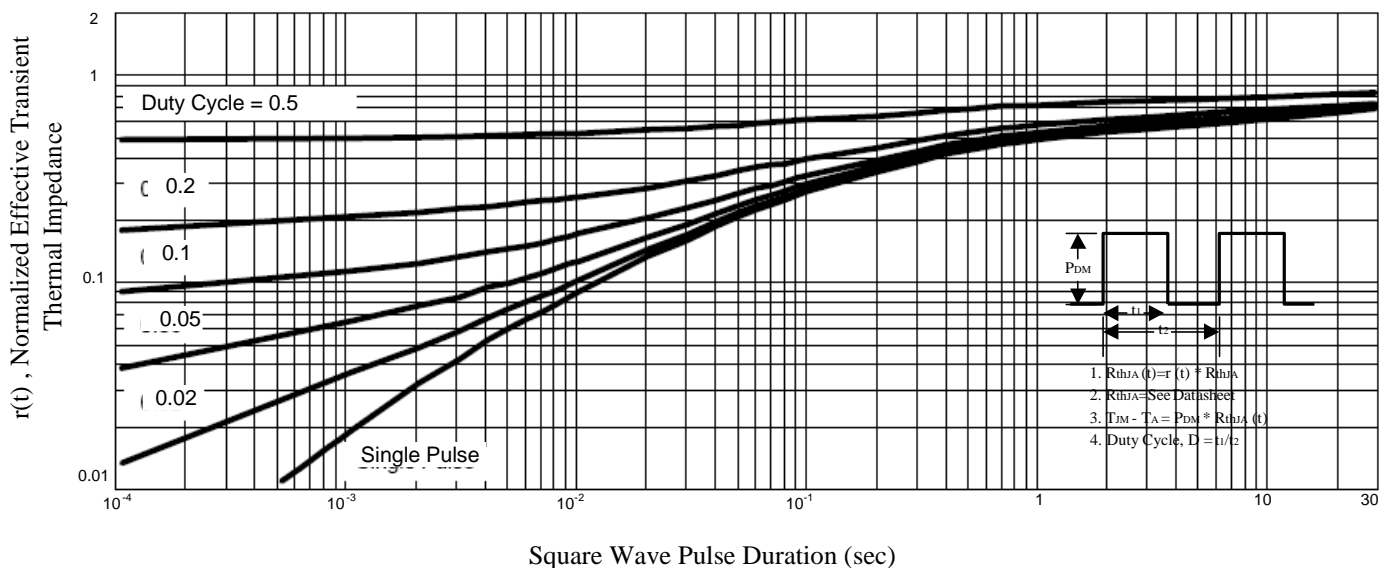


Figure 10. Normalized Thermal Transient Impedance Curve