

P-Channel High Density Trench MOSFET

Features:

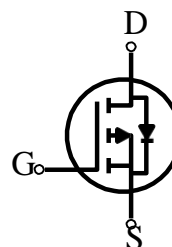
- Super high dense cell trench design for low RDS(on).
- Rugged and reliable.
- Surface Mount package.

SOT-23



PRODUCT SUMMARY

VDSS	ID	RDS(on) (m-ohm) Max
-60V	-2A	210 @ VGS= 10V
	-1.5A	330 @ VGS= 4.5V



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-60	V
Gate-Source Voltage	VGS	± 20	V
Drain Current-Continuous ^a @ T _A = 25 °C -Pulse ^b	ID	-1.5	A
	IDM	-6	A
Drain-Source Diode Forward Current ^a	IS	-0.75	A
Maximum Power Dissipation ^a	PD	1.25	W
		0.75	
Operating Junction and Storage Temperature Range	T _J , T _{STG}	- 55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R _{thJA}	100	°C/W
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Note

a. Surface Mounted on FR4 Board , t ≤ 10sec.

b. Pulse width limited by maximum junction temperature.

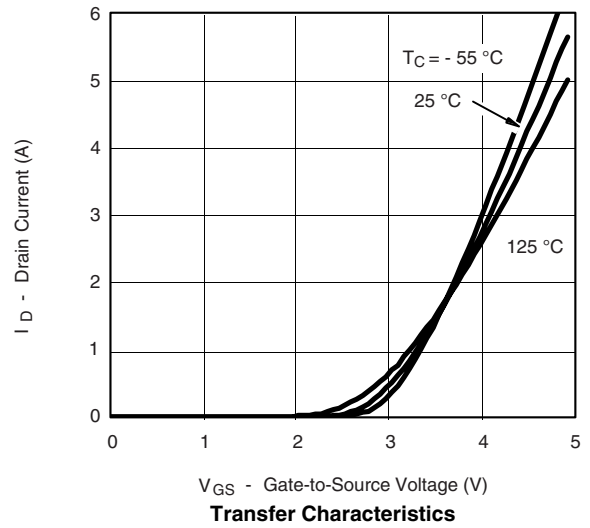
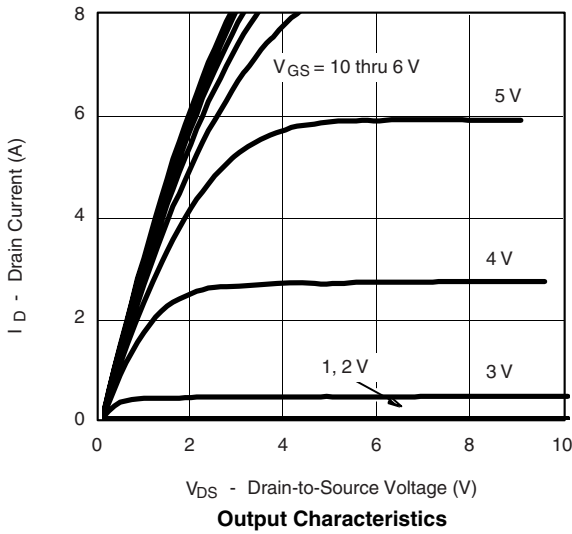
ELECTRICAL CHARACTERISTICS (T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V , I _D = -250uA	-60			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V , V _{GS} = 0V			-1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} = ±8V , V _{DS} = 0V			-100	nA
ON CHARACTERISTICS^b						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250uA	-1	-1.6	-3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = -10V , I _D = -1.5A		150	210	m-ohm
		V _{GS} = -4.5V , I _D = -1.2A		260	330	m-ohm
DRAIN-SOURCE DIODE CHARACTERISTICS^b						
Diode Forward Voltage	V _{SD}	V _{GS} = 0V , I _S = -0.75A			-1.2	V
DYNAMIC CHARACTERISTICS^c						
Input Capacitance	C _{ISS}	V _{DS} = -60V , V _{GS} = 0V f = 1.0MHz		785		pF
Output Capacitance	C _{OSS}			285		pF
Reverse Transfer Capacitance	C _{RSS}			112		pF
SWITCHING CHARACTERISTICS^c						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = -6V , I _D = -1A		10.5		ns
Rise Time	t _r	V _{GEN} = -4.5V		15.6		ns
Turn-Off Delay Time	t _{D(OFF)}	R _L = 6 ohm		18.7		ns
Fall Time	t _f	R _{GEN} = 6 ohm		8.2		ns
Total Gate Charge	Q _g	V _{DS} = -6V		8.2		nC
Gate-Source Charge	Q _{gs}	I _D = -2.8A		5.8		nC
Gate-Drain Charge	Q _{gd}	V _{GS} = -4.5V		3.7		nC

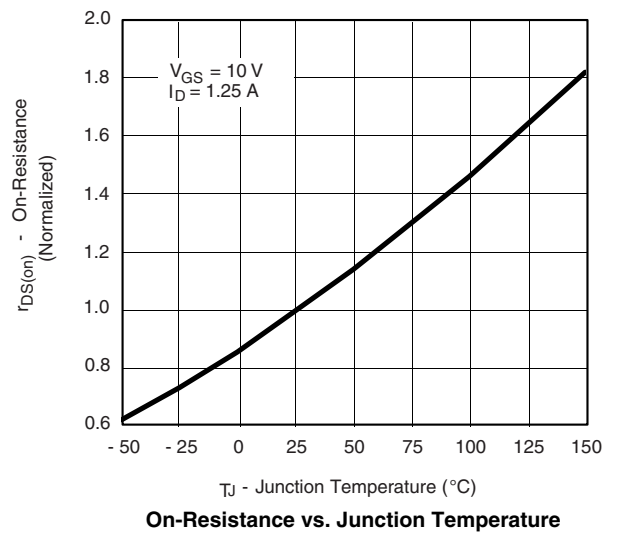
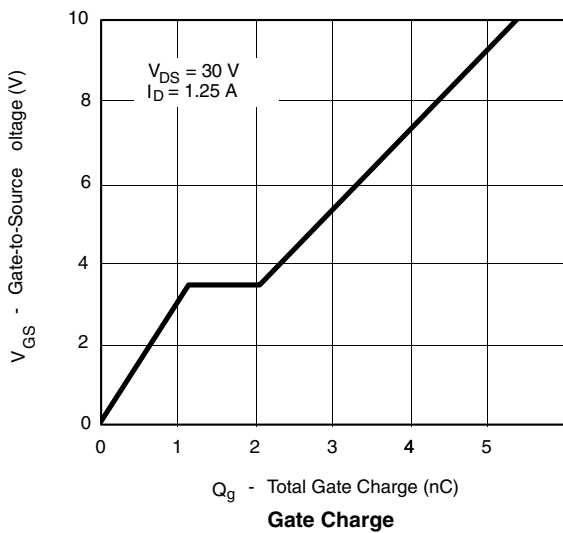
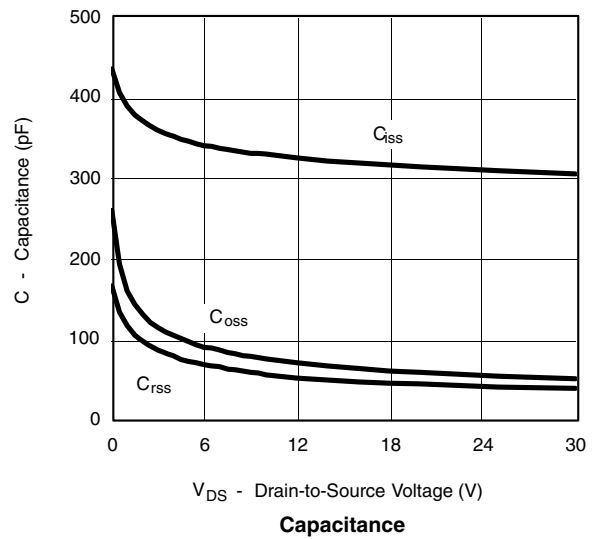
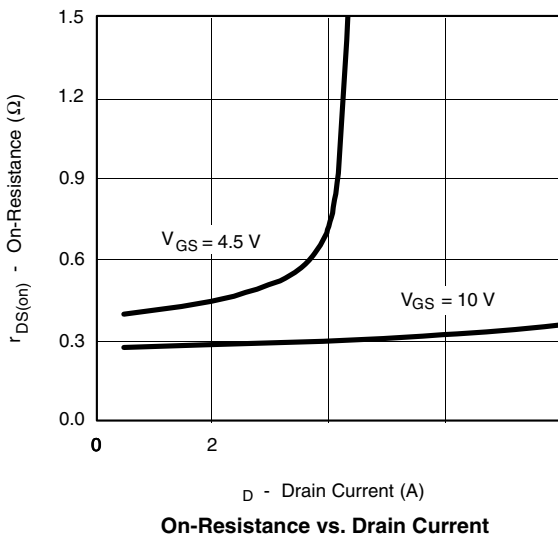
Note

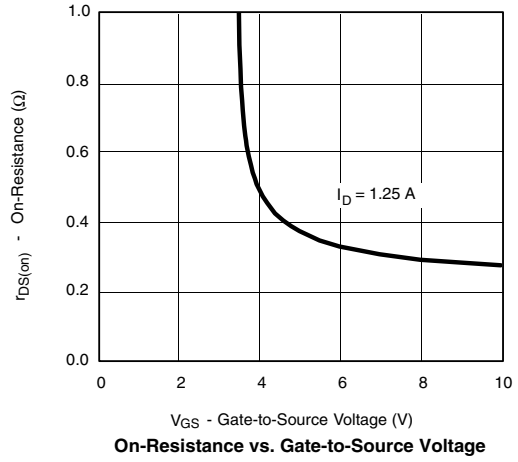
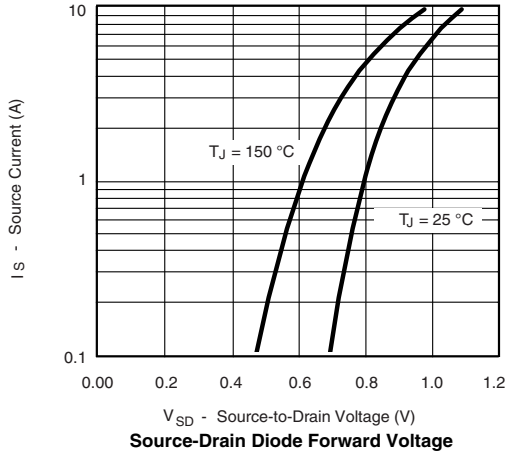
b. Pulse Test Pulse width ≤ 300us , Duty Cycle ≤ 2% .

c. Guaranteed by design , not subject to production testing .



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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