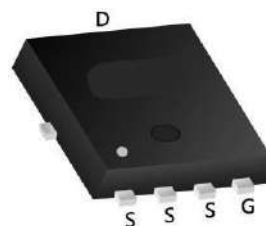


Features:

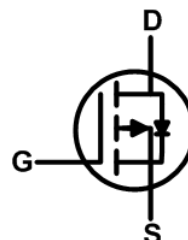
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



PRPAK5X6 Pin Configuration

Product Summary

| BVDSS | RDSON | ID |
|-------|-------|------|
| -30V | 9.8mΩ | -59A |



Absolute Maximum Ratings

| Symbol | Parameter | Rating | | Units |
|---------------------------------------|---|------------|--------------|-------|
| | | 10s | Steady State | |
| V _{DS} | Drain-Source Voltage | -30 | | V |
| V _{GS} | Gate-Source Voltage | ±25 | | V |
| I _D @T _C =25°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -59 | | A |
| I _D @T _C =100°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -37 | | A |
| I _D @T _A =25°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -18 | -11.6 | A |
| I _D @T _A =70°C | Continuous Drain Current, V _{GS} @ -10V ¹ | -14.6 | -9.3 | A |
| I _{DM} | Pulsed Drain Current ² | -180 | | A |
| EAS | Single Pulse Avalanche Energy ³ | 153 | | mJ |
| I _{AS} | Avalanche Current | -55.4 | | A |
| P _D @T _C =25°C | Total Power Dissipation ⁴ | 52.1 | | W |
| P _D @T _A =25°C | Total Power Dissipation ⁴ | 5 | 2 | W |
| T _{STG} | Storage Temperature Range | -55 to 150 | | °C |
| T _J | Operating Junction Temperature Range | -55 to 150 | | °C |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|------------------|--|------|------|------|
| R _{θJA} | Thermal Resistance Junction-Ambient ¹ | --- | 62 | °C/W |
| R _{θJA} | Thermal Resistance Junction-Ambient ¹ (t ≤ 10s) | --- | 25 | °C/W |
| R _{θJC} | Thermal Resistance Junction-Case ¹ | --- | 2.4 | °C/W |

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------------|--|--|------|--------|------|-------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =-250uA | -30 | --- | --- | V |
| ΔBV _{DSS} /ΔT _J | BV _{DSS} Temperature Coefficient | Reference to 25°C, I _D =-1mA | --- | -0.018 | --- | V/°C |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =-10V, I _D =-20A | --- | 7.8 | 9.8 | mΩ |
| | | V _{GS} =-4.5V, I _D =-15A | --- | 11 | 15 | |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =-250uA | -1.0 | --- | -2.5 | V |
| ΔV _{GS(th)} | V _{GS(th)} Temperature Coefficient | | --- | 5.04 | --- | mV/°C |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =-24V, V _{GS} =0V, T _J =25°C | --- | --- | 1 | uA |
| | | V _{DS} =-24V, V _{GS} =0V, T _J =55°C | --- | --- | 5 | |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±25V, V _{DS} =0V | --- | --- | ±100 | nA |
| g _{fs} | Forward Transconductance | V _{DS} =-5V, I _D =-30A | --- | 26.4 | --- | S |
| Q _g | Total Gate Charge (-4.5V) | V _{DS} =-15V, V _{GS} =-4.5V, I _D =-15A | --- | 33 | --- | nC |
| Q _{gs} | Gate-Source Charge | | --- | 10.7 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 12.8 | --- | |
| T _{d(on)} | Turn-On Delay Time | V _{DD} =-15V, V _{GS} =-10V, R _G =3.3Ω, I _D =-15A | --- | 8 | --- | ns |
| T _r | Rise Time | | --- | 17.8 | --- | |
| T _{d(off)} | Turn-Off Delay Time | | --- | 78.4 | --- | |
| T _f | Fall Time | | --- | 43.6 | --- | |
| C _{iss} | Input Capacitance | V _{DS} =-15V, V _{GS} =0V, f=1MHz | --- | 3448 | --- | pF |
| C _{oss} | Output Capacitance | | --- | 508 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 421 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| I _S | Continuous Source Current ^{1,5} | V _G =V _D =0V, Force Current | --- | --- | -59 | A |
| V _{SD} | Diode Forward Voltage ² | V _{GS} =0V, I _S =-1A, T _J =25°C | --- | --- | -1.2 | V |
| t _{rr} | Reverse Recovery Time | I _F =-15A, dI/dt=100A/μs, T _J =25°C | --- | 29 | --- | nS |
| Q _{rr} | Reverse Recovery Charge | | --- | 15 | --- | nC |

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-55.4A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

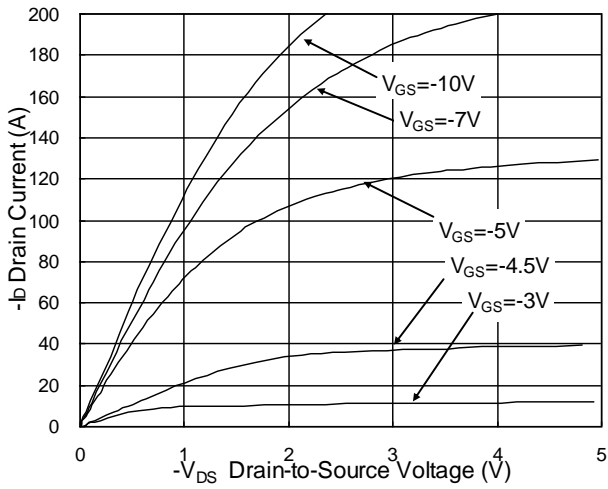


Fig.1 Typical Output Characteristics

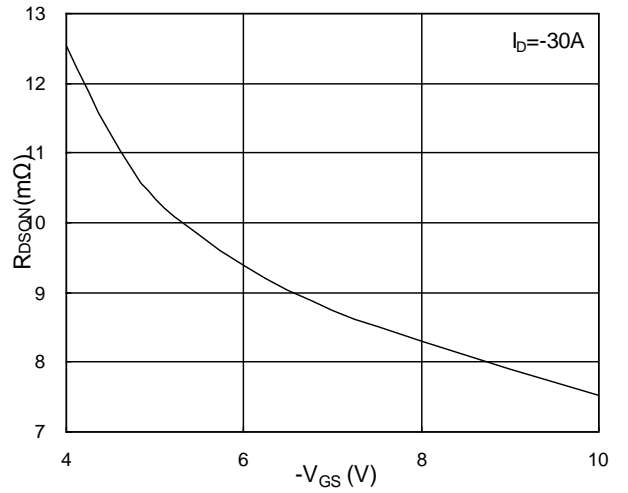


Fig.2 On-Resistance v.s Gate-Source

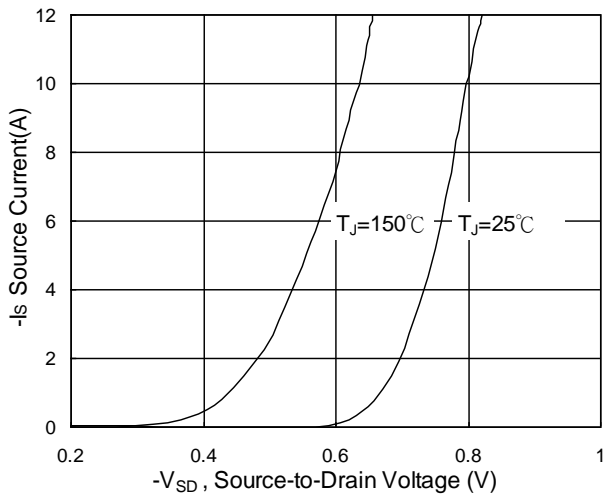


Fig.3 Forward Characteristics Of Reverse

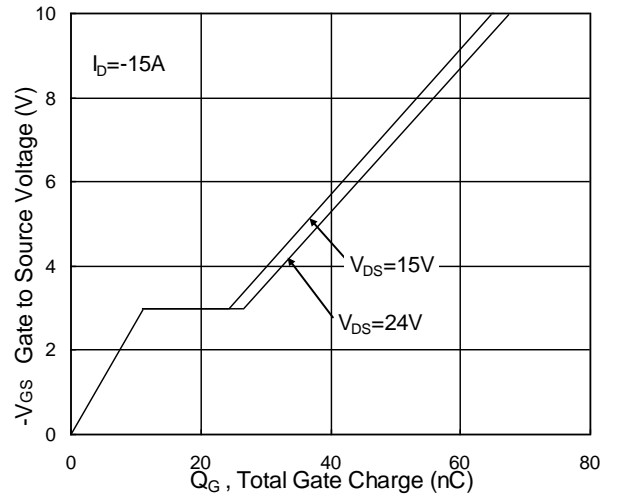


Fig.4 Gate-Charge Characteristics

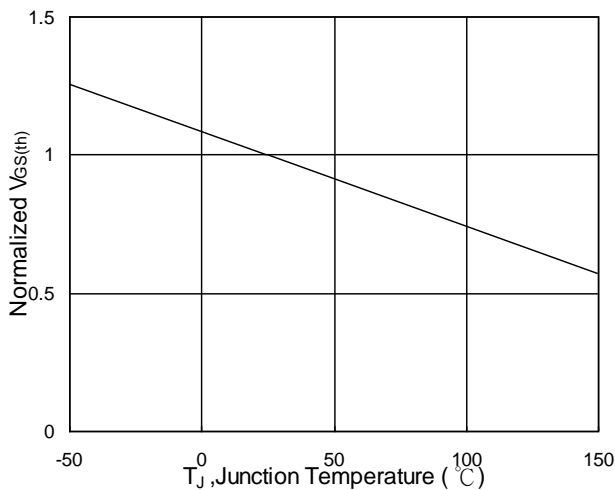


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

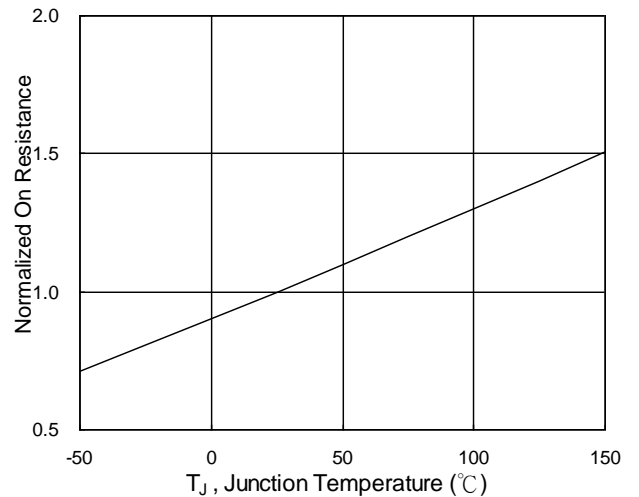


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

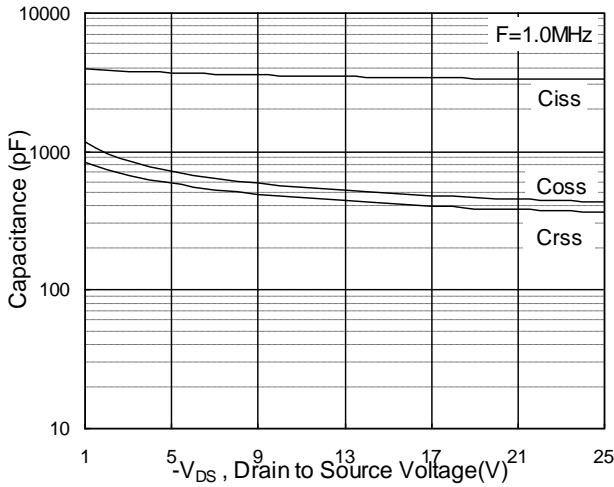


Fig.7 Capacitance

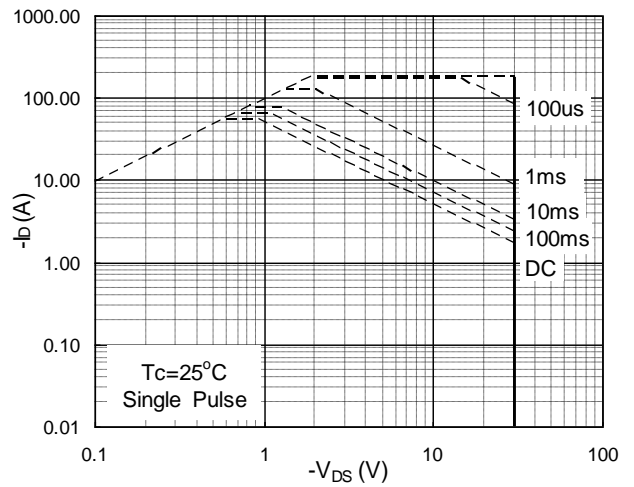


Fig.8 Safe Operating Area

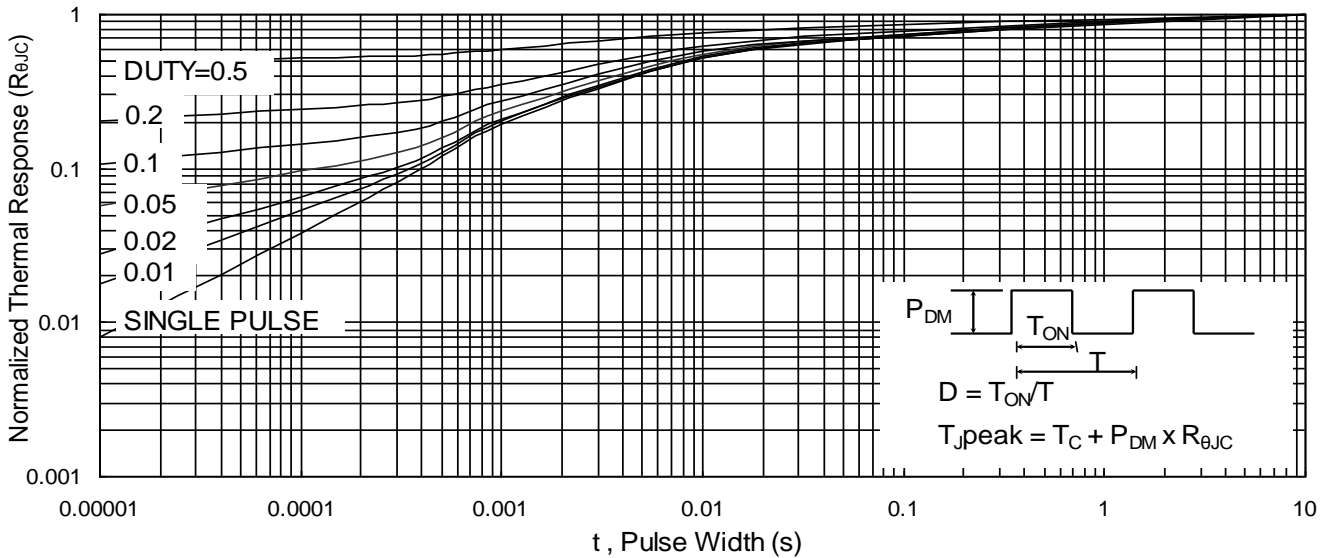


Fig.9 Normalized Maximum Transient Thermal Impedance

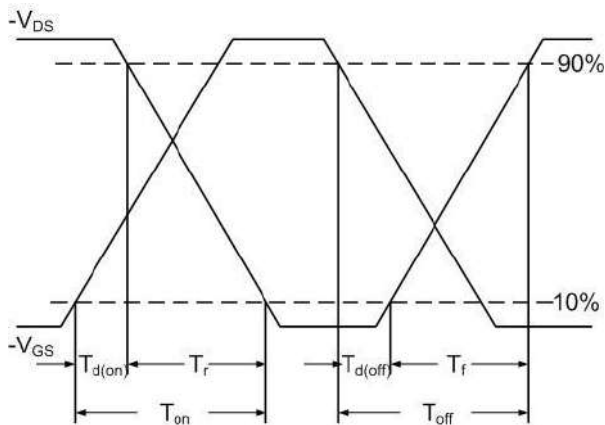


Fig.10 Switching Time Waveform

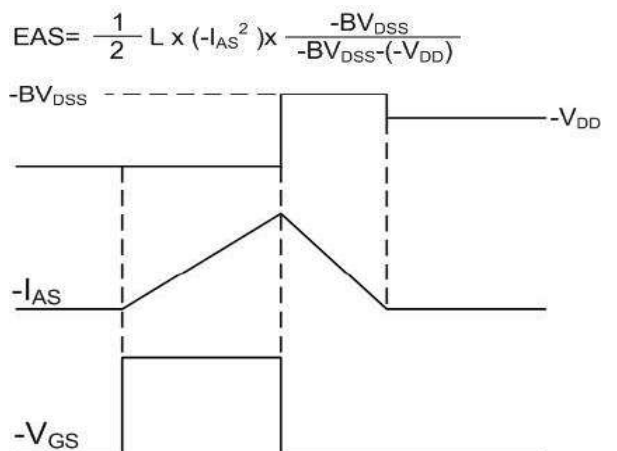


Fig.11 Unclamped Inductive Switching Waveform