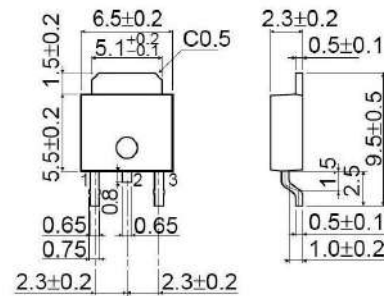


# SCHOTTKY BARRIER RECTIFIER

## Features:

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Metal silicon junction ,majority carrier conduction
- Guard ring for overvoltage protection
- Low power loss ,high efficiency
- High current capability ,Low forward voltage drop
- High surge capability
- For use in low voltage ,high frequency inverters, free wheeling ,and polarity protection applications
- Dual rectifier construction
- High temperature soldering guaranteed:260° C/10 seconds,, 0.25"(6.35mm)from case
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

## TO-252



## Mechanical Data:

- **Case:** JEDEC TO-220AB molded plastic body
- **Terminals:** Lead solderable per MIL-STD-750,method 2026
- **Polarity:** As marked
- **Mounting Position:** Any
- **Weight:** 0.08ounce, 2.24 grams

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Ratings at 25°C ambient temperature unless otherwise specified ,Single phase ,half wave ,resistive or inductive load. For capacitive load,derate by 20%.)

	Symbols	KSRD 1520CT	KSRD 1530CT	KSRD 1545CT	KSRD 1550CT	KSRD 1560CT	KSRD 1580CT	KSRD 15100CT	KSRD 15150CT	KSRD 15200CT	Units
Maximum repetitive peak reverse voltage	$V_{RRM}$	20	30	45	50	60	80	100	150	200	Volts
Maximum RMS voltage	$V_{RMS}$	14	21	31.5	35	42	56	70	105	140	Volts
Maximum DC blocking voltage	$V_{DC}$	20	30	45	50	60	80	100	150	200	Volts
Maximum average forward rectified current(see Fig.1)	Per leg	7.5									Amps
	Total device	15.0									
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	$I_{FSM}$	160.0									Amps
Maximum instantaneous forward voltage at 7.5A	$V_F$	0.60			0.75		0.85		0.90	0.95	Volts
Maximum instantaneous reverse current at rated DC blocking voltage(Note 1)	$T_c=25^{\circ}C$	0.2									mA
	$T_c=125^{\circ}C$	30			50						
Typical thermal resistance (Note 2)	$R_{JC}$	3.0									°C/W
Operating junction temperature range	$T_J$	-65 to+150									°C
Storage temperature range	$T_{STG}$	-65 to+150									°C

- Notes:** 1.Pulse test: 300 μs pulse width,1% duty cycle  
 2.Thermal resistance from junction to case

FIG.1-FORWARD CURRENT DERATING CURVE

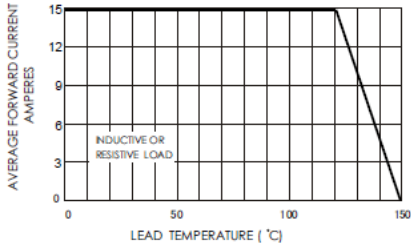


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

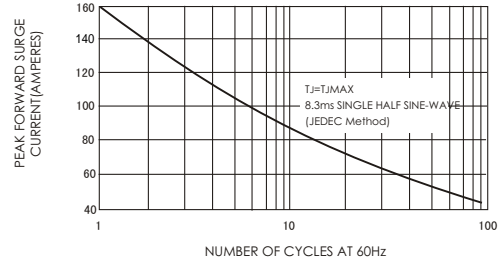


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

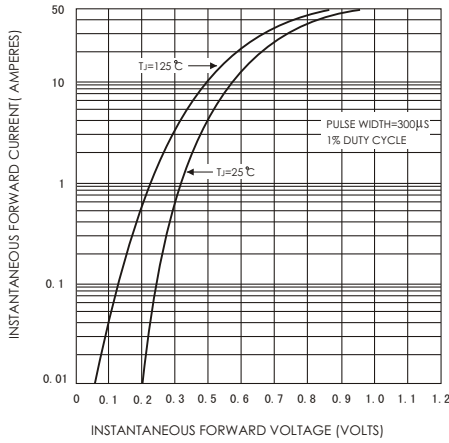


FIG.4-TYPICAL REVERSE CHARACTERISTICS

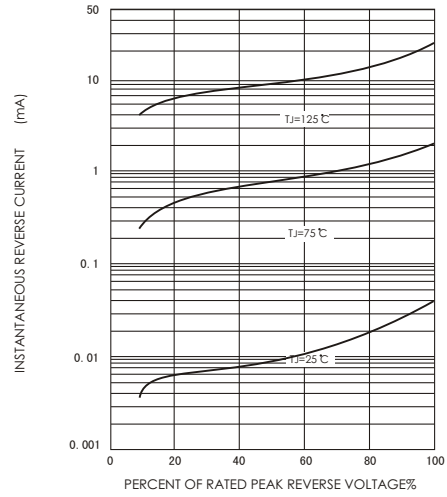


FIG.5-TYPICAL JUNCTION CAPACITANCE

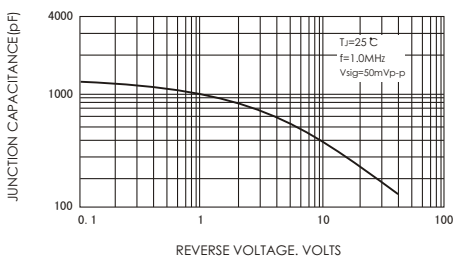


FIG.6-TYPICAL TRANSIENT THERMAL IMPEDANCE

