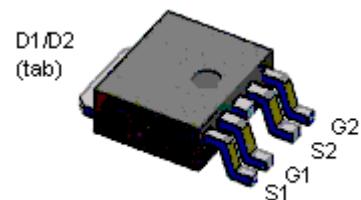


N & P-Channel Enhancement Mode Power MOSFET

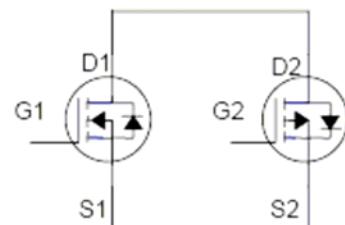
Features:

- Low Gate Charge
- Simple Drive Requirement
- RoHS compliant & Halogen-free package

TO-252-4L



	N-CH	P-CH
BV _{DSS}	40V	-40V
I _D	5.2A	-6.2A
R _{DSON} (typ.) @ V _{GS} =(-)10V	20 mΩ	13.3 mΩ
R _{DSON} (typ.) @ V _{GS} =(-)4.5V	28 mΩ	17.8 mΩ



G : Gate D : Drain S : Source

Absolute Maximum Ratings (T_A=25°C, unless otherwise noted)

Parameter	Symbol	Limits		Unit	
		N-channel	P-channel		
Drain-Source Voltage	V _{DS}	40	-40	V	
Gate-Source Voltage	V _{GS}	±20	±20		
Continuous Drain Current @ T _c =25°C, V _{GS} =10V(-10V) (Note1)	I _D	22	-26	A	
Continuous Drain Current @ T _c =100°C, V _{GS} =10V(-10V) (Note1)		15.6	-18.4		
Continuous Drain Current @ T _A =25°C, V _{GS} =10V(-10V) (Note4)		5.2	-6.2		
Continuous Drain Current @ T _A =70°C, V _{GS} =10V(-10V) (Note4)		4.2	-5.0		
Pulsed Drain Current *1 (Note3)	I _{DM}	30	-30		
Total Power Dissipation (T _c =25°C) (Note1)	P _D	25		W	
Total Power Dissipation (T _c =100°C) (Note1)		12.5			
Total Power Dissipation (T _A =25°C) (Note2)	P _{DSM}	2.4			
Total Power Dissipation (T _A =70°C) (Note2)		1.7			
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+175		°C	

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	6	°C/W
Thermal Resistance, Junction-to-ambient, max (Note2)	R _{th,j-a}	62.5	
Thermal Resistance, Junction-to-ambient, max (Note4)		90	

- Note : 1.The power dissipation P_D is based on T_{J(MAX)}=175°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
 2. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
 3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=175°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.
 4. When mounted on the minimum pad size recommended (PCB mount), t≤10s.

N-CH Characteristics (T_c=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	40	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1.0	1.6	2.5		V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
ID _{SS}	-	-	1		V _{DS} =32V, V _{GS} =0V
	-	-	10	μA	V _{DS} =30V, V _{GS} =0V, T _j =55°C
R _{D(S(ON))} *1	-	20	28	mΩ	V _{GS} =10V, I _D =5A
	-	28	38		V _{GS} =4.5V, I _D =4A
G _{FS} *1	-	7.9	-	S	V _{DS} =5V, I _D =5A
Dynamic					
Q _g *1	-	10	-	nC	V _{DS} =20V, I _D =5A, V _{GS} =10V
Q _{gs} *1	-	2.6	-		
Q _{gd} *1	-	2.7	-		
t _{d(ON)} *1	-	14	-	ns	V _{DS} =20V, I _D =1A, V _{GS} =10V, R _G =6Ω
t _r *1	-	16	-		
t _{d(OFF)} *1	-	34	-		
t _f *1	-	22	-		
C _{iss}	-	695	-	pF	V _{DS} =15V, V _{GS} =0V, f=1MHz
C _{oss}	-	57	-		
C _{rss}	-	48	-		
Source-Drain Diode					
I _S *1	-	-	5	A	I _S =1A, V _{GS} =0V
I _{SM} *2	-	-	30		
V _{SD} *1	-	0.74	1	V	I _S =1A, V _{GS} =0V
t _{rr} *1	-	16	-	ns	I _F =5A, V _{GS} =0V, dI _F /dt=100A/μs
Q _{rr} *1	-	10	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2.Pulse width limited by maximum junction temperature.

P-CH Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-40	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-1.0	-1.2	-2.5		V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	-1		V _{DS} =-32V, V _{GS} =0V
	-	-	-10		V _{DS} =-30V, V _{GS} =0V, T _j =55°C
R _{DSS(ON)} *1	-	13.3	20	mΩ	V _{GS} =-10V, I _D =-6A
	-	17.8	26		V _{GS} =-4.5V, I _D =-5A
G _{Fs} *1	-	18	-	S	V _{DS} =-5V, I _D =-6A
Dynamic					
Q _g *1	-	38	-	nC	V _{DS} =-20V, I _D =-6A, V _{GS} =-10V
Q _{gs} *1	-	9.8	-		
Q _{gd} *1	-	11	-		
t _{d(ON)} *1	-	30	-	ns	V _{DS} =-20V, I _D =-1A, V _{GS} =-10V, R _G =6Ω
t _r *1	-	20	-		
t _{d(OFF)} *1	-	100	-		
t _f *1	-	36	-		
C _{iss}	-	2977	-	pF	V _{DS} =-15V, V _{GS} =0V, f=1MHz
C _{oss}	-	243	-		
C _{rss}	-	201	-		
Source-Drain Diode					
I _s *1	-	-	-6	A	Is=-1A, V _{GS} =0V
I _{SM} *2	-	-	-30		
V _{SD} *1	-	-0.7	-1	V	Is=-1A, V _{GS} =0V
t _{rr} *1	-	24	-	ns	I _F =-6A, V _{GS} =0V, dI _F /dt=100A/μs
Q _{rr} *1	-	18	-		

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

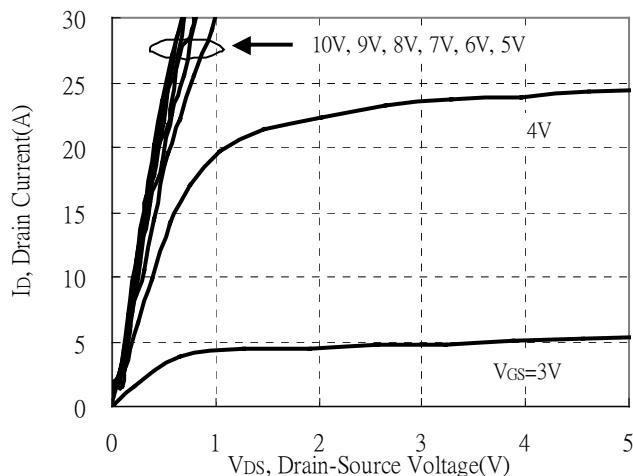
*2.Pulse width limited by maximum junction temperature.

Ordering Information

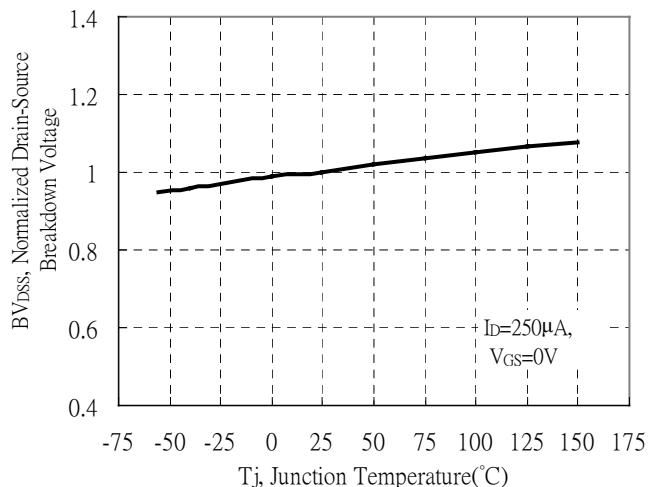
Device	Package	Shipping
KJB23C04	TO-252 (RoHS compliant & Halogen-free package)	3000 pcs / Tape & Reel

Q1, N-CH Typical Characteristics

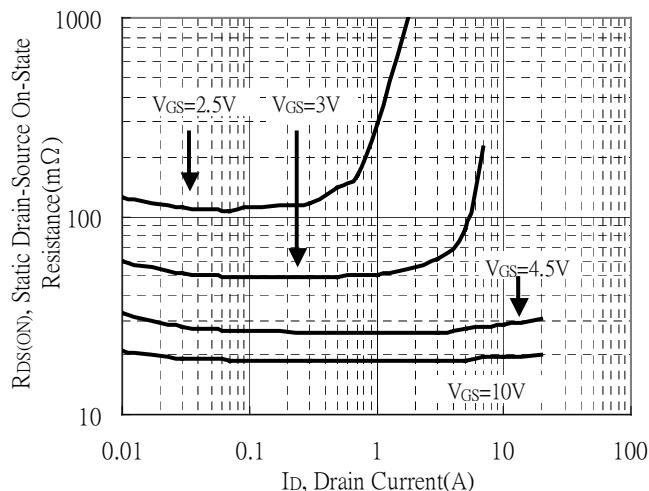
Typical Output Characteristics



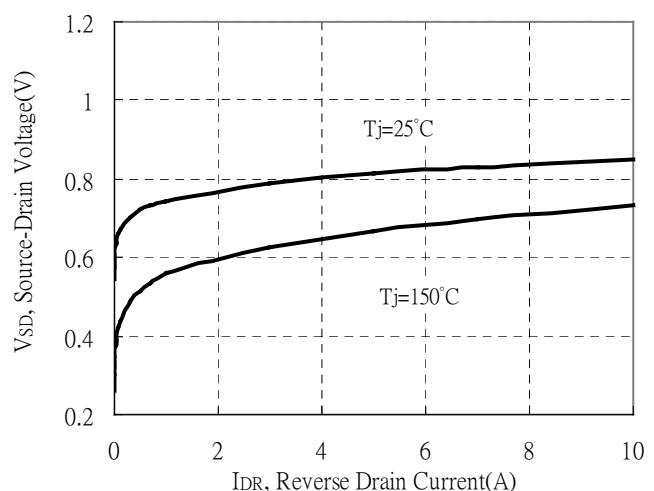
Breakdown Voltage vs Ambient Temperature



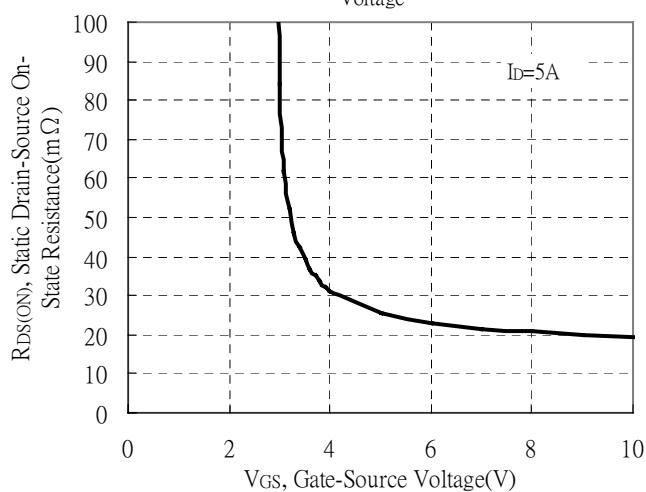
Static Drain-Source On-State resistance vs Drain Current



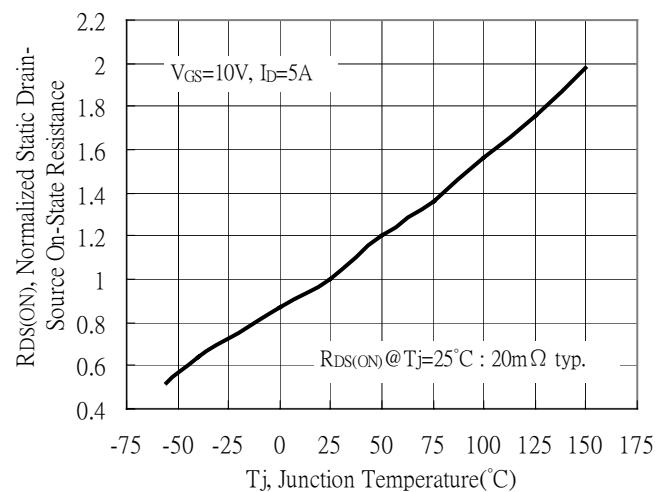
Reverse Drain Current vs Source-Drain Voltage



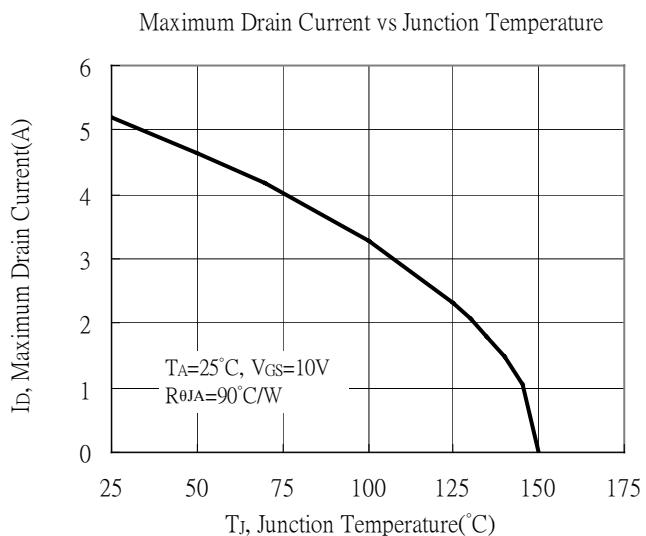
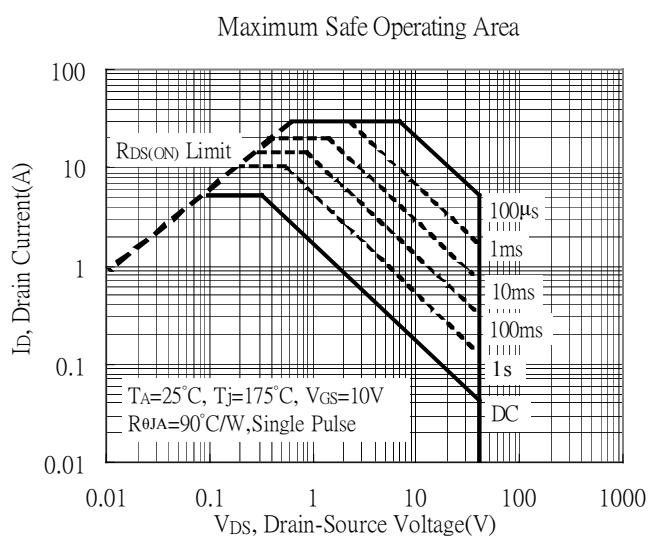
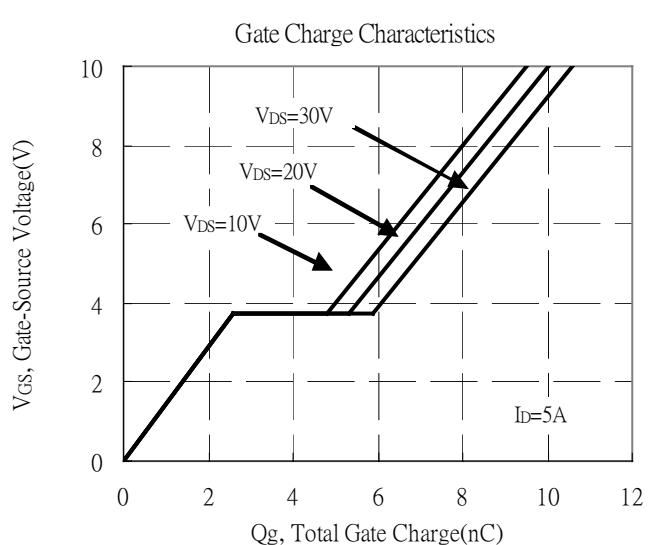
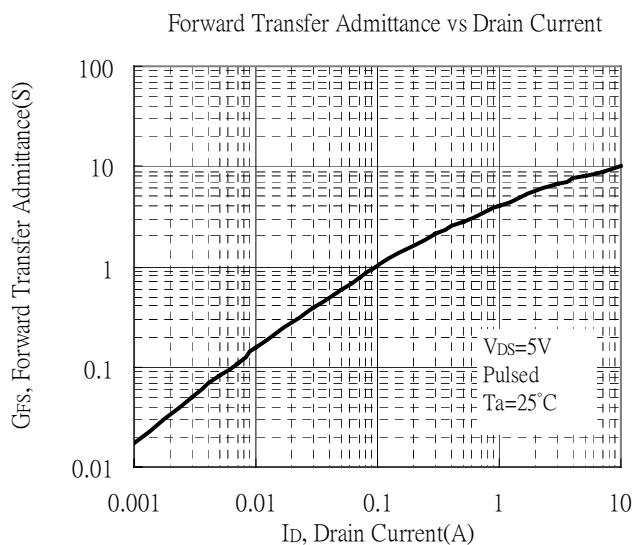
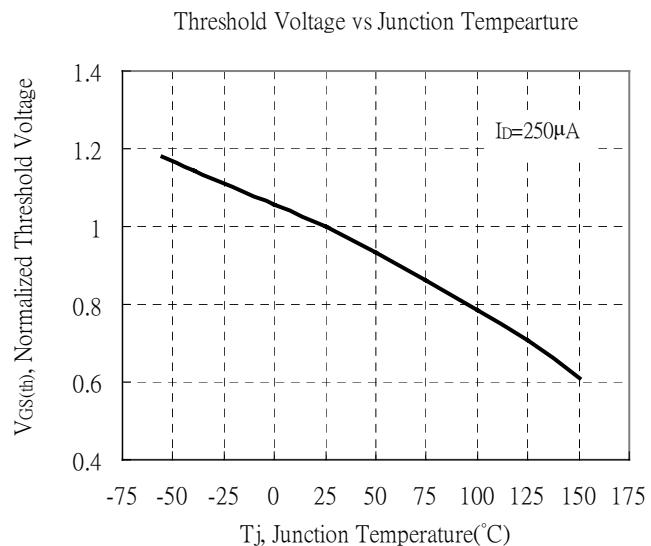
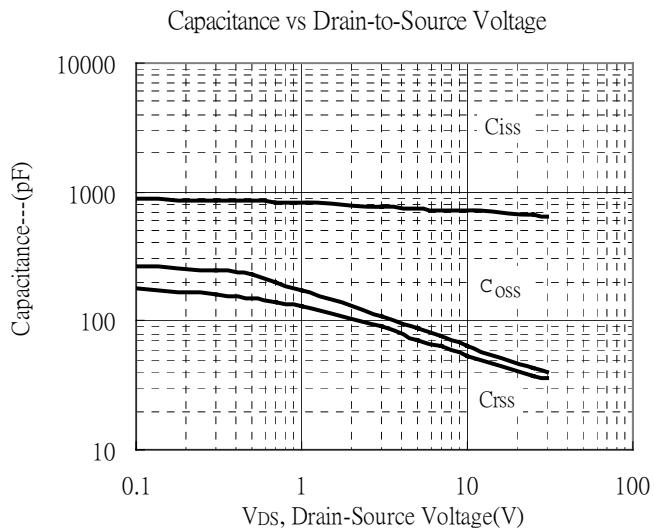
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

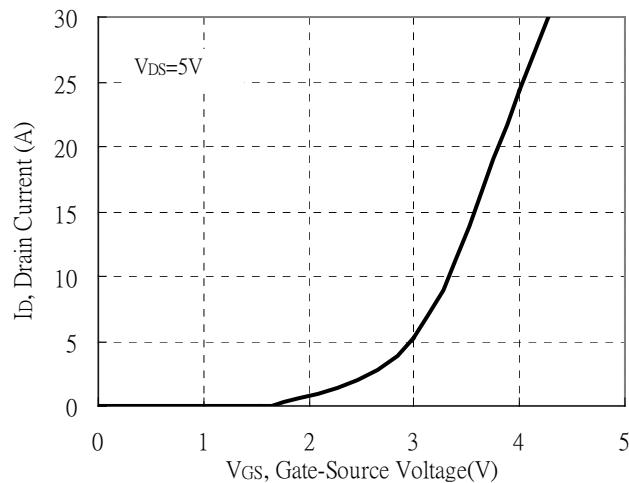


Q1, N-CH Typical Characteristics(Cont.)

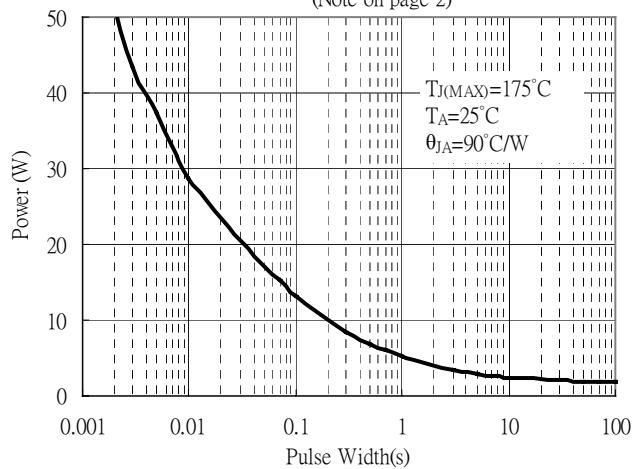


Q1, N-CH Typical Characteristics(Cont.)

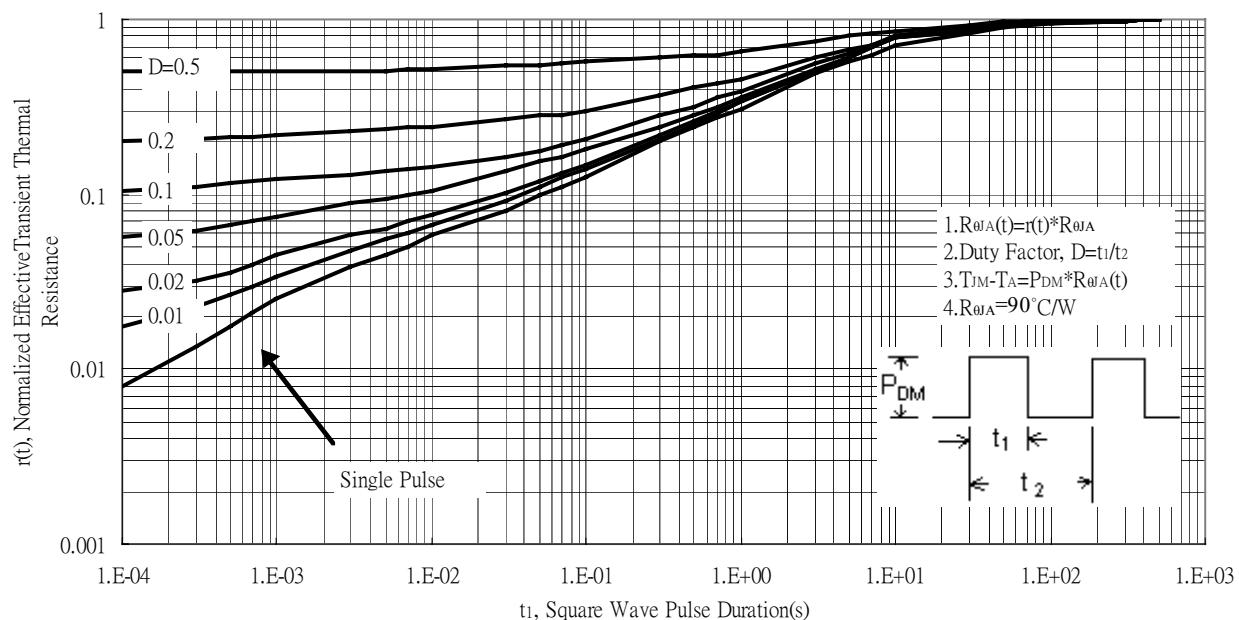
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)

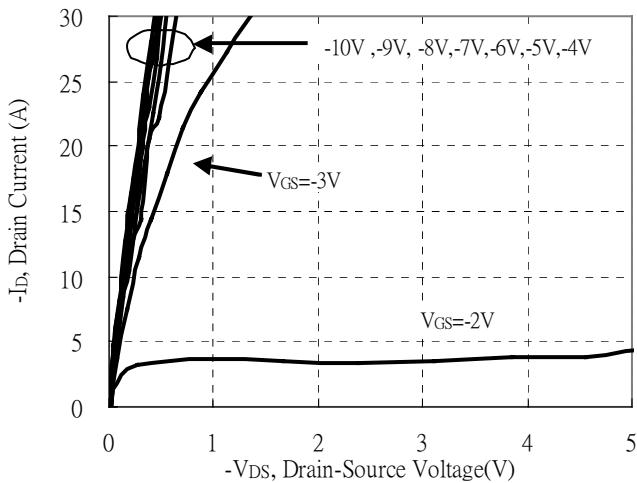


Transient Thermal Response Curves

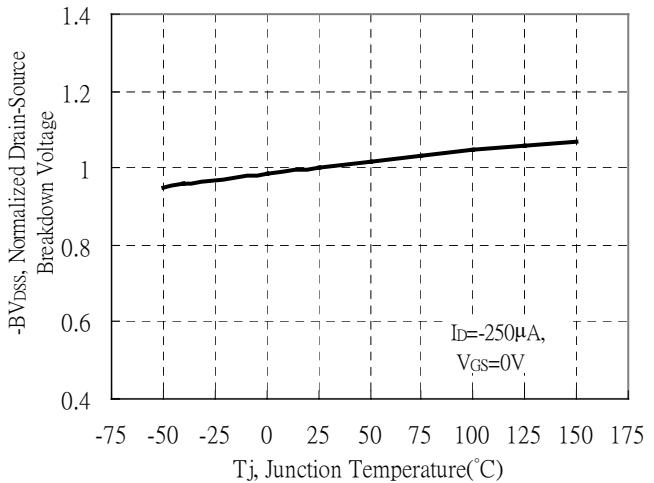


Q2, P-CH Typical Characteristics

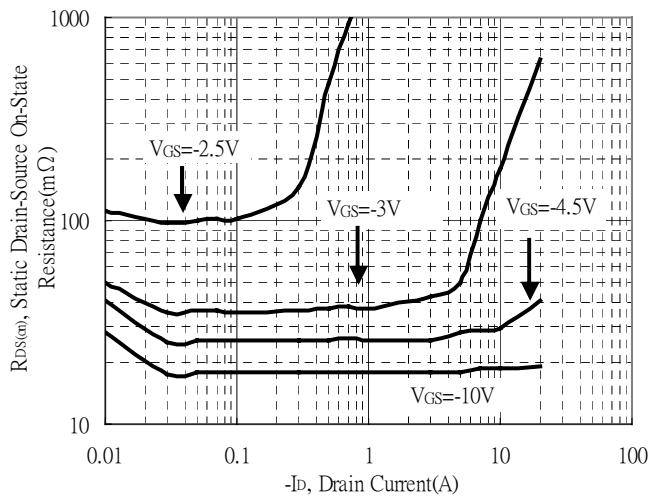
Typical Output Characteristics



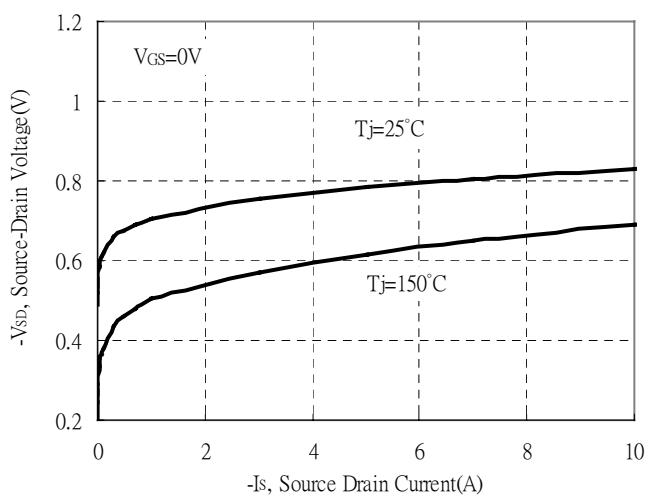
Breakdown Voltage vs Ambient Temperature



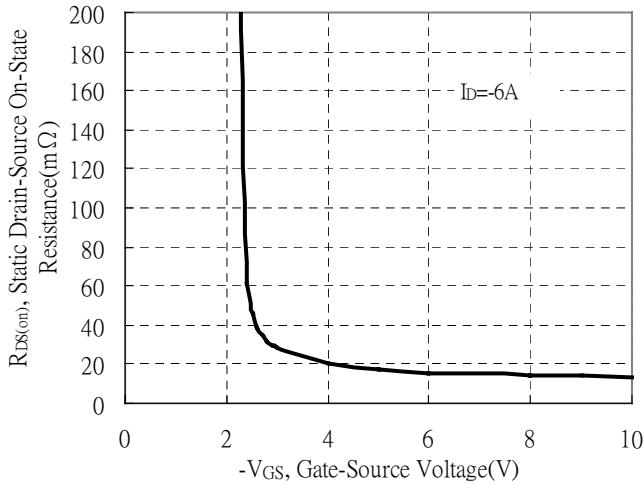
Static Drain-Source On-State resistance vs Drain Current



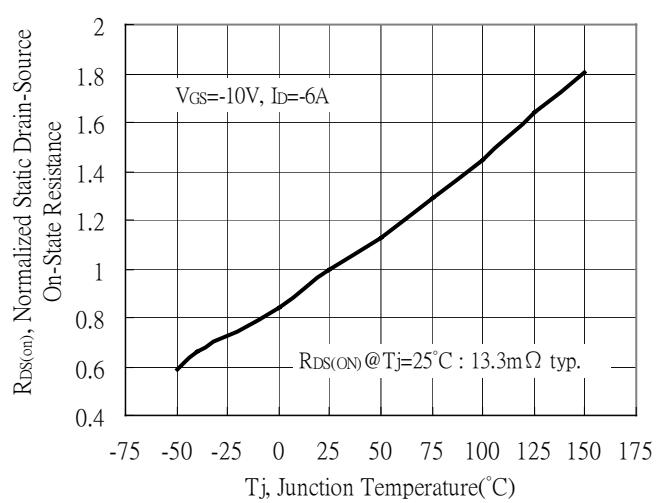
Source Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

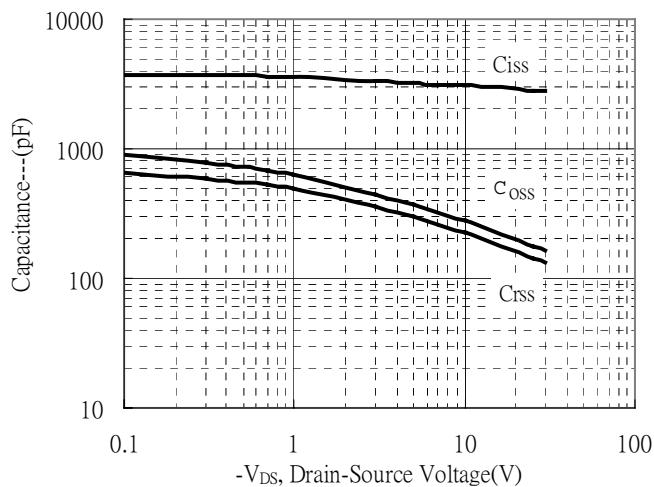


Drain-Source On-State Resistance vs Junction Temperature

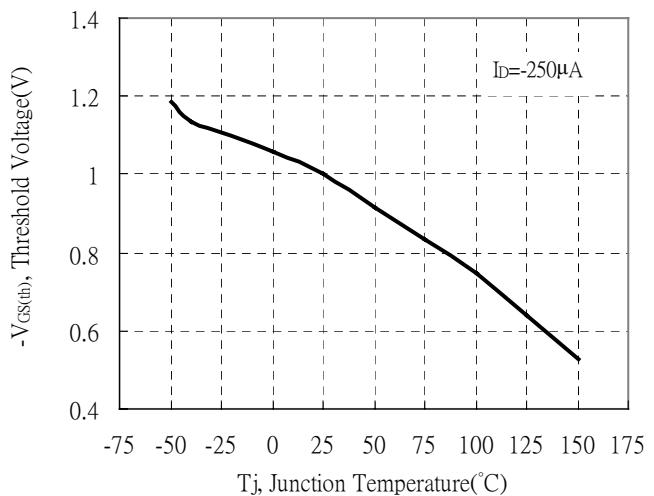


Q2, P-CH Typical Characteristics(Cont.)

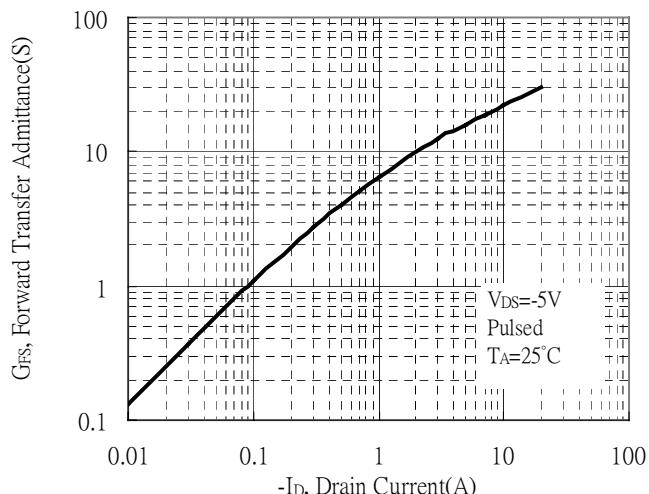
Capacitance vs Drain-to-Source Voltage



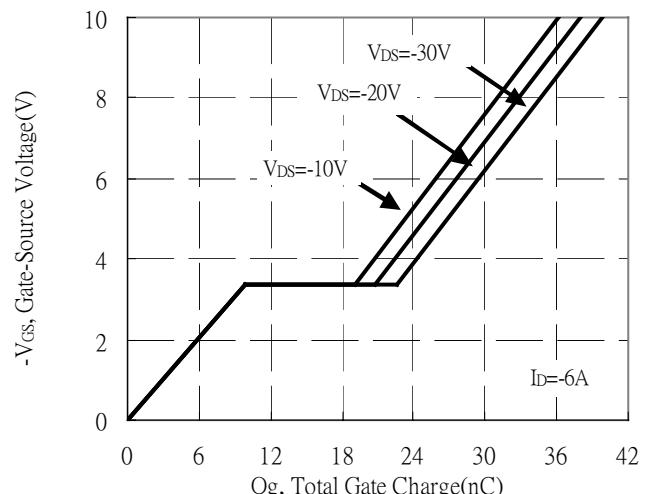
Threshold Voltage vs Junction Temperature



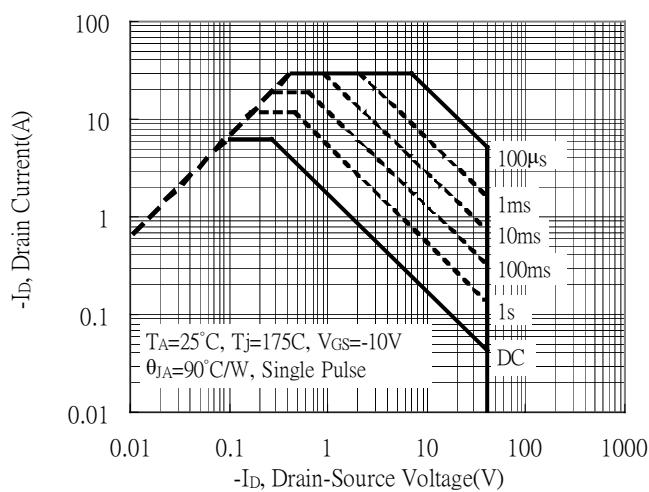
Forward Transfer Admittance vs Drain Current



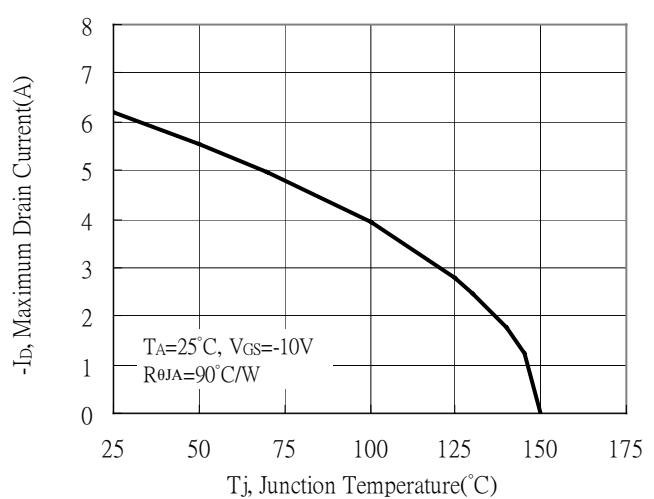
Gate Charge Characteristics



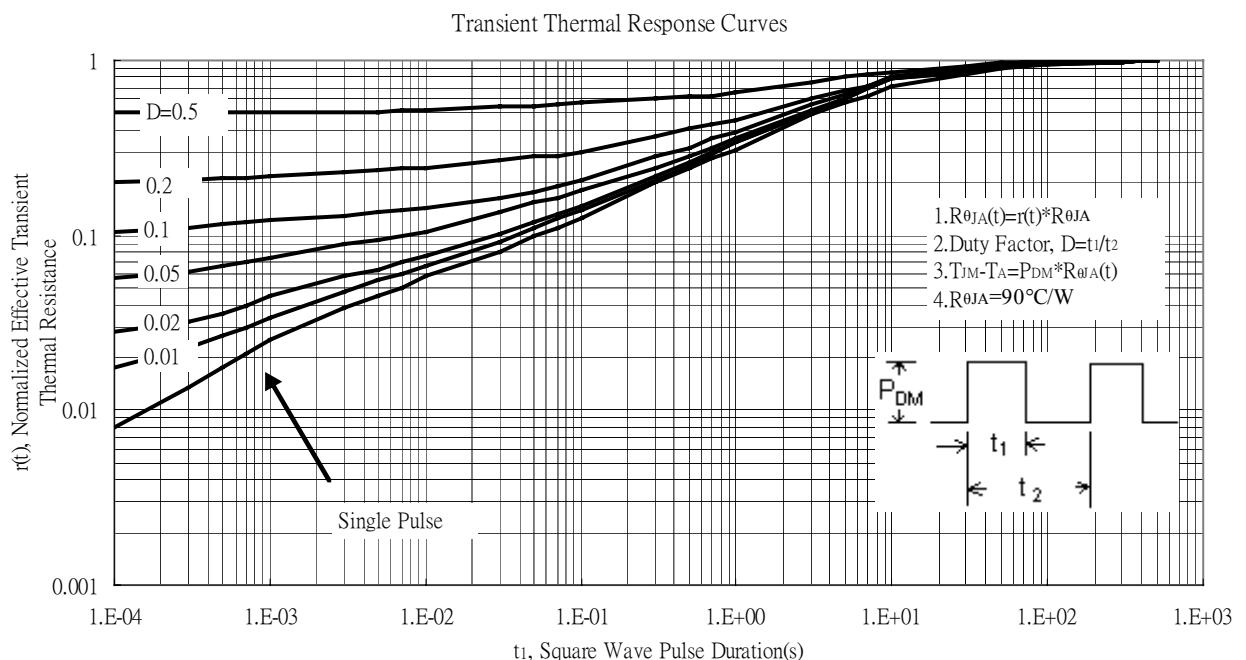
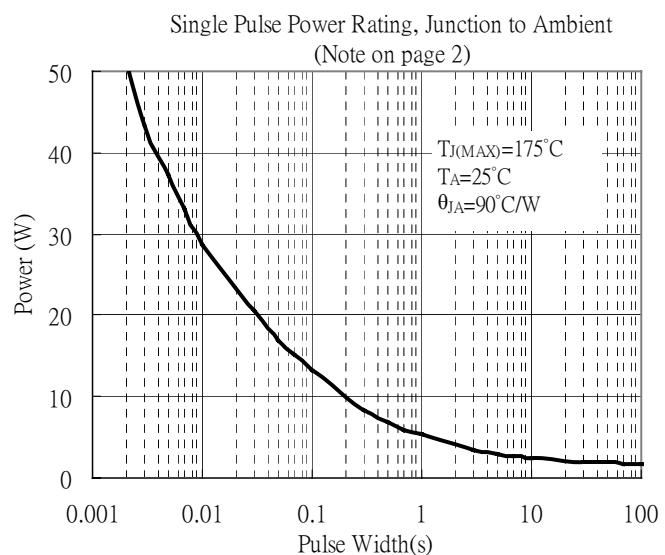
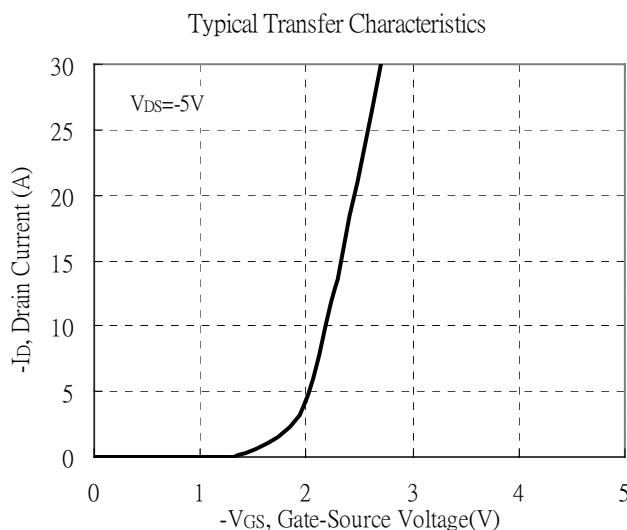
Maximum Safe Operating Area



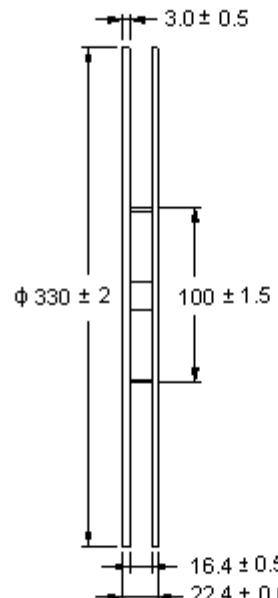
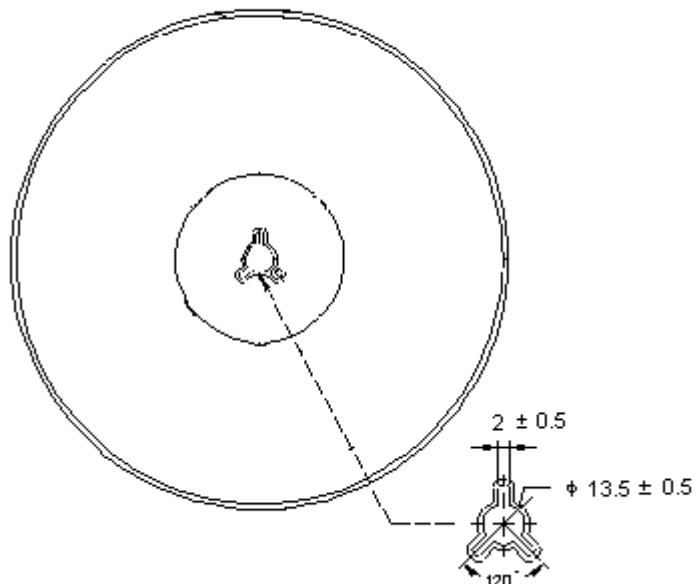
Maximum Drain Current vs Junction Temperature



Q2, P-CH Typical Characteristics(Cont.)

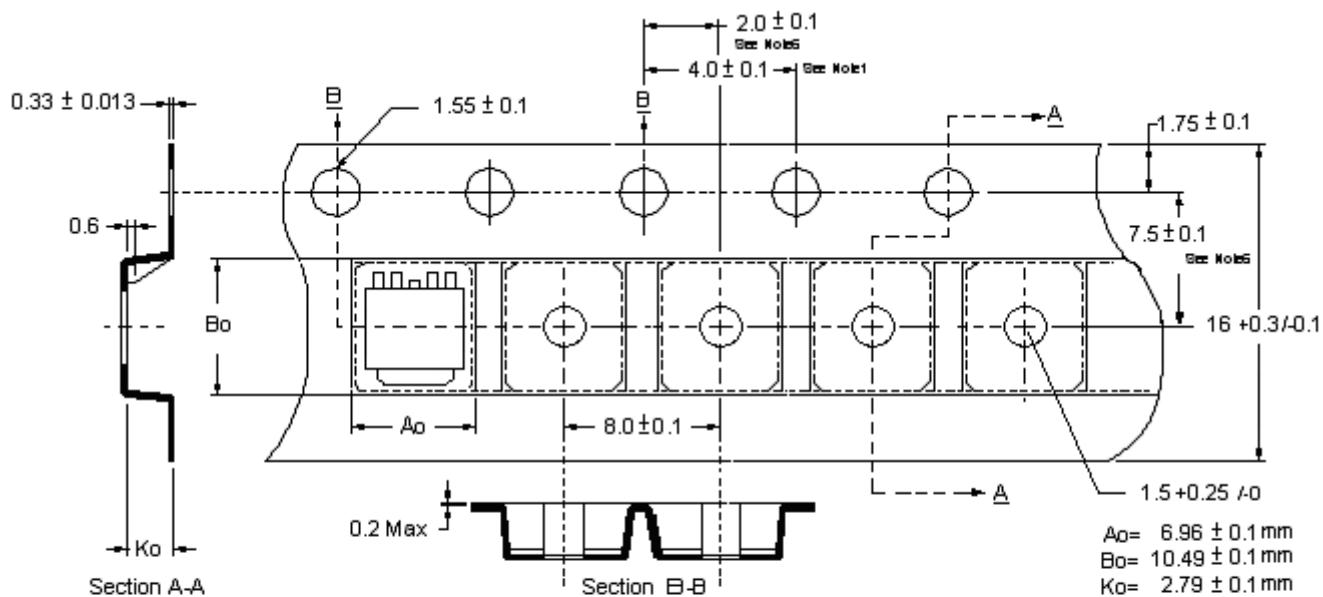


Reel Dimension



Unit: millimeter

Carrier Tape Dimension



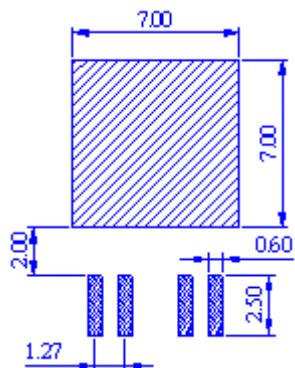
$$\begin{aligned} A_0 &= 6.96 \pm 0.1 \text{ mm} \\ B_0 &= 10.49 \pm 0.1 \text{ mm} \\ Ko &= 2.79 \pm 0.1 \text{ mm} \end{aligned}$$

Notes:

1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material : Conductive black polystyrene.
4. A_0 & B_0 measured on a plane 0.3mm above the bottom of the pocket.
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

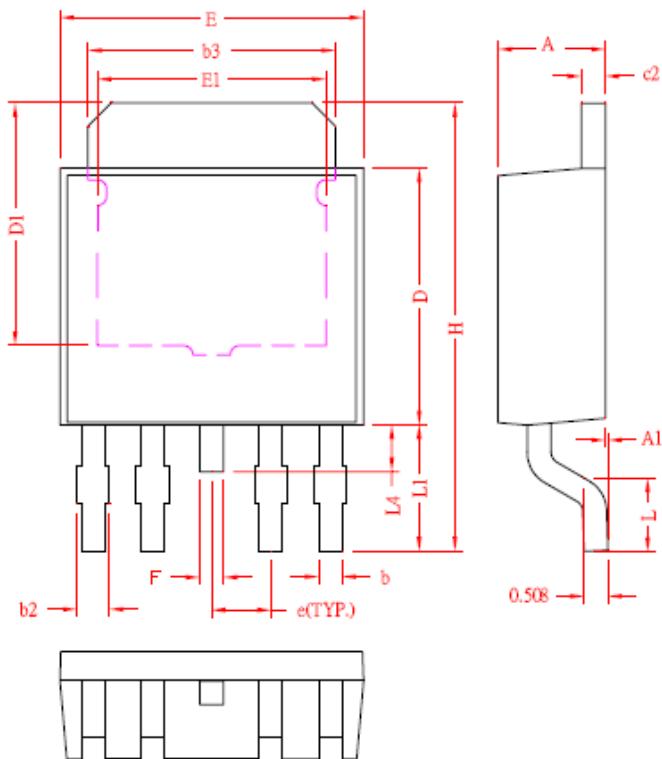
unit : millimeter

Recommended soldering footprint



Unit : mm

TO-252 Dimension

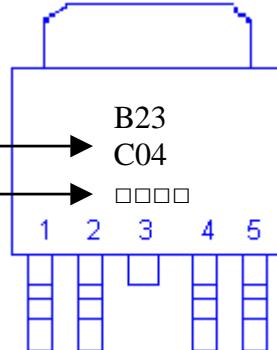


Marking:

Device Name

Date code

Tab



Style: Pin 1.Soure 1 2.Gate 1 3.&Tab
 Drain 1& Drain 2 4. Source 2 5. Gate 2

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0866	0.0945	2.20	2.40	E	0.2520	0.2677	6.40	6.80
A1	0.0000	0.0059	0.00	0.15	E1	0.1500	-	3.81	-
b	0.0157	0.0236	0.40	0.60	e	0.0500	REF	1.27	REF
b2	0.0199	0.0315	0.50	0.80	F	0.0157	0.0236	0.40	0.60
b3	0.2047	0.2165	5.20	5.50	H	0.3701	0.4016	9.40	10.20
c2	0.0177	0.0217	0.45	0.55	L	0.0551	0.0697	1.40	1.77
D	0.2126	0.2283	5.40	5.80	L1	0.0945	0.1181	2.40	3.00
D1	0.1799	-	4.57	-	L4	0.0315	0.0472	0.80	1.20