

P-Channel Enhancement Mode Power MOSFET

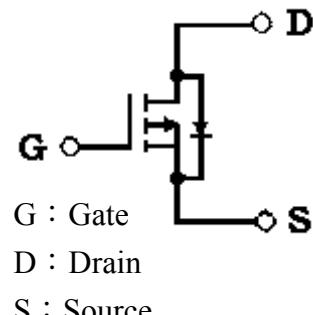
Features:

- Single Drive Requirement
- Low On-resistance
- Fast switching Characteristic
- Pb-free lead plating and halogen-free package

TO-252(DPAK)



BVDSS	-100V
ID@VGS=-10V, Tc=25°C	-24A
ID@VGS=-10V, Tc=100°C	-17A
ID@VGS=-10V, TA=25°C	-4.1A
ID@VGS=-10V, TA=70°C	-3.3A
RDS(ON)@VGS=-10V, ID=-15A	45 mΩ (typ)
RDS(ON)@VGS=-4.5V, ID=-12A	51 mΩ (typ)



Ordering Information

Device	Package	Shipping
KJB050P10	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel

Absolute Maximum Ratings ($T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_c=25^{\circ}\text{C}$	I_D	-24	A
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_c=100^{\circ}\text{C}$		-17	
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_A=25^{\circ}\text{C}$		-4.1	
Continuous Drain Current @ $V_{GS}=-10\text{V}$, $T_A=70^{\circ}\text{C}$		-3.3	
Pulsed Drain Current	I_{DM}	-96	
Power Dissipation	P_D	75	W
		37.5	
	P_{DSM}	2	
		1.3	
Single Pulse Avalanche Energy @ $L=1\text{mH}$, $I_{AS}=-24\text{A}$, $V_{DD}=-50\text{V}$	E_{AS}	288	mJ
Single Pulse Avalanche Current	I_{AS}	-24	A
Operating Junction and Storage Temperature	T_J , T_{stg}	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	2	°C/W
Thermal Resistance, Junction-to-ambient, max (Note2)	$R_{th,j-a}$	62.5	
Thermal Resistance, Junction-to-ambient, max (Note4)		90	

Note : 1.The power dissipation P_D is based on $T_J(\text{MAX})=175^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

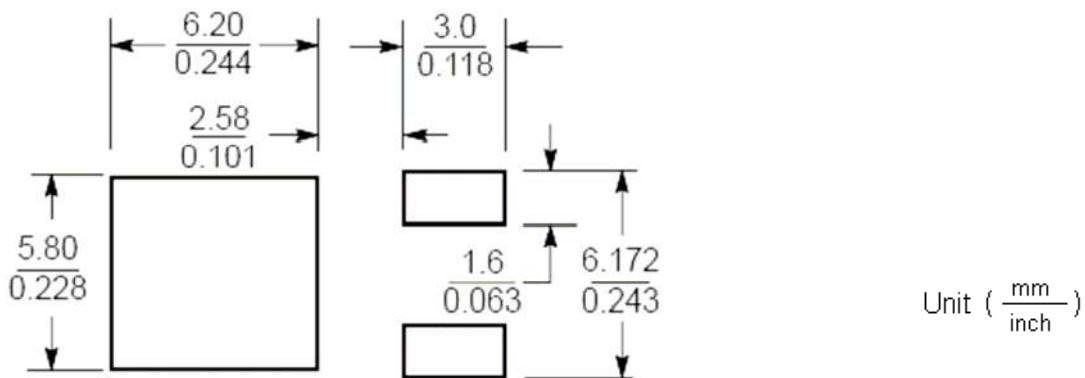
2. The value of $R_{th,JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The power dissipation P_{DSM} is based on $R_{th,JA}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating, pulse width limited by junction temperature $T_J(\text{MAX})=175^{\circ}\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^{\circ}\text{C}$.
4. When mounted on the minimum pad size recommended (PCB mount), $t \leq 10\text{s}$.

Characteristics (T_j=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-100	-	-	V	V _{GS} =0V, I _D =-250μA
ΔBV _{DSS} /ΔT _j	-	-0.08	-	V/°C	Reference to 25°C, I _D =-250μA
V _{GS(th)}	-1.0	-	-2.5	V	V _{DS} = V _{GS} , I _D =-250μA
G _{FS}	-	32.8	-	S	V _{DS} = -5V, I _D =-20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V
I _{DSS}	-	-	-1	μA	V _{DS} = -80V, V _{GS} = 0V
I _{DSS}	-	-	-25	μA	V _{DS} = -80V, V _{GS} = 0V, T _j =70°C
*R _{DSS(ON)}	-	45	60	mΩ	V _{GS} = -10V, I _D =-15A
*R _{DSS(ON)}	-	51	72	mΩ	V _{GS} = -4.5V, I _D =-12A
Dynamic					
*Q _g	-	51	-	nC	V _{DS} =-80V, I _D =-15A, V _{GS} =-10V
*Q _{gs}	-	6.5	-	nC	V _{DS} =-80V, I _D =-15A, V _{GS} =-10V
*Q _{gd}	-	14.3	-	nC	V _{DS} =-80V, I _D =-15A, V _{GS} =-10V
*t _{d(ON)}	-	13	-	ns	V _{DS} =-50V, I _D =-15A, V _{GS} =-10V, R _G =2.7Ω
*t _r	-	21.6	-	ns	V _{DS} =-50V, I _D =-15A, V _{GS} =-10V, R _G =2.7Ω
*t _{d(OFF)}	-	66.2	-	ns	V _{DS} =-50V, I _D =-15A, V _{GS} =-10V, R _G =2.7Ω
*t _f	-	15.2	-	ns	V _{DS} =-50V, I _D =-15A, V _{GS} =-10V, R _G =2.7Ω
C _{iss}	-	2421	-	pF	V _{GS} =0V, V _{DS} =-25V, f=1MHz
C _{oss}	-	211	-	pF	V _{GS} =0V, V _{DS} =-25V, f=1MHz
C _{rss}	-	93	-	pF	V _{GS} =0V, V _{DS} =-25V, f=1MHz
R _g	-	3.6	-	Ω	f=1MHz
Source-Drain Diode					
*I _s	-	-	-24	A	
*V _{SD}	-	-0.83	-1.2	V	I _s =-15A, V _{GS} =0V
*t _{rr}	-	25	-	ns	
*Q _{rr}	-	33	-	nC	I _F =-15A, V _{GS} =0, dI _F /dt=-100A/μs

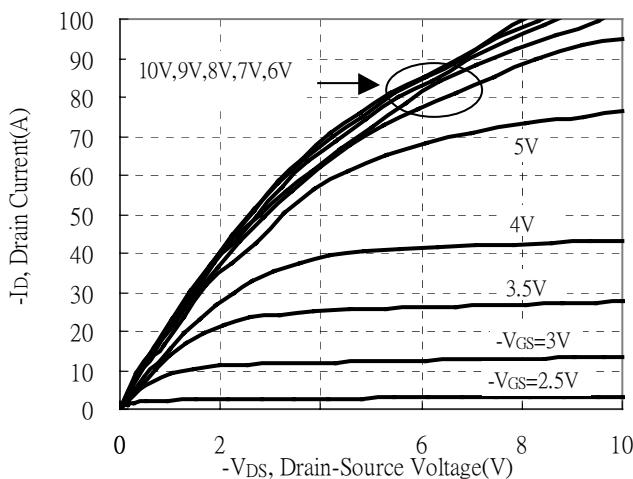
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended soldering footprint

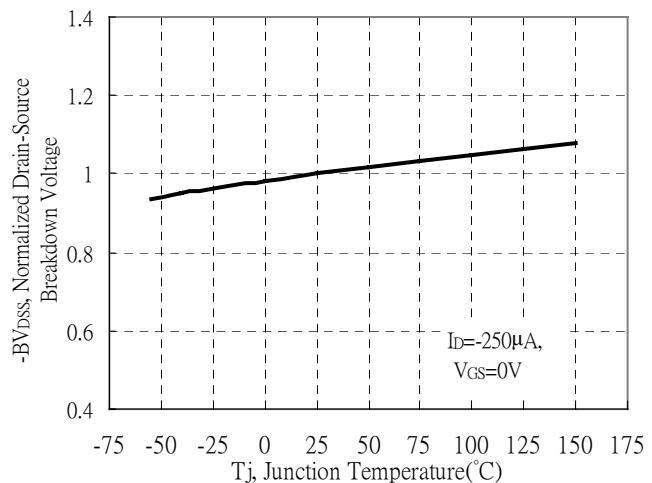


Typical Characteristics

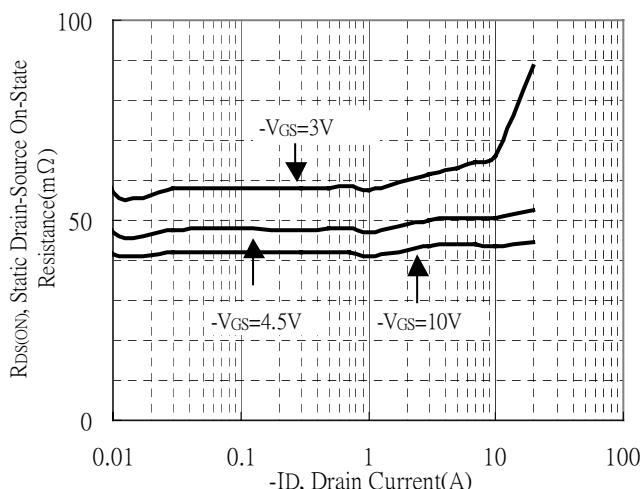
Typical Output Characteristics



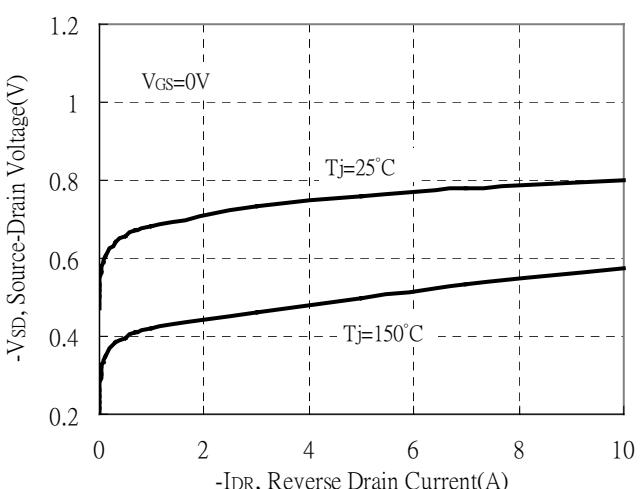
Breakdown Voltage vs Ambient Temperature



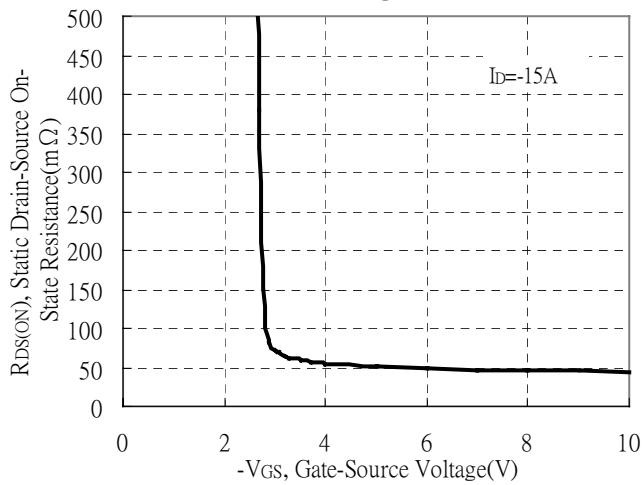
Static Drain-Source On-State resistance vs Drain Current



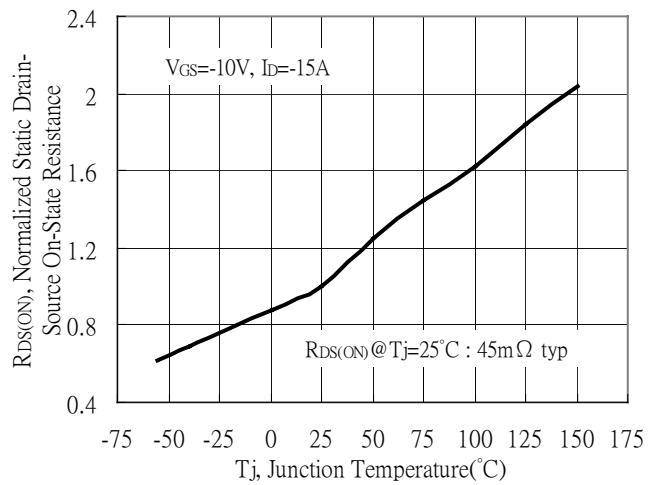
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

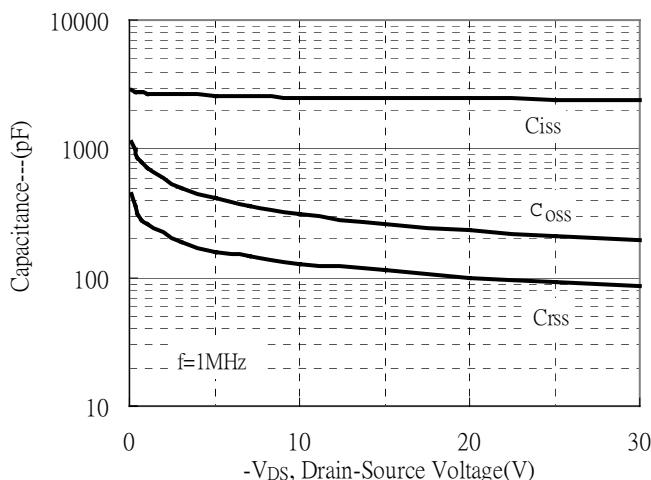


Drain-Source On-State Resistance vs Junction Temperature

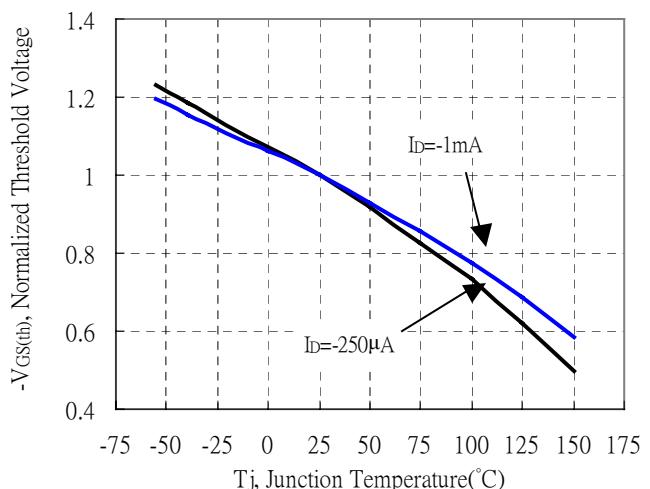


Typical Characteristics(Cont.)

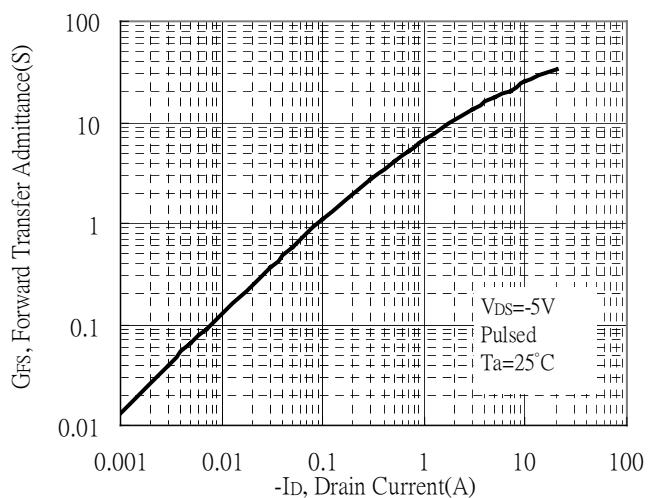
Capacitance vs Drain-to-Source Voltage



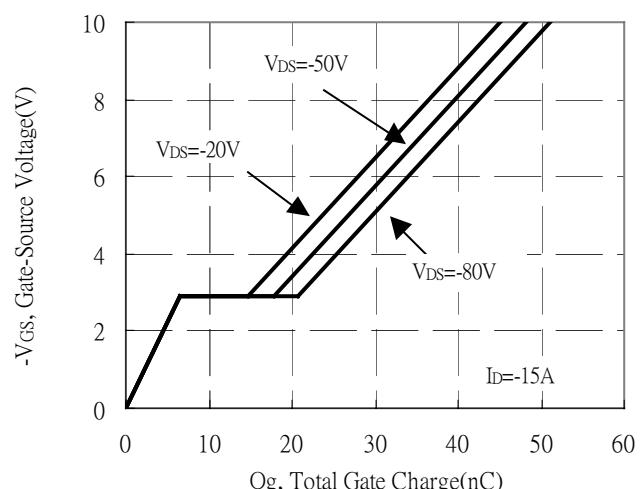
Threshold Voltage vs Junction Temperature



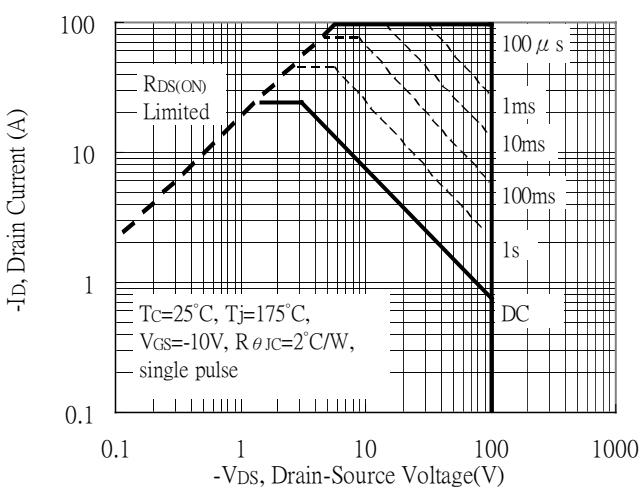
Forward Transfer Admittance vs Drain Current



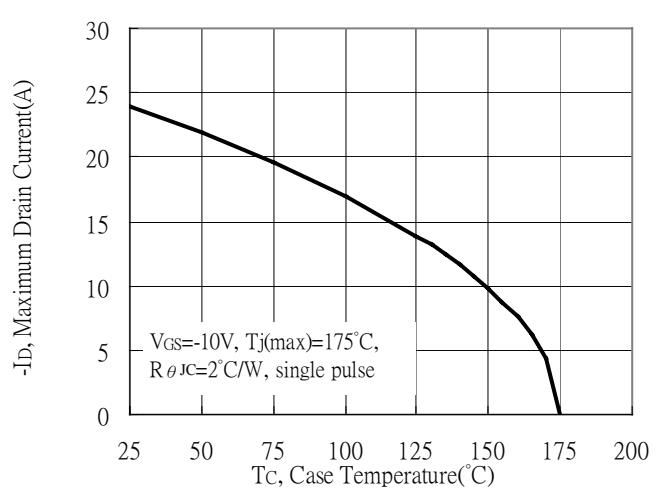
Gate Charge Characteristics



Maximum Safe Operating Area

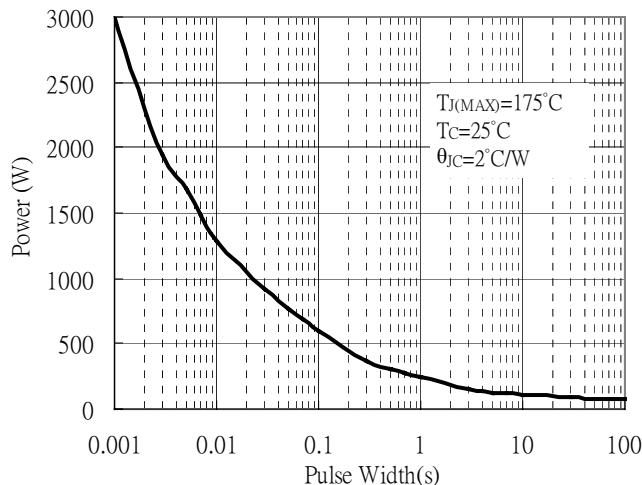


Maximum Drain Current vs Case Temperature

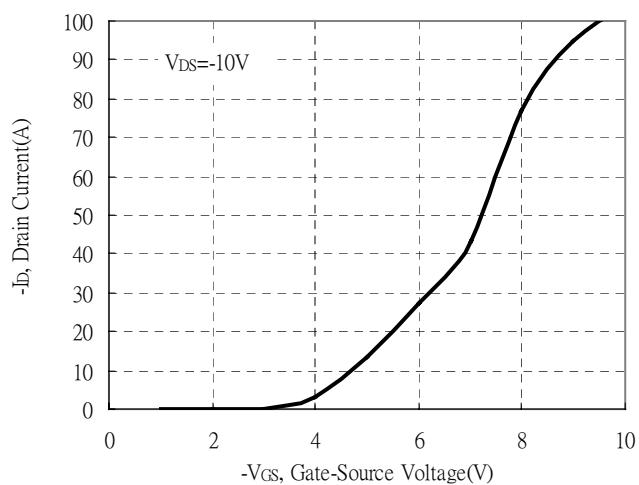


Typical Characteristics(Cont.)

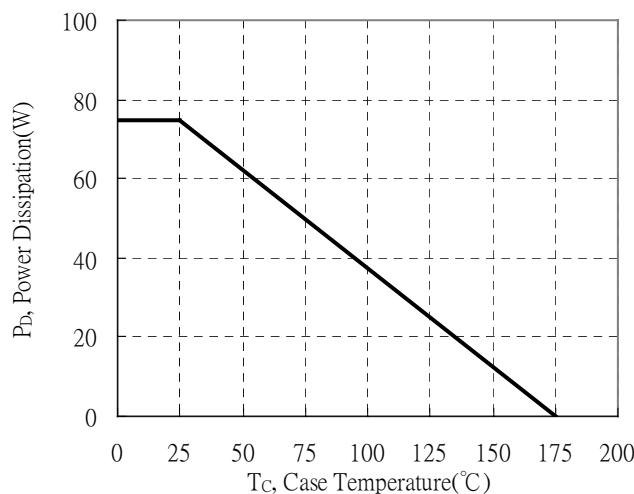
Single Pulse Power Rating, Junction to Case



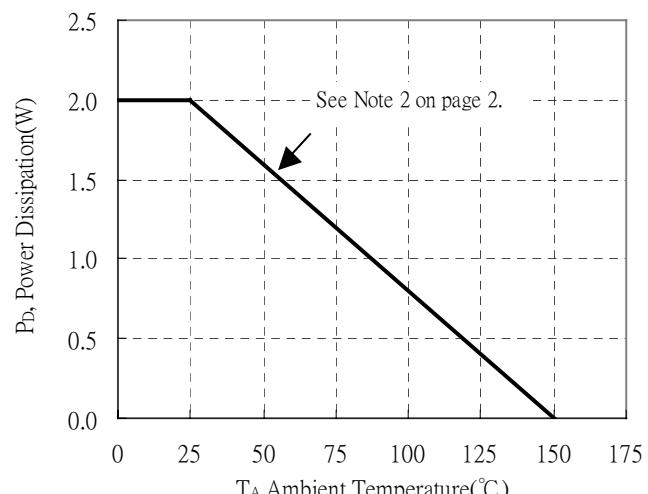
Typical Transfer Characteristics



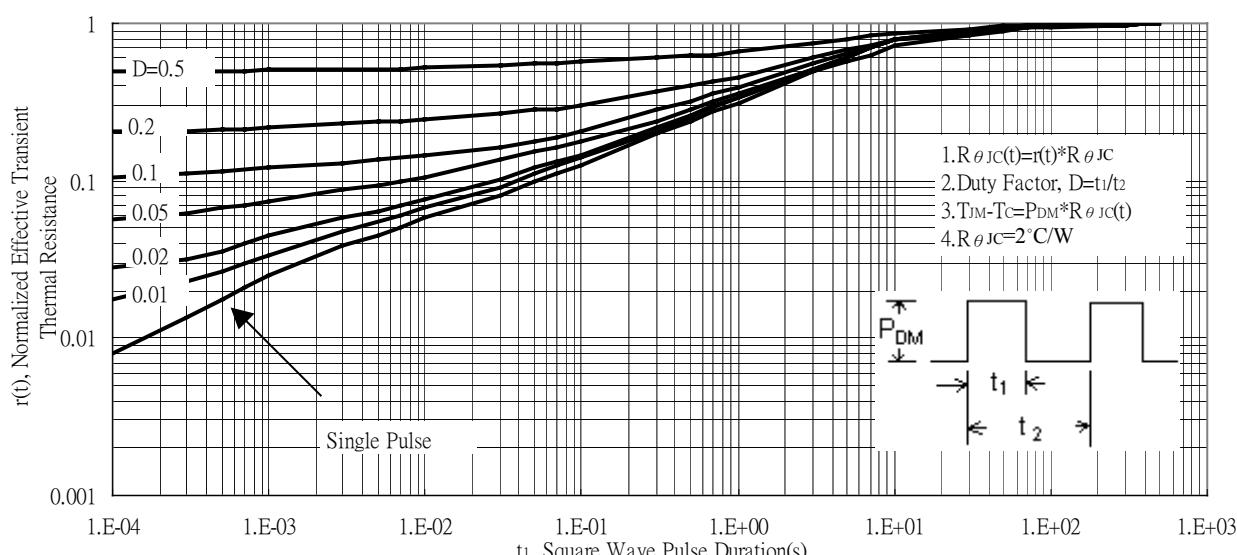
Power Derating Curve



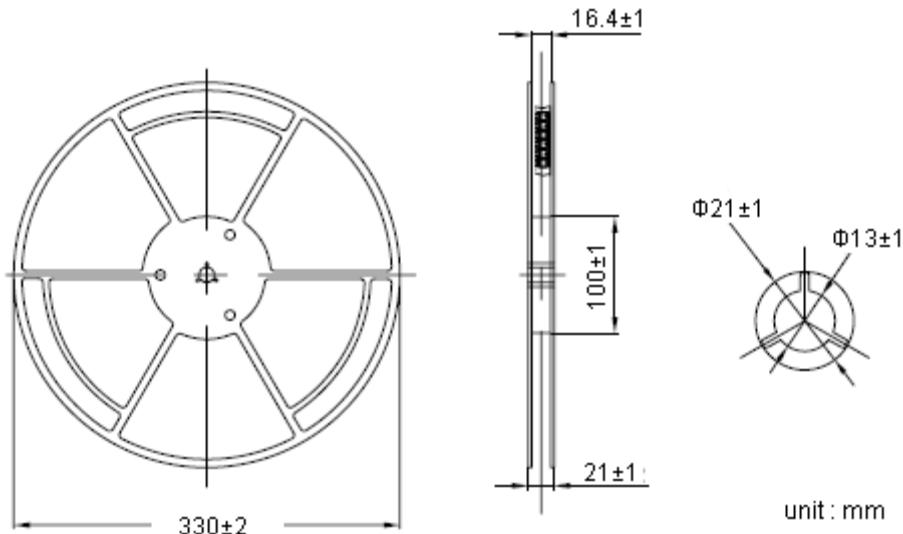
Power Derating Curve



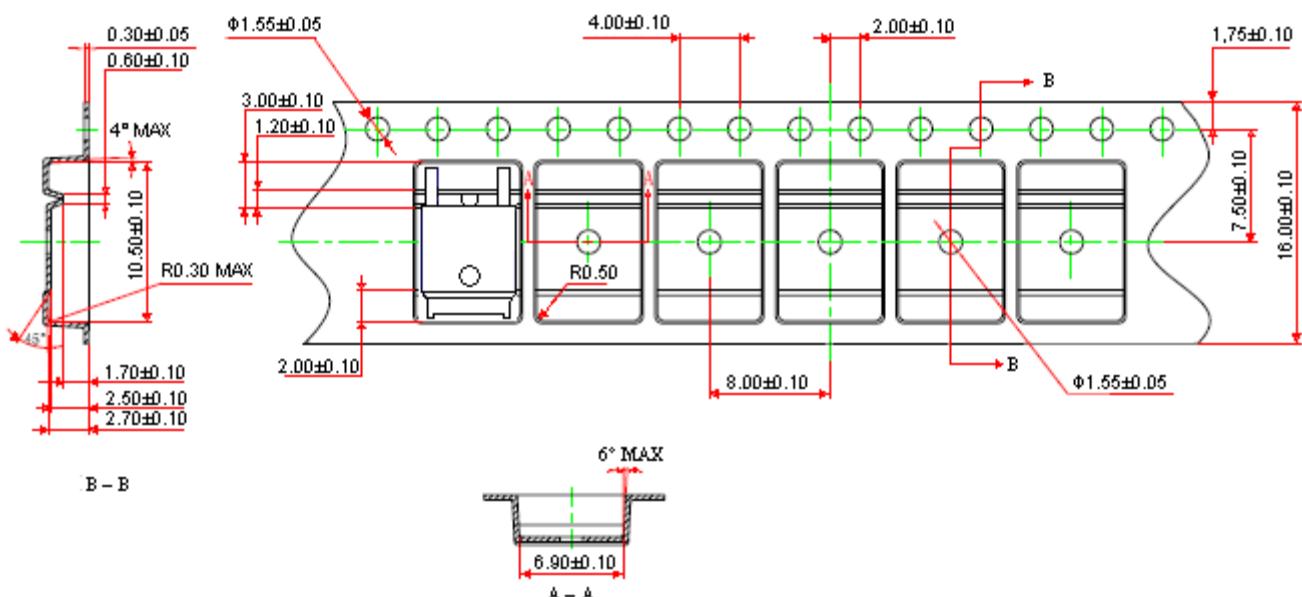
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension

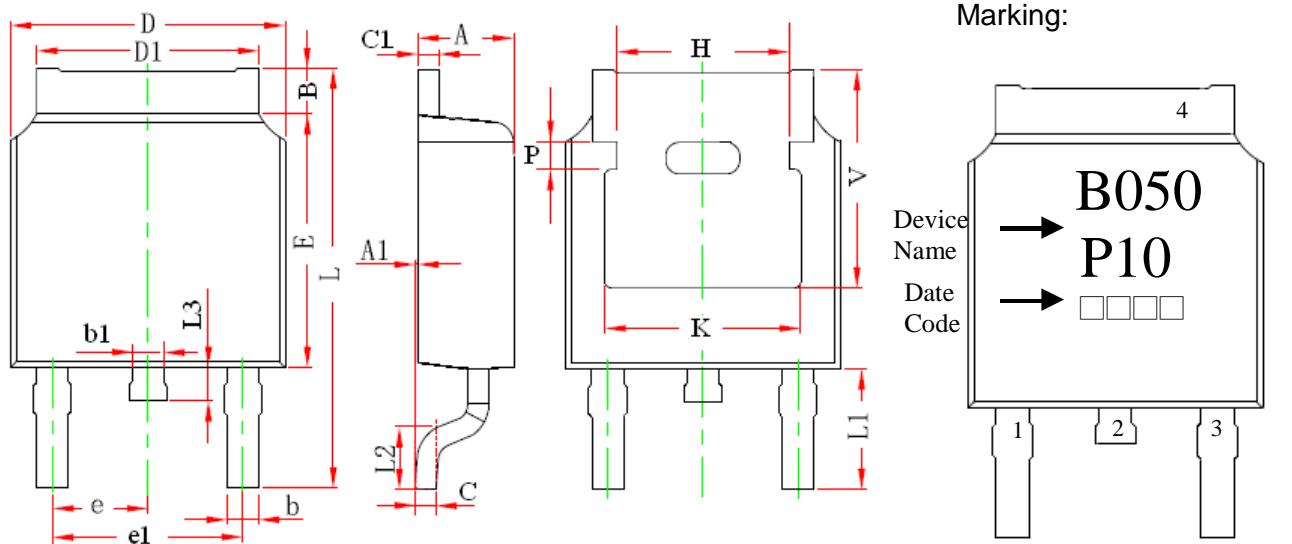


Notes:

1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material: conductive black polystyrene, antistatic coated : $10^5 \Omega/\square \sim 10^{11} \Omega/\square$

unit : mm

TO-252 Dimension



3-Lead TO-252 Plastic Surface Mount Package

Style: Pin 1.Gate 2.Drain 3.Source
 4.Drain

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF