

P-Channel Logic Level Enhancement Mode Power MOSFET

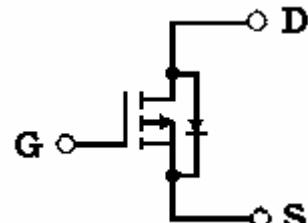
Features:

- Low Gate Charge
- Simple Drive Requirement
- Pb-free lead plating & Halogen-free package

TO-252(DPAK)



BV_{DSS}	-30V
I_D	-23A
$R_{DS(on)} @ V_{GS} = -10V, I_D = -10A$	35m Ω (typ.)
$R_{DS(on)} @ V_{GS} = -4.5V, I_D = -5A$	57m Ω (typ.)



G : Gate D : Drain
S : Source

Ordering Information

Device	Package	Shipping
KJ5103	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current @ $T_c=25^\circ\text{C}$	I_D	-23	A
Continuous Drain Current @ $T_c=100^\circ\text{C}$	I_D	-15	
Pulsed Drain Current *1	I_{DM}	-80	
Total Power Dissipation @ $T_c=25^\circ\text{C}$	P_d	25	W
Total Power Dissipation @ $T_c=100^\circ\text{C}$		10	
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55~+150	°C

Note : *1. Pulse width limited by maximum junction temperature

*2. Duty cycle $\leq 1\%$

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	5	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	50 (Note)	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	110	$^\circ\text{C}/\text{W}$

Note : When mounted on the minimum pad size recommended (PCB mount).

Characteristics ($T_c=25^\circ\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	-30	-	-	V	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$
$V_{GS(\text{th})}$	-1	-1.5	-2.5	V	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$
G_{FS} *1	-	7	-	S	$V_{DS}=-10\text{V}, I_D=-10\text{A}$
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 20, V_{DS}=0\text{V}$
Id_{SS}	-	-	-1	μA	$V_{DS}=-30\text{V}, V_{GS}=0\text{V}$
	-	-	-10		$V_{DS}=-30\text{V}, V_{GS}=0\text{V}, T_j=125^\circ\text{C}$
$R_{DS(\text{ON})}$ *1	-	35	50	$\text{m}\Omega$	$V_{GS}=-10\text{V}, I_D=-10\text{A}$
	-	57	70		$V_{GS}=-4.5\text{V}, I_D=-5\text{A}$
Dynamic					
Q_g *1, 2	-	10	-	nC	$I_D=-10\text{A}, V_{DS}=-24\text{V}, V_{GS}=-4.5\text{V}$
Q_{gs} *1, 2	-	3	-		
Q_{gd} *1, 2	-	5	-		
$t_{d(\text{ON})}$ *1, 2	-	5	-	ns	$V_{DS}=-15\text{V}, I_D=-10\text{A}, V_{GS}=-10\text{V}, R_G=3.3\Omega$
t_r *1, 2	-	4	-		
$t_{d(\text{OFF})}$ *1, 2	-	15	-		
t_f *1, 2	-	10	-		
C_{iss}	-	748	-	pF	$V_{GS}=0\text{V}, V_{DS}=-25\text{V}, f=1\text{MHz}$
C_{oss}	-	72	-		
C_{rss}	-	62	-		

Source-Drain Diode

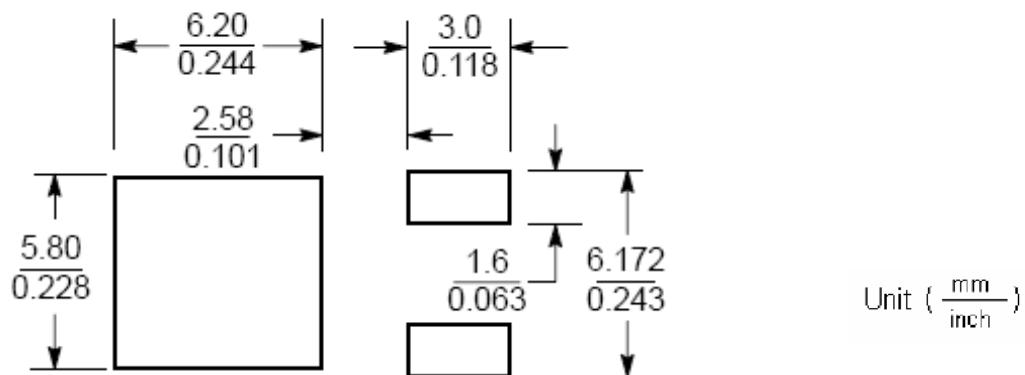
I _S *1	-	-	-5	A	
I _{SM} *3	-	-	-20		
V _{SD} *1	-	-0.9	-1.2	V	I _F =-5A, V _{GS} =0V
t _{rr}	-	20	-	ns	
Q _{rr}	-	10	-	nC	I _F =-10A, dI _F /dt=100A/μs

Note : *1.Pulse Test : Pulse Width \leq 300μs, Duty Cycle \leq 2%

*2.Independent of operating temperature

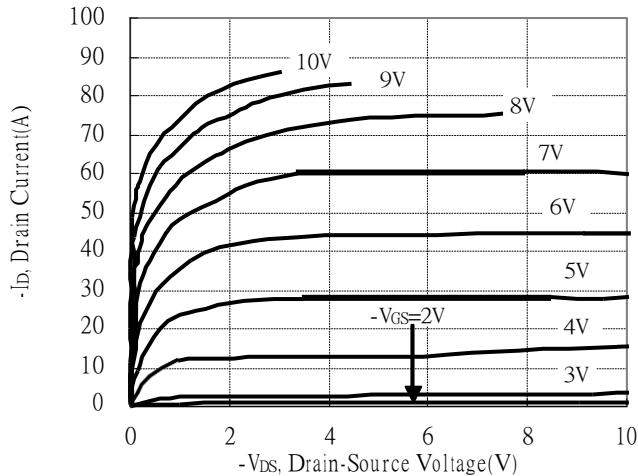
*3.Pulse width limited by maximum junction temperature.

Recommended soldering footprint

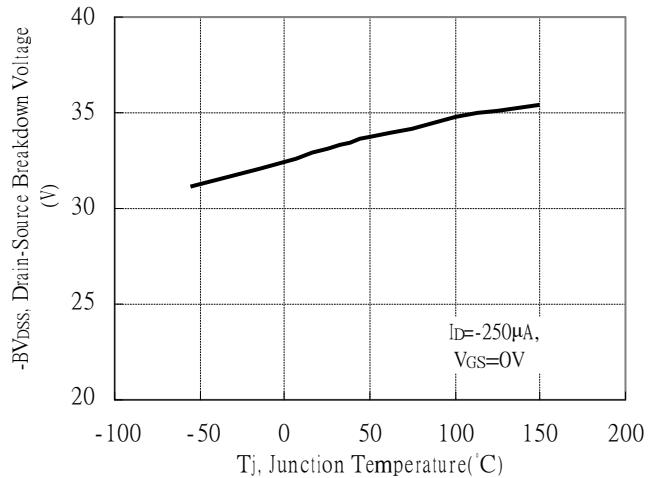


Typical Characteristics

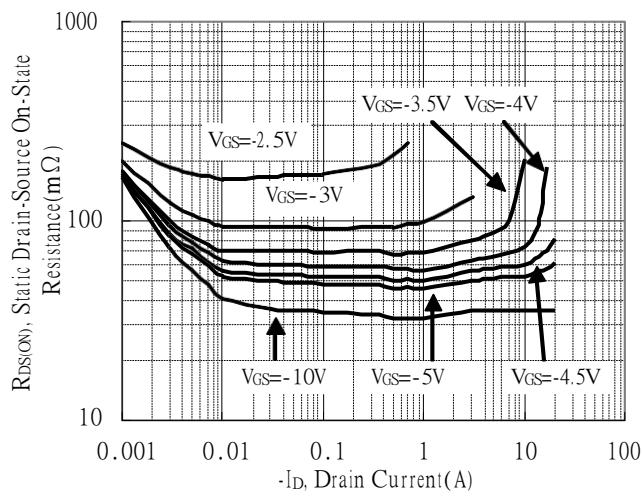
Typical Output Characteristics



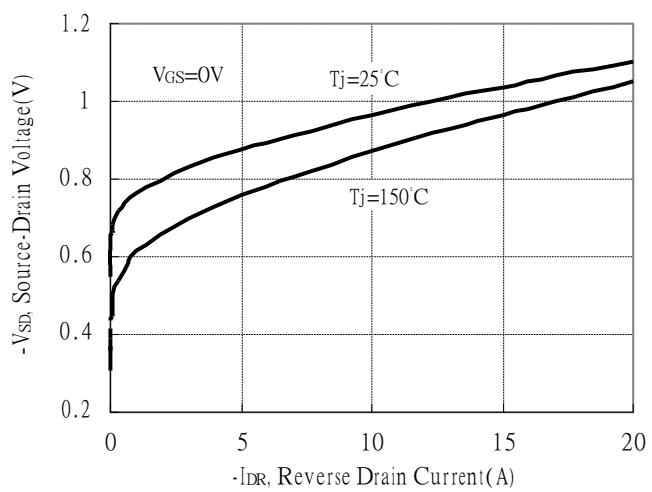
Breakdown Voltage vs Ambient Temperature



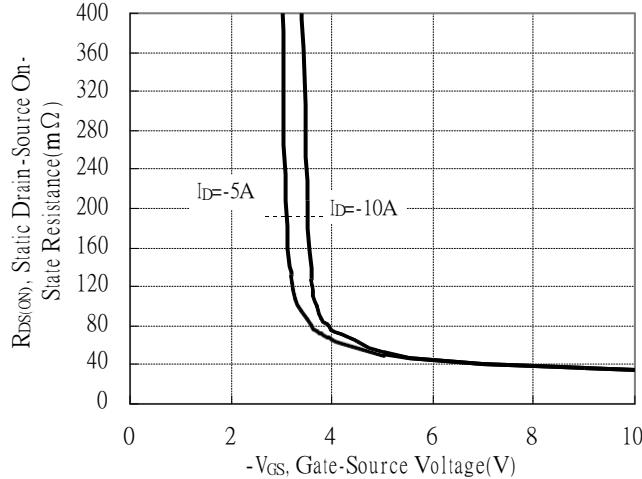
Static Drain-Source On-State resistance vs Drain Current



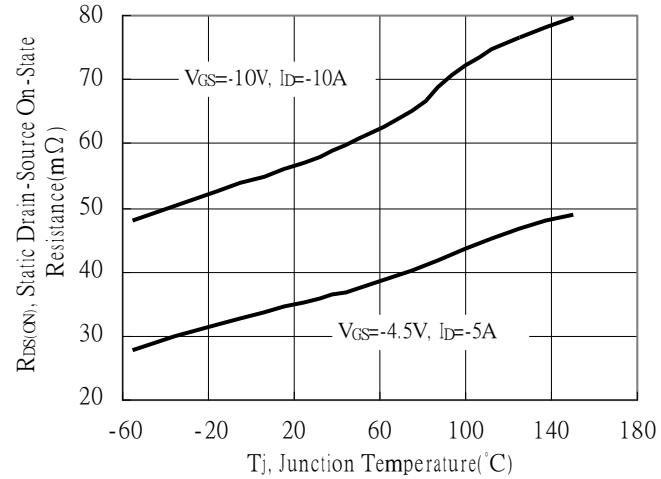
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

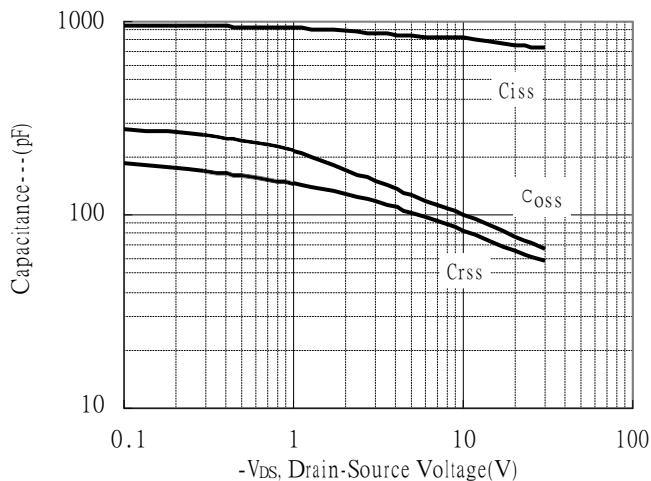


Drain-Source On-State Resistance vs Junction Temperature

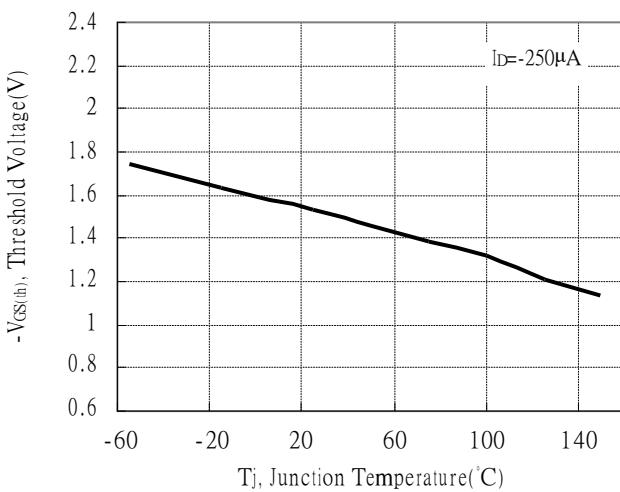


Typical Characteristics(Cont.)

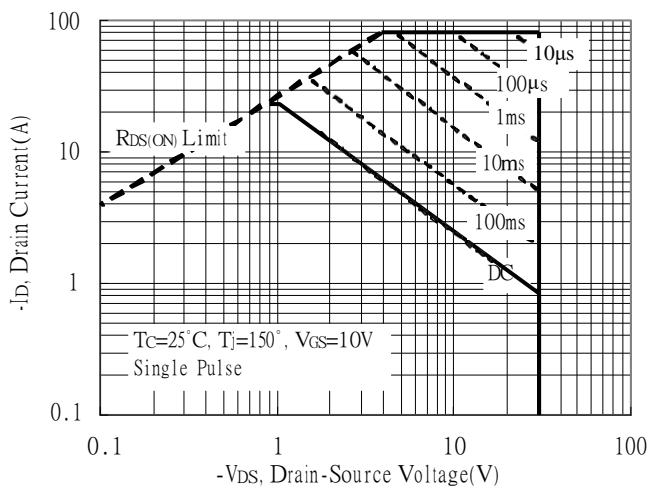
Capacitance vs Drain-to-Source Voltage



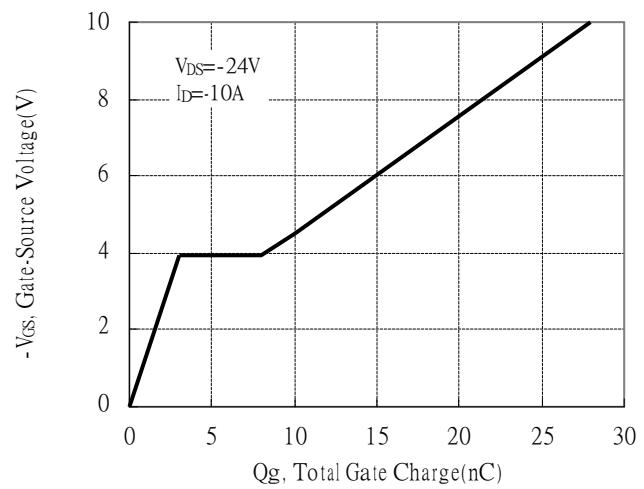
Threshold Voltage vs Junction Temperature



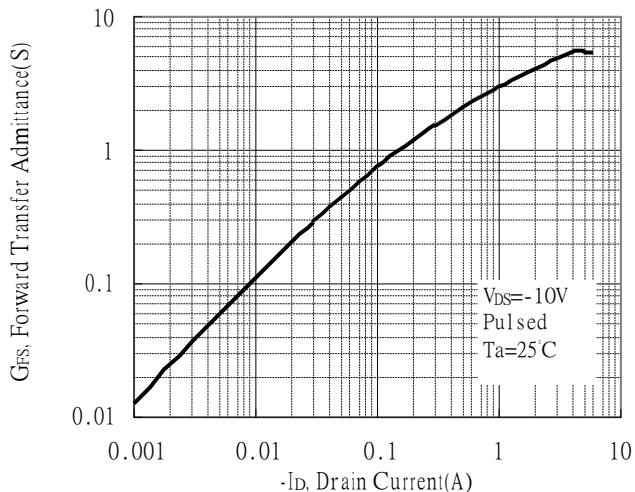
Maximum Safe Operating Area



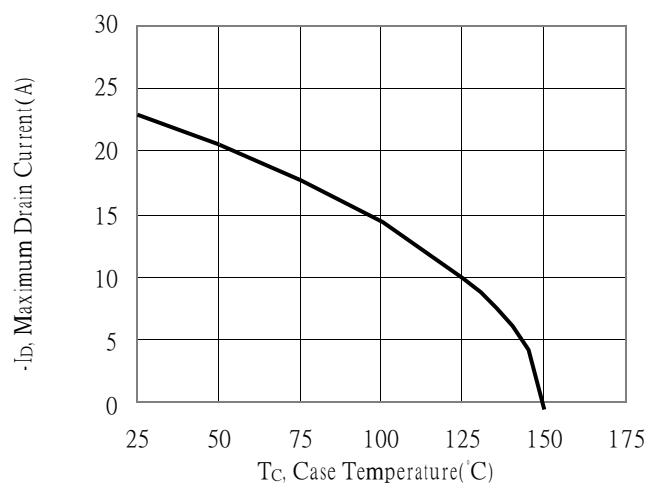
Gate Charge Characteristics



Forward Transfer Admittance vs Drain Current

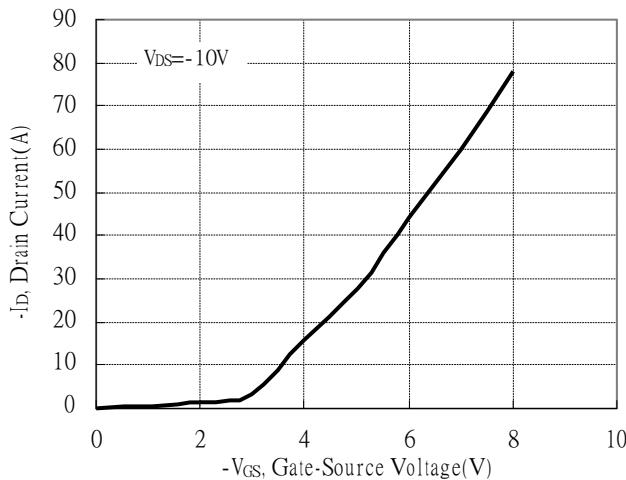


Maximum Drain Current vs Case Temperature

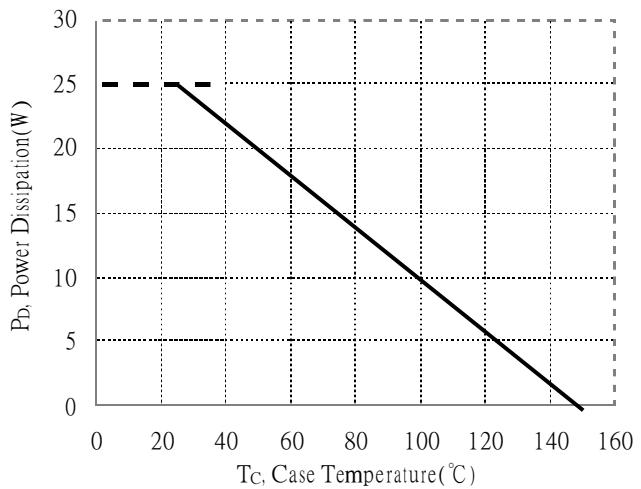


Typical Characteristics(Cont.)

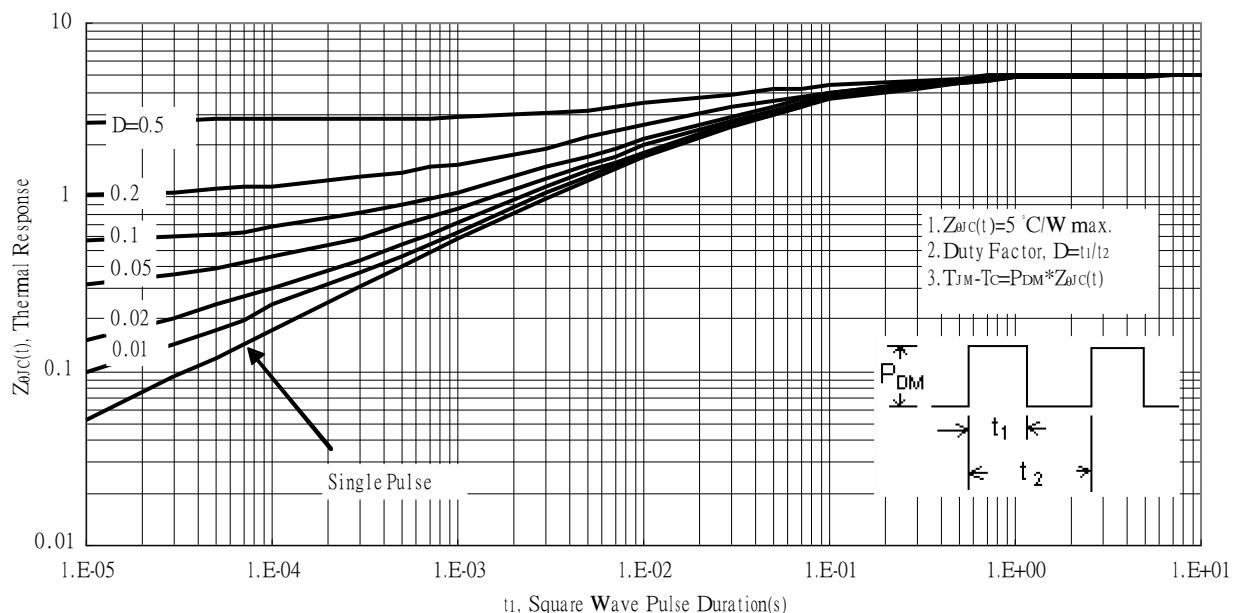
Typical Transfer Characteristics



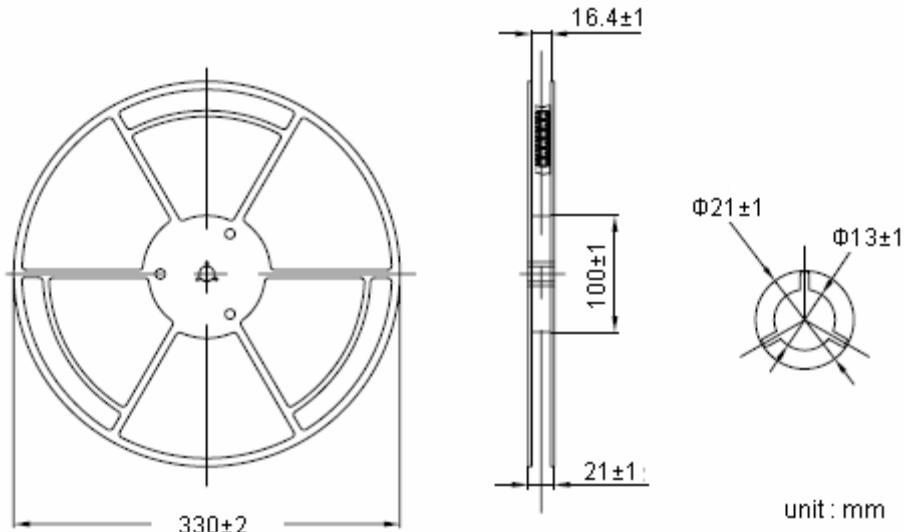
Power Derating Curve



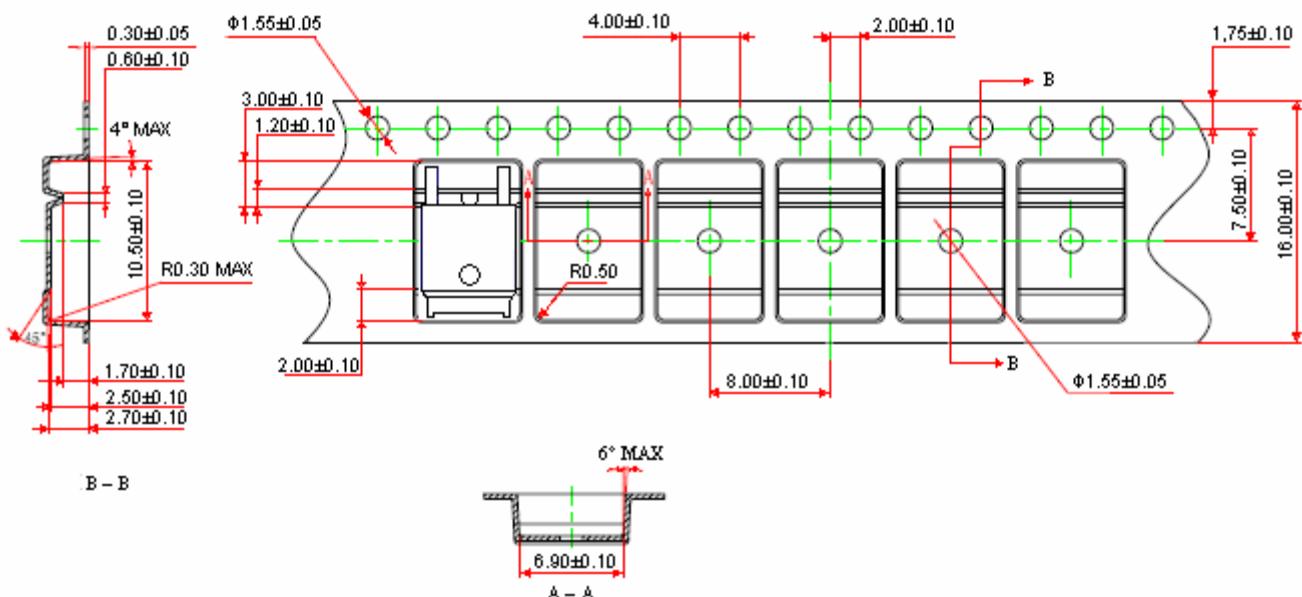
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension

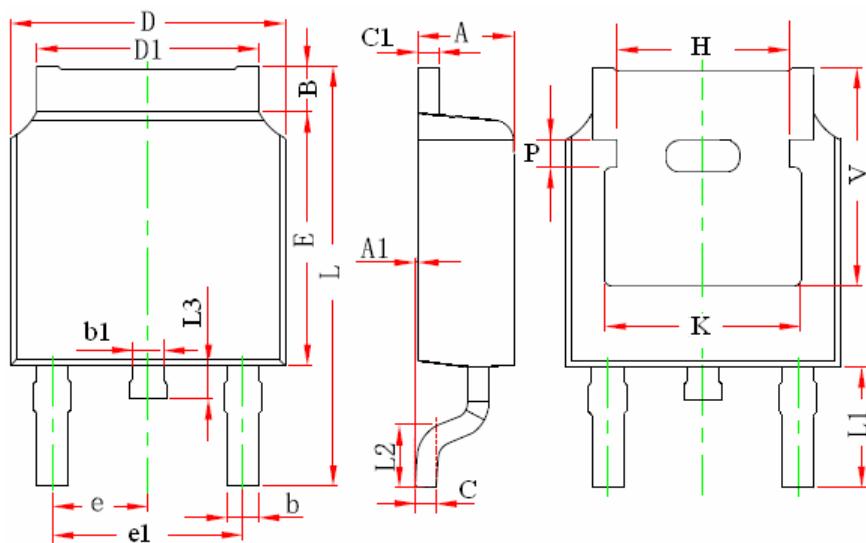


Notes:

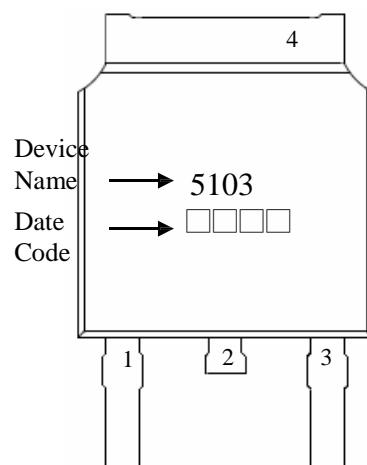
1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material: conductive black polystyrene, antistatic coated : $10^5 \Omega/\square \sim 10^{11} \Omega/\square$

unit : mm

TO-252 Dimension



Marking:



3-Lead TO-252 Plastic Surface Mount Package
 Package Code: J3

Style: Pin 1.Gate 2.Drain 3.Source
 4.Drain

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF