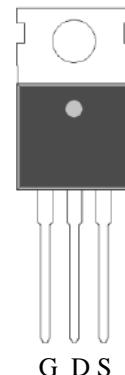


N-Channel Enhancement Mode Power MOSFET

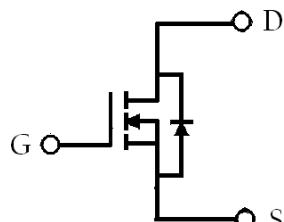
Features:

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- RoHS compliant package

TO-220



G D S



G : Gate D : Drain S : Source

Ordering Information

Device	Package	Shipping
KEB1D6N04R	TO-220 (RoHS compliant)	50 pcs/tube, 20 tubes/box, 5 boxes / carton

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage (Note 1)	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current @ $T_c=25^\circ\text{C}$, $V_{GS}=10\text{V}$ (Note 1)	I_D	132*	A
Continuous Drain Current @ $T_c=100^\circ\text{C}$, $V_{GS}=10\text{V}$ (Note 1)		84*	
Continuous Drain Current @ $T_A=25^\circ\text{C}$, $V_{GS}=10\text{V}$ (Note 2)	I_{DSM}	27	
Continuous Drain Current @ $T_A=70^\circ\text{C}$, $V_{GS}=10\text{V}$ (Note 2)		21.5	
Pulsed Drain Current	I_{DM}	528*	
Avalanche Current @ $L=0.1\text{mH}$	I_{AS}	35	
Avalanche Energy @ $L=0.5\text{mH}$	E_{AS}	132	mJ
Power Dissipation	P_D	82	W
		32.8	
	P_{DSM}	3.4	
		2.2	
Maximum Temperature for Soldering @ Lead at 0.063 in(1.6mm)	T_L	300	°C
Maximum Temperature for Soldering @ Package Body for 10	T_{PKG}	260	
Operating Junction and Storage Temperature	T_J, T_{stg}	-55~+150	

*Drain current limited by maximum junction temperature

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R_{JC}	1.52	°C/W
Thermal Resistance, Junction-to-ambient, max (Note 2)	R_{JA}	37	

- Note : 1.The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
 2. The value of R_{JA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^\circ\text{C}$. The power dissipation P_{DSM} is based on R_{JA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
 3. Pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.

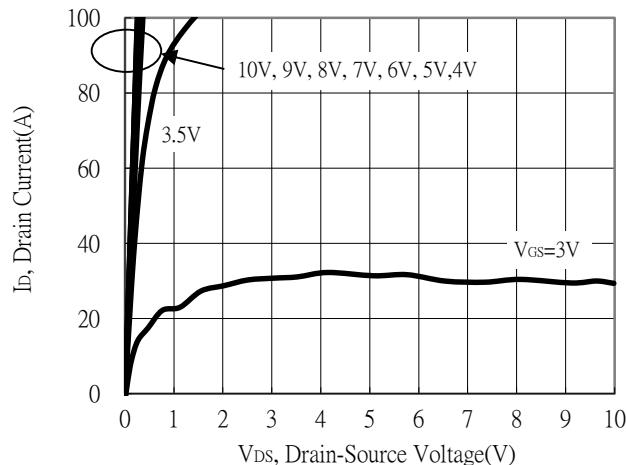
Characteristics ($T_j=25^\circ\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Static						
BVDSS	40	-	-	V	V _{GS} =0V, I _D =250μA	
ΔBV _{DSS} /ΔT _j	-	50	-	mV/°C	Reference to 25°C, I _D =250μA	
V _{GS(th)}	1	-	2.5	V	V _{DS} =V _{GS} , I _D =250μA	
*G _{FS}	-	49.2	-	S	V _{DS} =5V, I _D =20A	
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V	
I _{DSS}	-	-	1	μA	V _{DS} =32V, V _{GS} =0V	
	-	-	5		V _{DS} =32V, V _{GS} =0V, T _j =55°C	
*R _{DSS(ON)}	-	1.8	2.3	mΩ	V _{GS} =10V, I _D =20A	
	-	2.8	3.9		V _{GS} =4.5V, I _D =20A	
Dynamic						
*Q _g	-	31	-	nC	V _{DS} =20V, I _D =20A, V _{GS} =4.5V	
*Q _{gs}	-	11	-			
*Q _{gd}	-	11	-	ns	V _{DD} =20V, I _D =20A, V _{GS} =10V, R _G =3Ω	
*t _{d(ON)}	-	21	-			
*t _r	-	17	-			
*t _{d(OFF)}	-	63	-			
*t _f	-	12	-	pF	V _{GS} =0V, V _{DS} =20V, f=1MHz	
C _{iss}	-	3660	-			
C _{oss}	-	1969	-			
C _{rss}	-	133	-	Ω	f=1MHz	
R _g	-	0.62	-			
Source-Drain Diode						
*I _S	-	-	68	A	I _S =20A, V _{GS} =0V	
*I _{SM}	-	-	272			
*V _{SD}	-	0.8	1.2	V	I _S =20A, V _{GS} =0V	
*trr	-	48	-	ns	V _{GS} =0V, I _F =20A, dI _F /dt=100A/μs	
*Qrr	-	40	-			

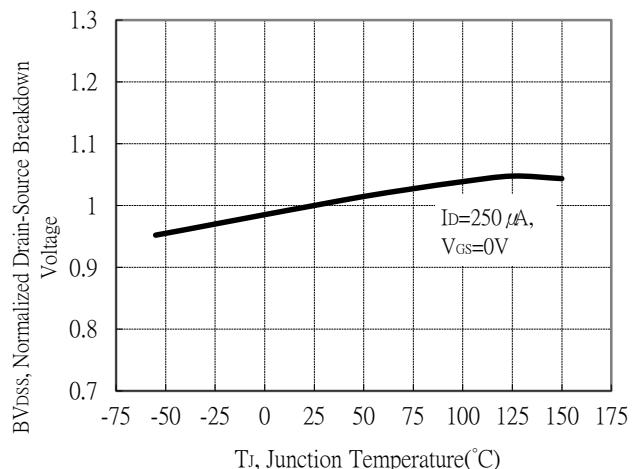
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

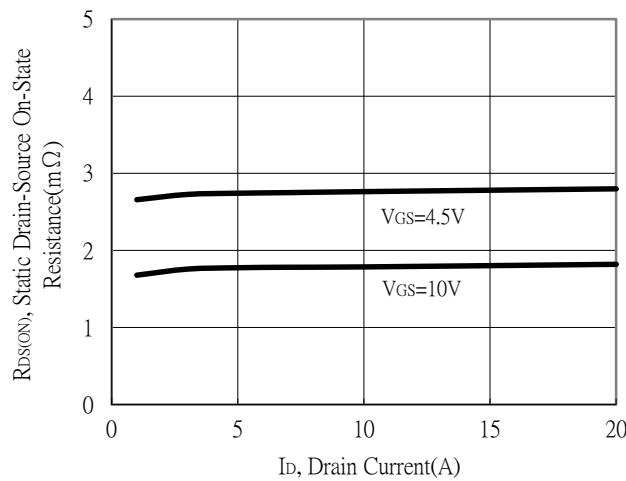
Typical Output Characteristics



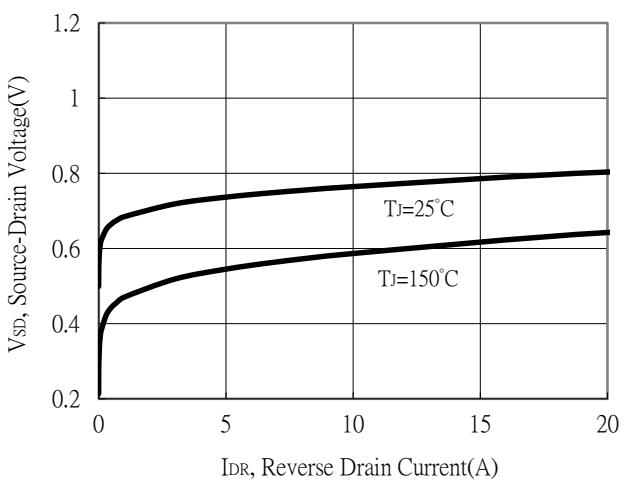
Breakdown Voltage vs Ambient Temperature



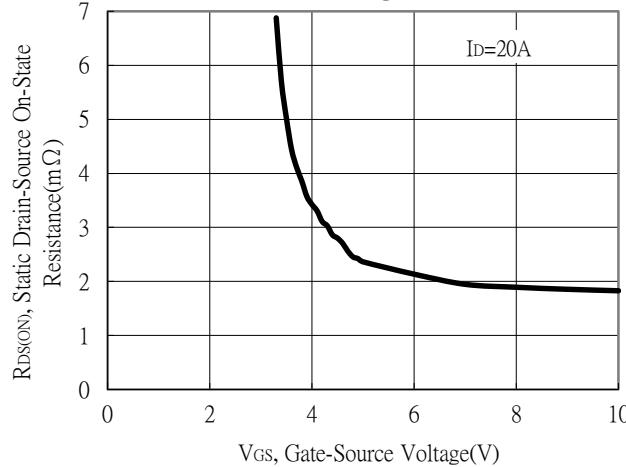
Static Drain-Source On-State resistance vs Drain Current



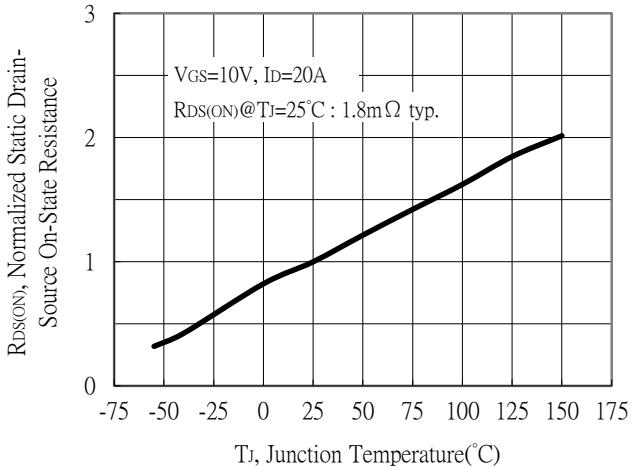
Reverse Drain Current vs Source-Drain Voltage



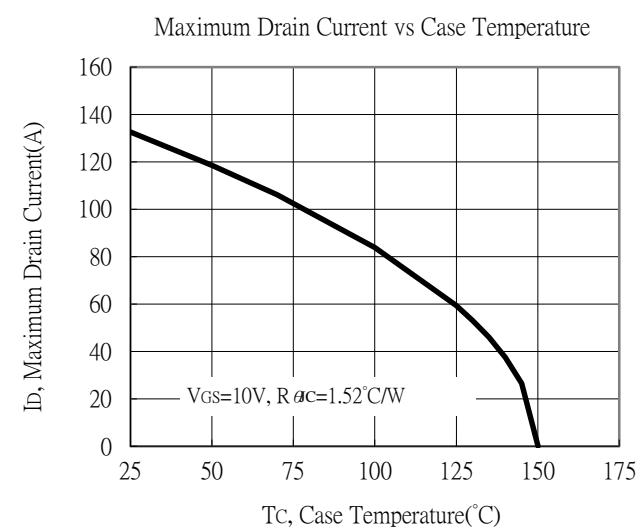
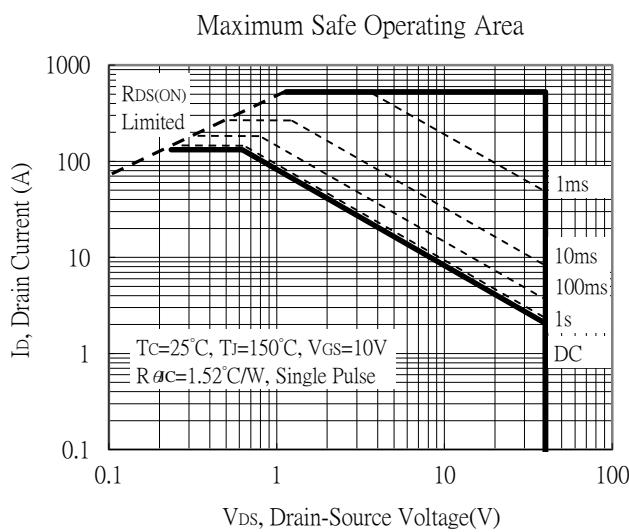
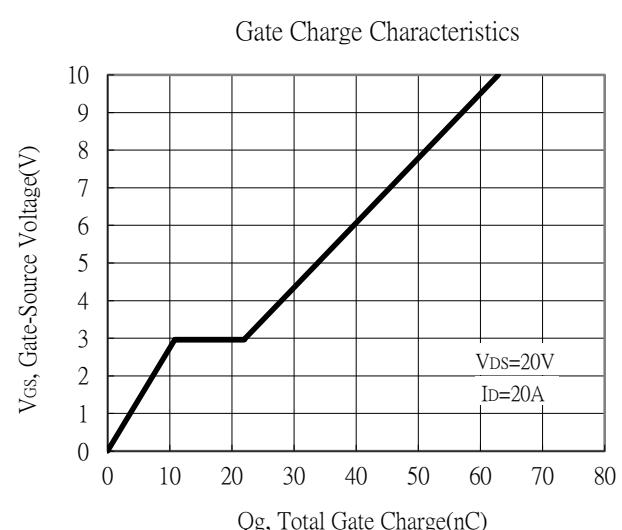
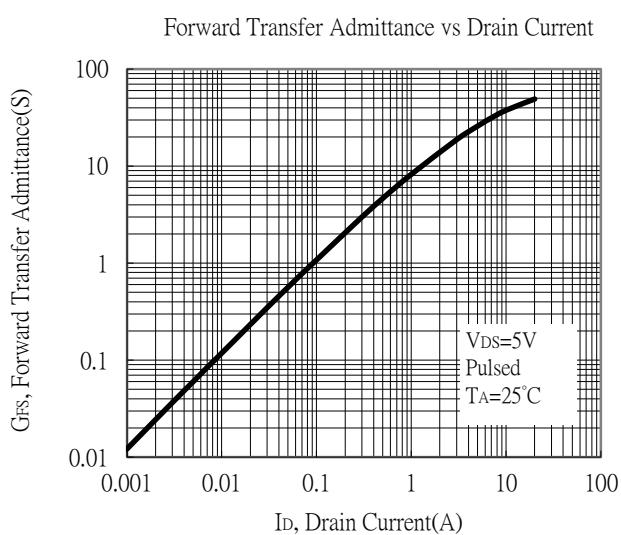
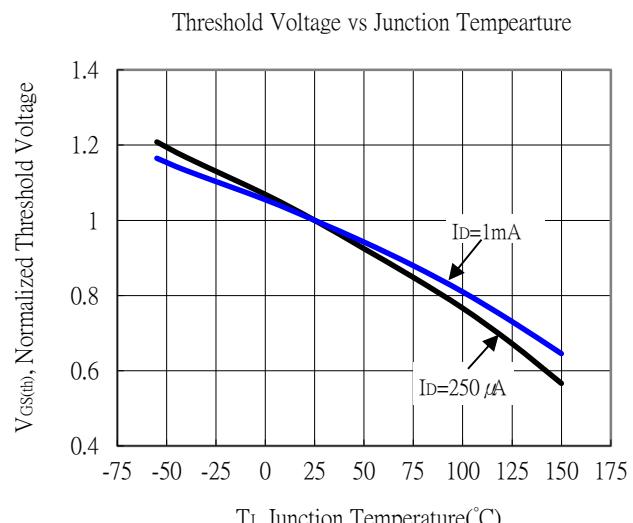
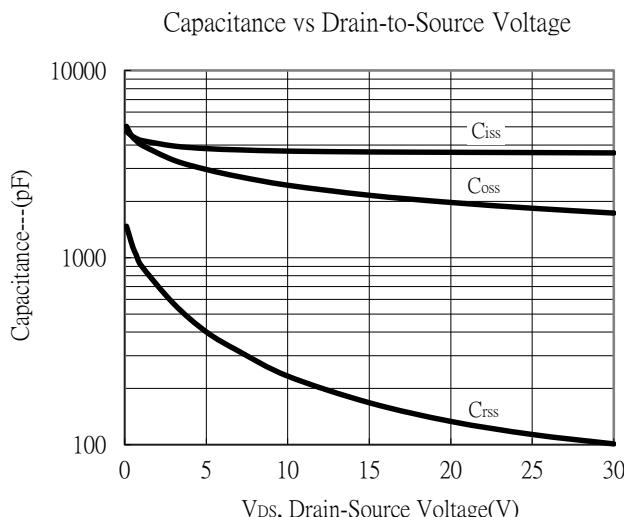
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

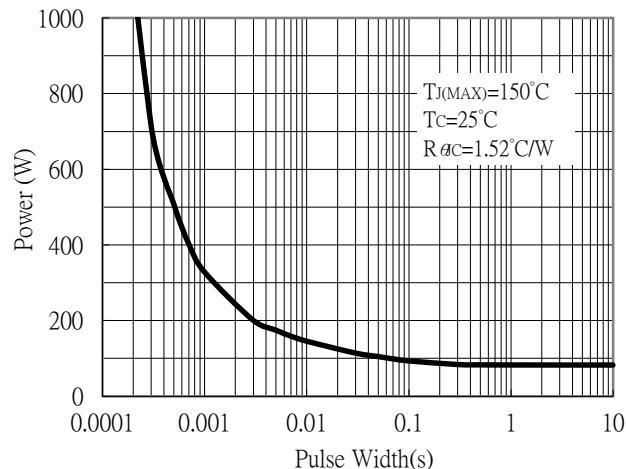


Typical Characteristics(Cont.)

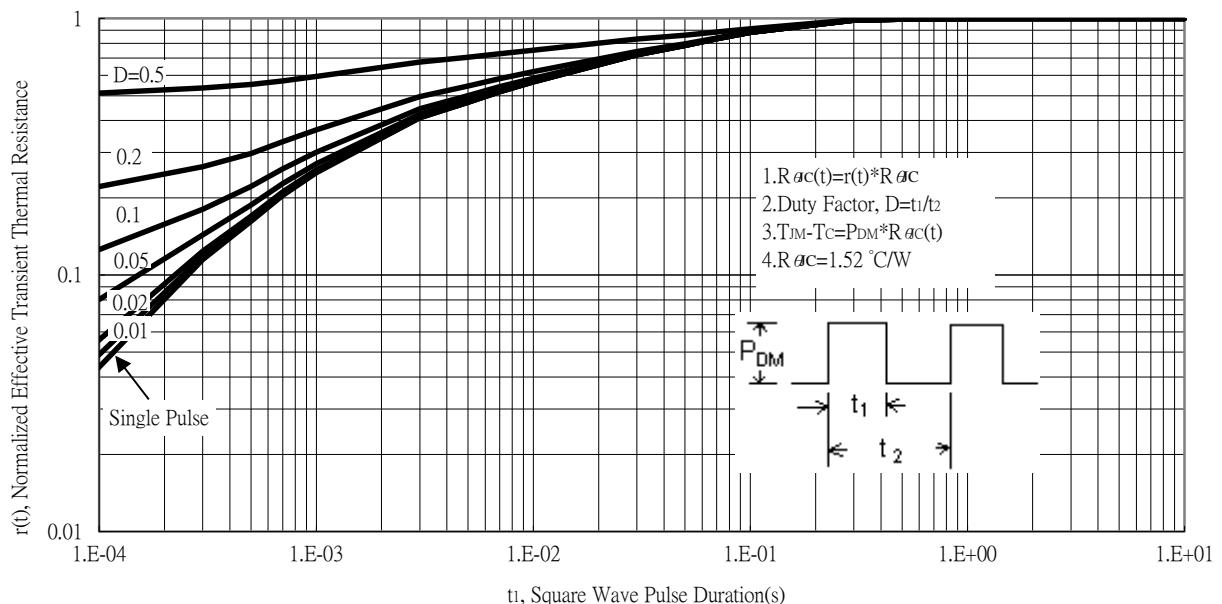


Typical Characteristics(Cont.)

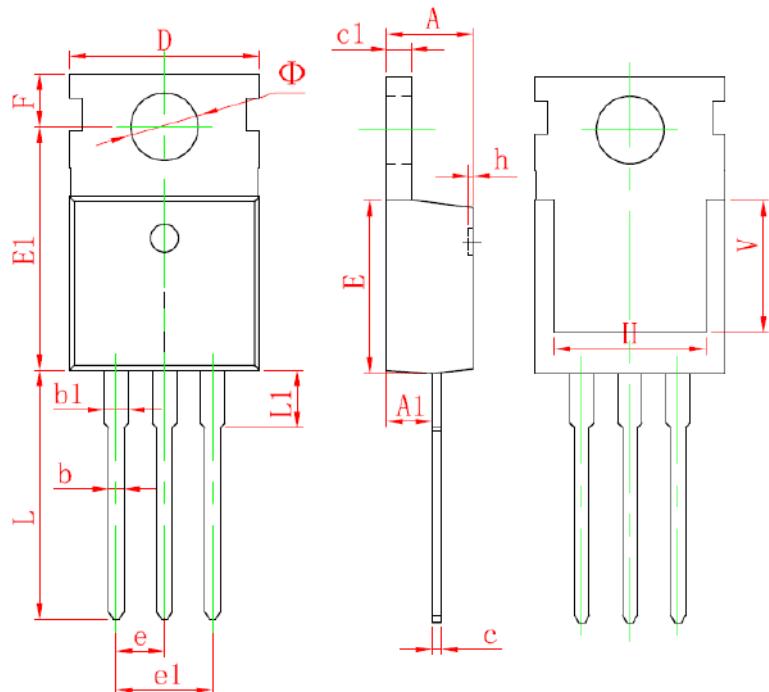
Single Pulse Maximum Power Dissipation



Transient Thermal Response Curves

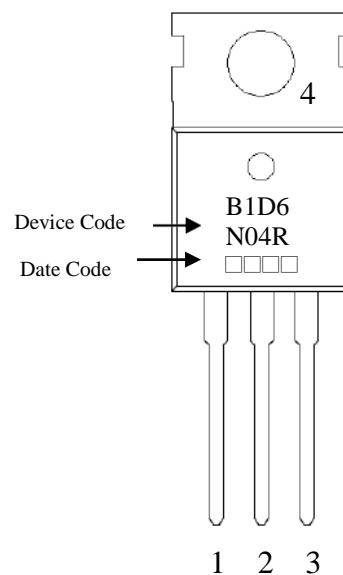


TO-220 Dimension



3-Lead TO-220 Plastic Package

Marking:



Style: Pin 1.Gate 2.Drain 3.Source
4.Drain

*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181	e	2.540*		0.100*	
A1	2.250	2.550	0.089	0.100	e1	4.980	5.180	0.196	0.204
b	0.710	0.910	0.028	0.036	F	2.650	2.950	0.104	0.116
b1	1.170	1.370	0.046	0.054	H	7.900	8.100	0.311	0.319
c	0.330	0.650	0.013	0.026	h	0.000	0.300	0.000	0.012
c1	1.200	1.400	0.047	0.055	L	12.900	13.400	0.508	0.528
D	9.910	10.250	0.390	0.404	L1	2.850	3.250	0.112	0.128
E	8.950	9.750	0.352	0.384	V	7.500 REF		0.295	REF
E1	12.650	12.950	0.498	0.510	Φ	3.400	3.800	0.134	0.150