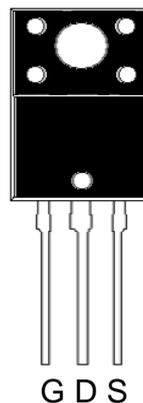


N-Channel Enhancement Mode Power MOSFET

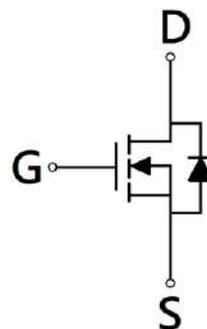
Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

TO-220FP



BV_{DSS}	100V
$I_D @ V_{GS}=10V, T_C=25^\circ C$ (silicon limit)	64A
$I_D @ V_{GS}=10V, T_C=25^\circ C$ (package limit)	56A
$I_D @ V_{GS}=10V, T_A=25^\circ C$	15A
$R_{DS(ON)}$ typ. @ $V_{GS}=10V, I_D=50A$	6.2m Ω



G : Gate S : Source D : Drain

Ordering Information

Device	Package	Shipping
KE5D0N10R	TO-220FP (RoHS compliant)	50 pcs/tube, 20 tubes/box, 5 boxes / carton

Absolute Maximum Ratings (T_A=25°C)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage	V _{GS}	±20		
Continuous Drain Current @ V _{GS} =10V, T _C =25°C (silicon limit) *a	I _D	64	A	
Continuous Drain Current @ V _{GS} =10V, T _C =25°C (package limit) *a		56		
Continuous Drain Current @ V _{GS} =10V, T _C =100°C *a		40		
Continuous Drain Current @ V _{GS} =10V, T _A =25°C *b		15		
Continuous Drain Current @ V _{GS} =10V, T _A =70°C *b		12		
Pulsed Drain Current *c		I _{DM}		224
Continuous Body Diode Forward Current @ T _C =25°C *a	I _S	52		
Avalanche Current @ L=0.1mH	I _{AS}	45		
Avalanche Energy @ L=0.5mH	E _{AS}	156	mJ	
Total Power Dissipation	P _D	T _C =25°C *a	62	W
		T _C =100°C *a	25	
		T _A =25°C *b	3.3	
		T _A =70°C *b	2.1	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C	

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	R _{θJC}	2	°C/W
Thermal Resistance, Junction-to-ambient *b	R _{θJA}	38	

Note:

- *a. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- *b. The value of R_{θJA} is measured with the device mounted on 1 in²FR -4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.

Electrical Characteristics (T_A=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	100	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2	-	4		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	63	-	S	V _{DS} =5V, I _D =30A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =80V, V _{GS} =0V
R _{DS(ON)}	-	6.2	8	mΩ	V _{GS} =10V, I _D =50A
Dynamic					
C _{iss}	-	3350	-	pF	V _{DS} =50V, V _{GS} =0V, f=1MHz
C _{oss}	-	440	-		
C _{rss}	-	33	-		
R _g	-	1.4	-	Ω	f=1MHz
Q _g *1, 2	-	48	-	nC	V _{DS} =50V, I _D =50A, V _{GS} =10V
Q _{gs} *1, 2	-	18	-		
Q _{gd} *1, 2	-	10	-		
t _{d(ON)} *1, 2	-	30	-	ns	V _{DS} =50V, I _D =20A, V _{GS} =10V, R _{GS} =3Ω
t _r *1, 2	-	18	-		
t _{d(OFF)} *1, 2	-	49	-		
t _f *1, 2	-	11	-		
Source-Drain Diode					
V _{SD} *1	-	0.93	1.2	V	I _S =50A, V _{GS} =0V
t _{rr}	-	45	-	ns	I _F =20A, dI _F /dt=100A/μs
Q _{rr}	-	74	-	nC	

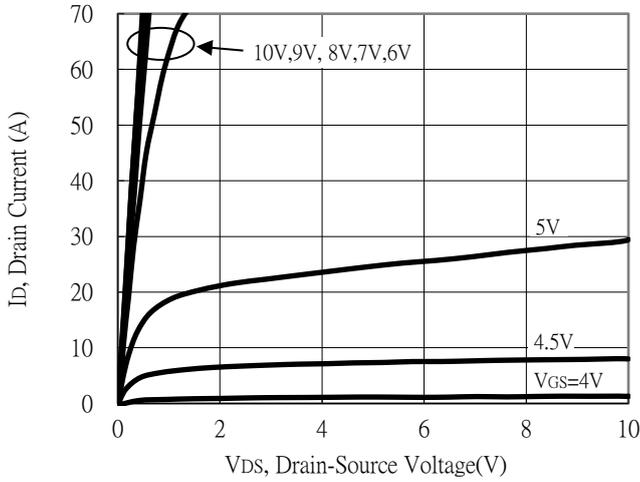
Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

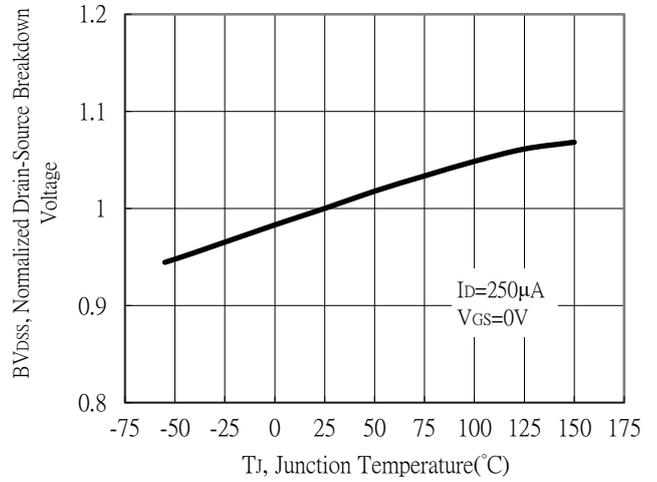
*2. Independent of operating temperature

Typical Characteristics

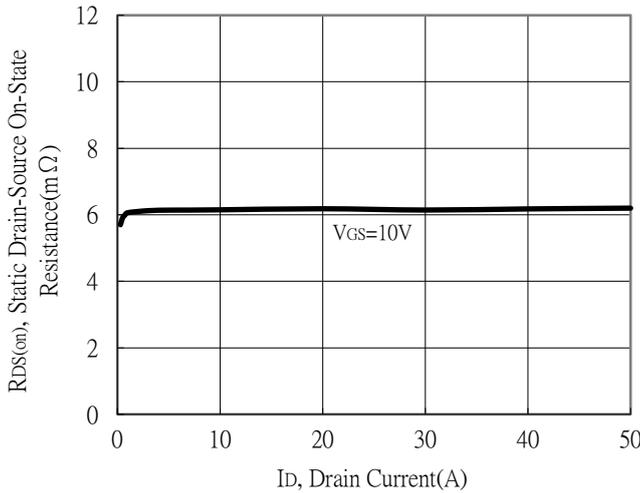
Typical Output Characteristics



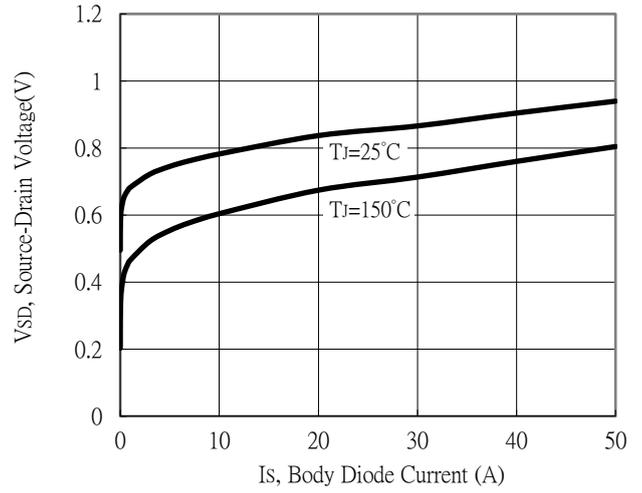
Breakdown Voltage vs Ambient Temperature



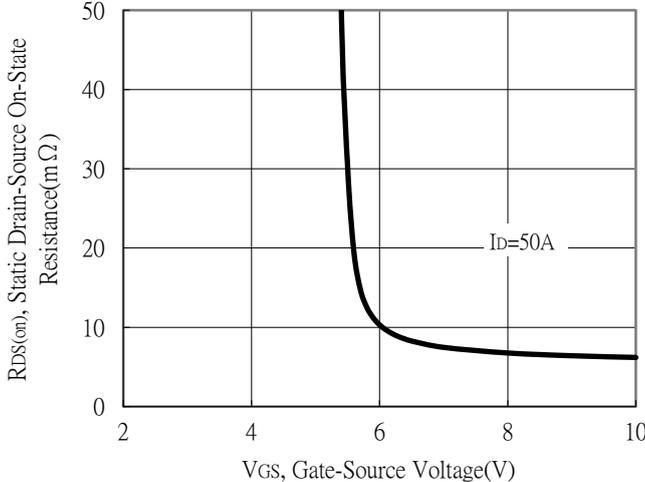
Static Drain-Source On-State resistance vs Drain Current



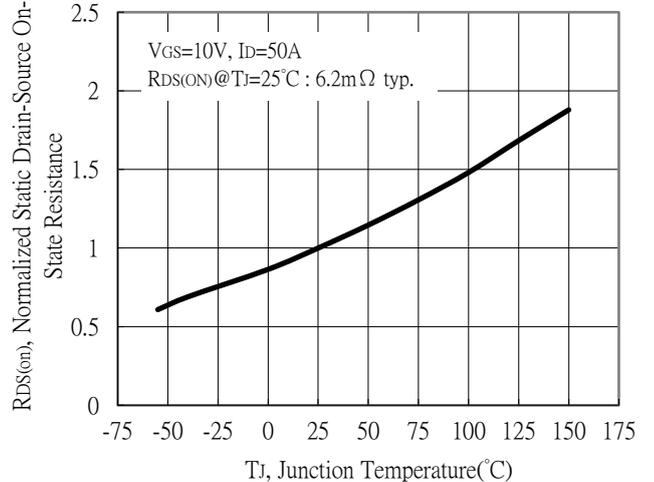
Body Diode Current vs Source-Drain Voltage



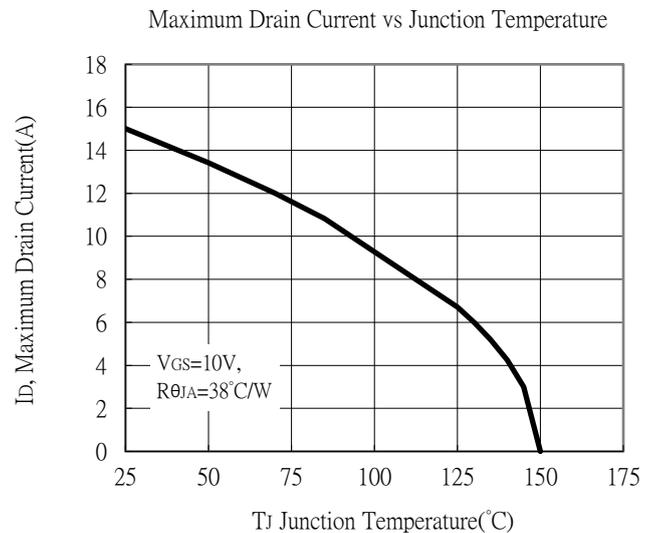
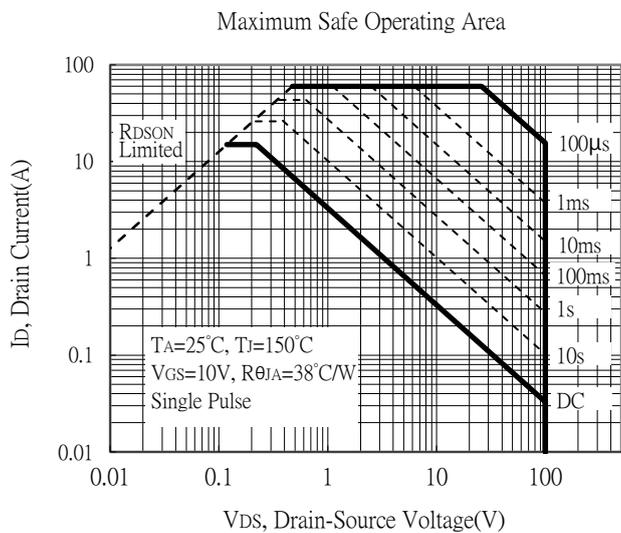
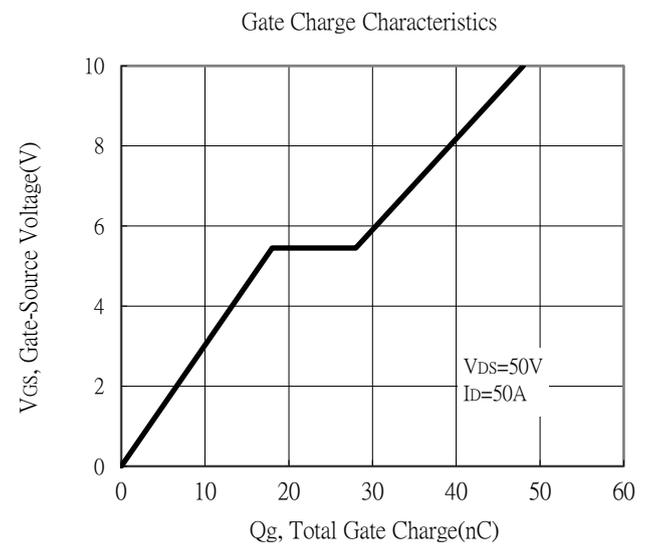
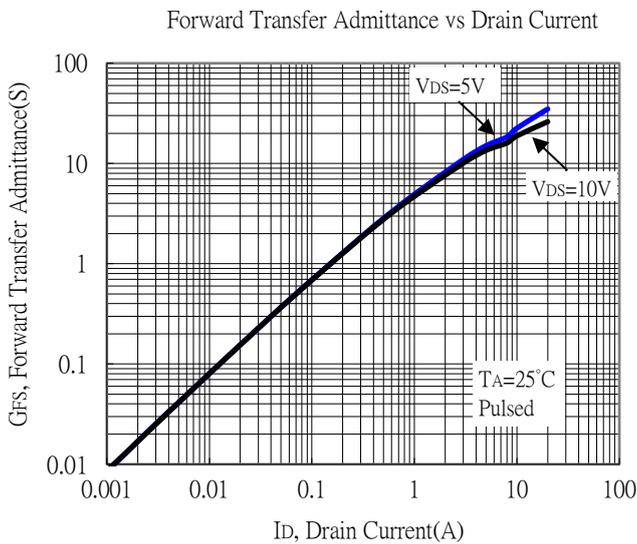
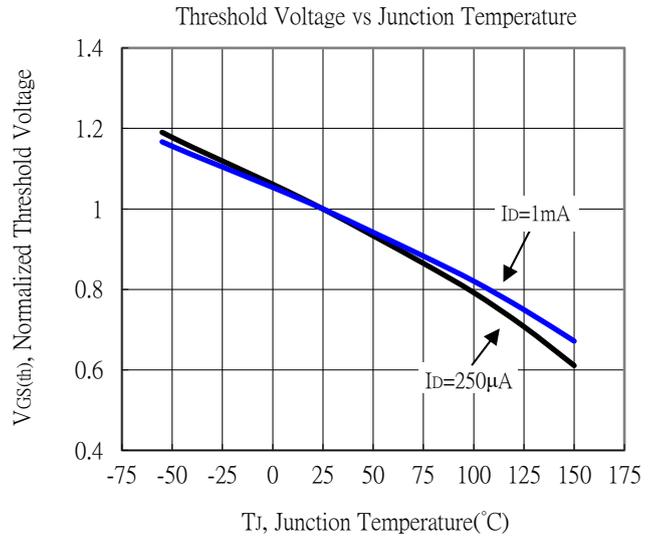
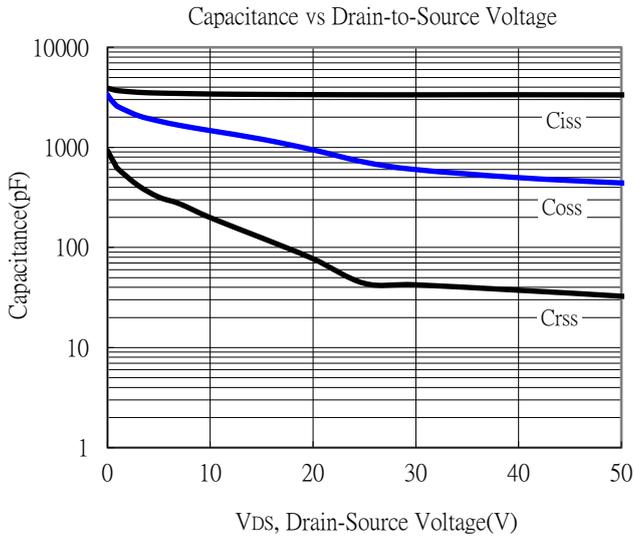
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

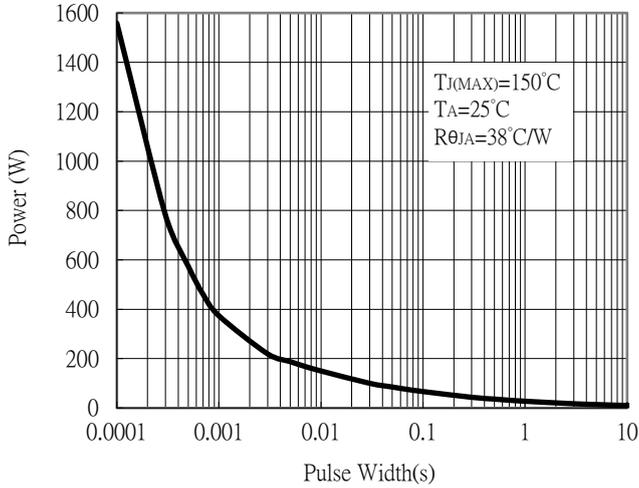


Typical Characteristics (Cont.)

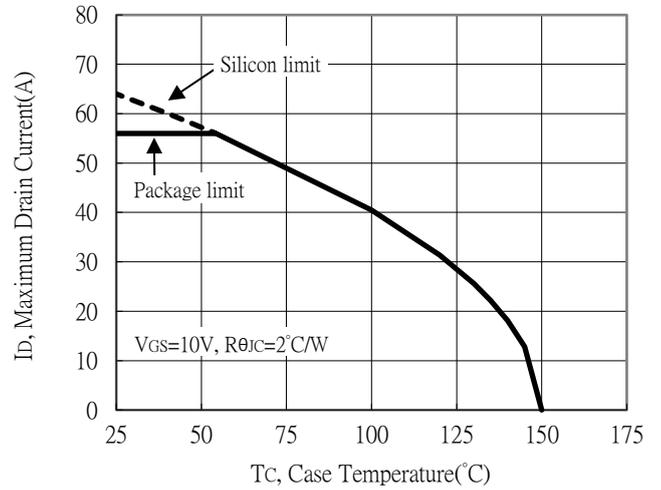


Typical Characteristics (Cont.)

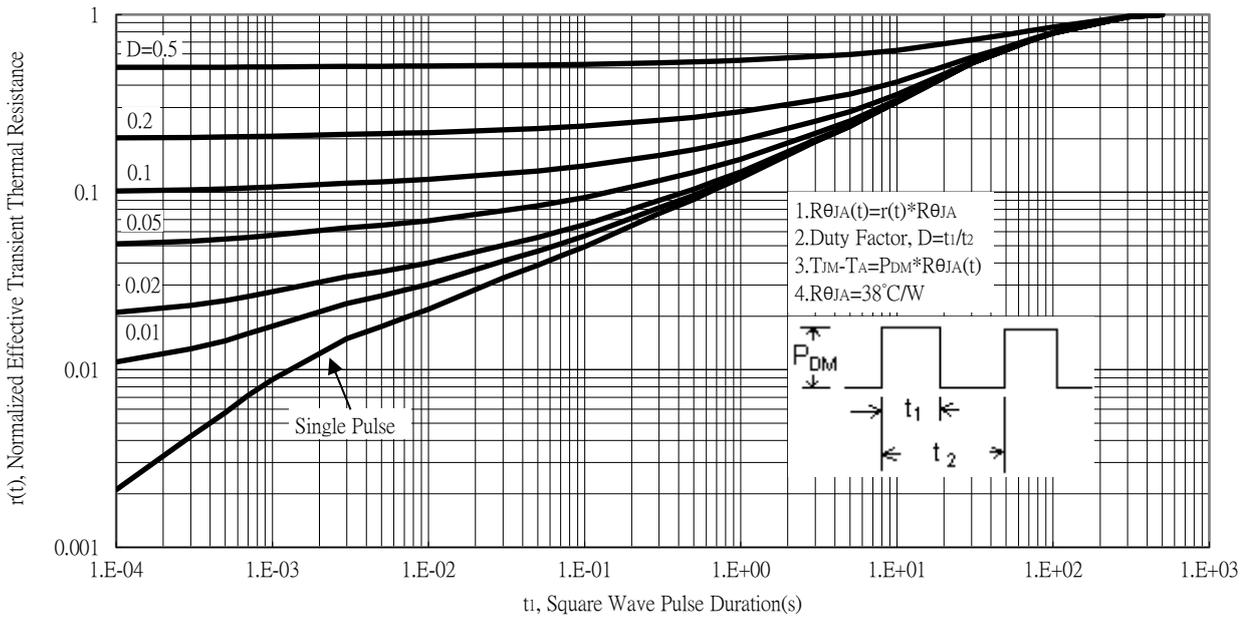
Single Pulse Power Rating, Junction to Ambient



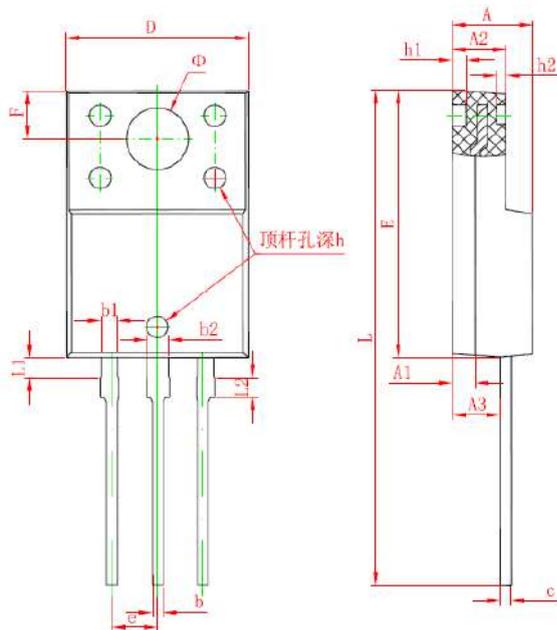
Maximum Drain Current vs Case Temperature



Transient Thermal Response Curves

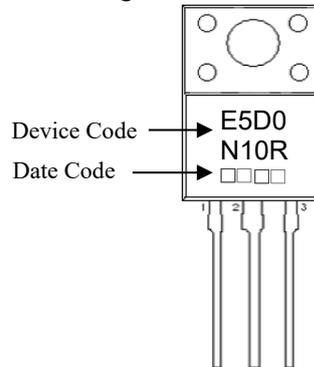


TO-220FP Dimension



3-Lead TO-220FP Plastic Package

Marking:



Style: Pin 1.Gate 2.Drain 3.Source

Date Code(counting from left to right) :
 1st code: year code, the last digit of Christian year
 2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D,
 May→E, Jun→F, Jul→G, Aug→H, Sep→J,
 Oct→K, Nov→L, Dec→M
 3rd and 4th codes : production serial number, 01~99

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.169	0.185	4.30	4.70	e	0.100 TYP		2.54 TYP	
A1	0.051 REF		1.300 REF		F	0.106 REF		2.70 REF	
A2	0.110	0.126	2.80	3.20	Φ	0.138 REF		3.50 REF	
A3	0.098	0.114	2.50	2.90	h	0.000	0.012	0.00	0.30
b	0.020	0.030	0.50	0.75	h1	0.031 REF		0.80 REF	
b1	0.043	0.053	1.10	1.35	h2	0.020 REF		0.50 REF	
b2	0.059	0.069	1.50	1.75	L	1.102	1.118	28.00	28.40
c	0.020	0.030	0.50	0.75	L1	0.067	0.075	1.70	1.90
D	0.392	0.408	9.96	10.36	L2	0.035	0.043	0.90	1.10
E	0.583	0.598	14.80	15.20					