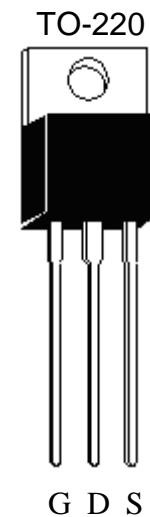


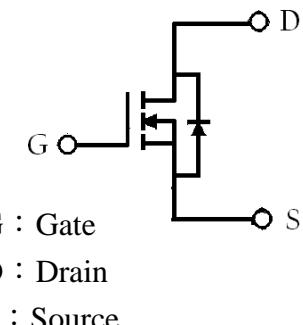
N-Channel Enhancement Mode Power MOSFET

Features:

- Simple Drive Requirement
- Fast Switching Characteristic
- RoHS compliant package



BV _{DSS}	60V
I _D @ V _{GS} =10V	120A
R _{DSON(TYP)} @ V _{GS} =10V, I _D =20A	2.6mΩ
R _{DSON(TYP)} @ V _{GS} =7V, I _D =20A	2.8mΩ



G : Gate

D : Drain

S : Source

Ordering Information

Device	Package	Shipping
KE2D4N06	TO-220 (Pb-free lead plating package)	50 pcs/tube, 20 tubes/box, 4 boxes / carton



Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current @ $T_C=25^\circ\text{C}$ (silicon limit)	I_D	200	A
Continuous Drain Current @ $T_C=100^\circ\text{C}$ (silicon limit)		141	
Continuous Drain Current @ $T_C=25^\circ\text{C}$ (package limit) (Note 1)		120	
Pulsed Drain Current (Note 3)	I_{DM}	480	A
Continuous Drain Current @ $T_A=25^\circ\text{C}$ (Note 2)	I_{DSM}	15.4	
Continuous Drain Current @ $T_A=70^\circ\text{C}$ (Note 2)		12.3	
Avalanche Current (Note 3)	I_{AS}	30	
Avalanche Energy @ $L=100\mu\text{H}$, $I_D=30\text{A}$, $R_G=25\Omega$ (Note 2)	E_{AS}	45	mJ
Power Dissipation	P_D	330	W
		165	
Power Dissipation	P_{DSM}	2	W
		1.3	
Operating Junction and Storage Temperature	T_J , T_{Stg}	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	0.45	°C/W
Thermal Resistance, Junction-to-ambient, max, (Note 2)	$R_{th,j-a}$	62.5	°C/W

- Note : 1.The power dissipation P_D is based on $T_J(\text{MAX})=175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- 2.The value of $R_{th,j-a}$ is measured with the device mounted on 1 in²FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^\circ\text{C}$. The power dissipation P_{DSM} is based on $R_{th,j-a}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.
3. Repetitive rating, pulse width limited by junction temperature $T_J(\text{MAX})=175^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.
4. The maximum current limited by package is 60A.
5. The static characteristics are obtained using <300μs pulses, duty cycle 0.5% maximum.
6. The $R_{th,j-a}$ is the sum of thermal resistance from junction to case $R_{th,j-c}$ and case to ambient.

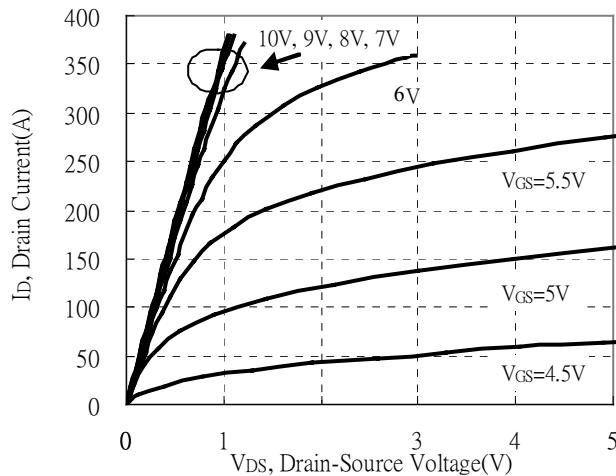
Characteristics (T_c=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2.0	2.7	4.0		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	56	-	S	V _{DS} =5V, I _D =15A
I _{GSS}	-	-	±100	nA	V _{GS} =±30V
I _{DSS}	-	-	1	μA	V _{DS} =48V, V _{GS} =0V
	-	-	25		V _{DS} =48V, V _{GS} =0V, T _j =125°C
*R _{DSS(ON)}	-	2.6	3.5	mΩ	V _{GS} =10V, I _D =20A
	-	2.8	4.0		V _{GS} =7V, I _D =20A
Dynamic					
*Q _g	-	148	-	nC	I _D =120A, V _{DS} =30V, V _{GS} =10V
*Q _{gs}	-	34.3	-		
*Q _{gd}	-	58.5	-		
*t _{d(ON)}	-	32	-	ns	V _{DS} =30V, I _D =60A, V _{GS} =10V, R _G =4.7Ω
*t _r	-	55	-		
*t _{d(OFF)}	-	147	-		
*t _f	-	143	-		
C _{iss}	-	6674	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	-	1241	-		
C _{rss}	-	1137	-		
R _g	-	1.9	-	Ω	f=1MHz
Source-Drain Diode					
*I _s	-	-	120	A	Is=1A, V _{GS} =0V
*I _{SM}	-	-	480		
*V _{SD}	-	0.65	0.9	V	Is=1A, V _{GS} =0V
*trr	-	70	-	ns	I _F =120A, V _{GS} =0V, dI/dt=100A/μs
*Q _{rr}	-	150	-	nC	

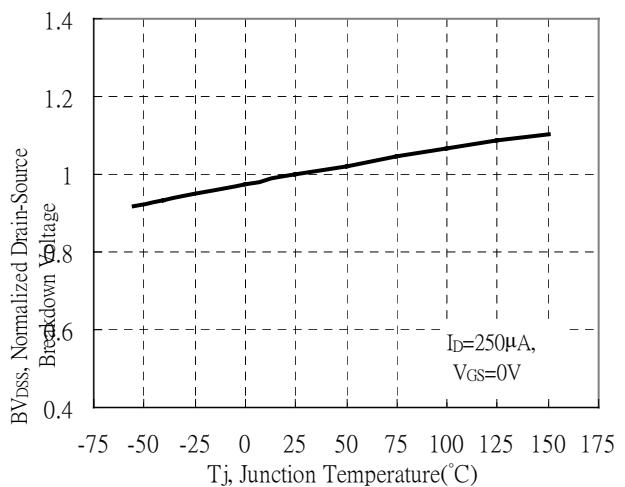
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

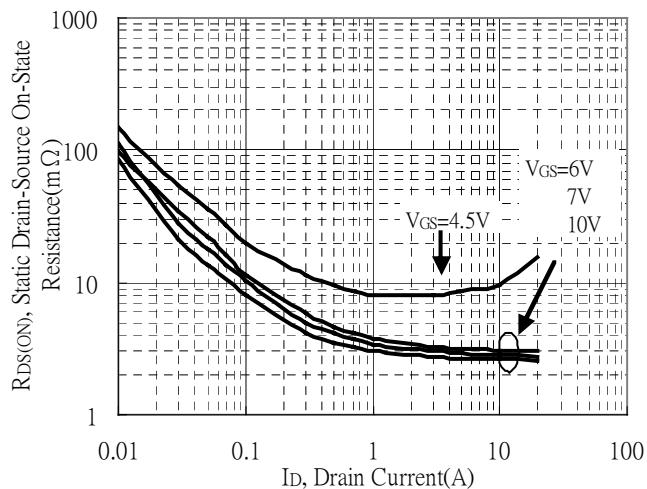
Typical Output Characteristics



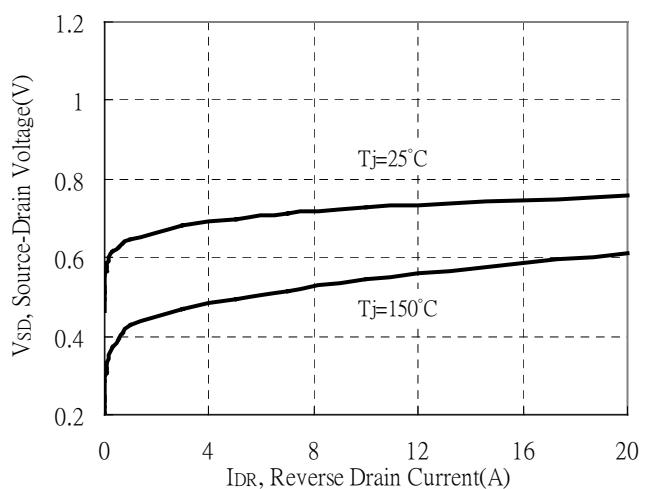
Breakdown Voltage vs Ambient Temperature



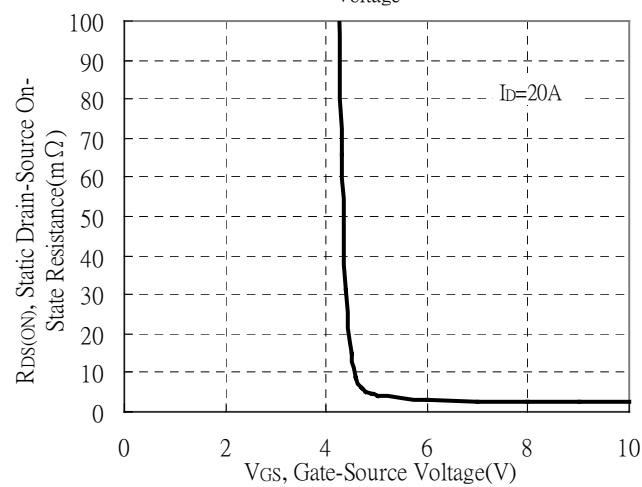
Static Drain-Source On-State resistance vs Drain Current



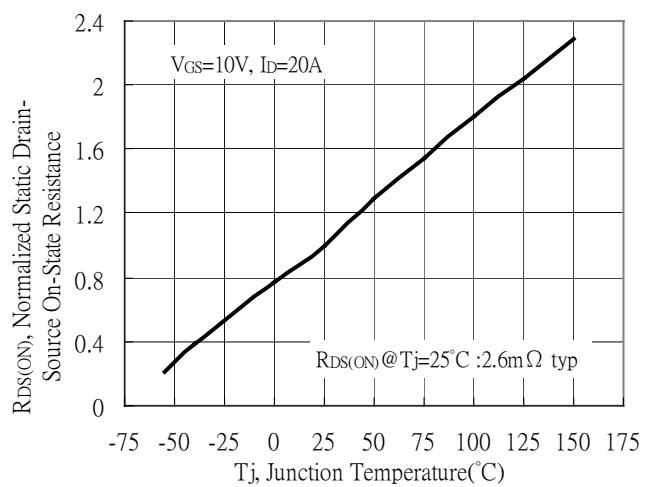
Reverse Drain Current vs Source-Drain Voltage



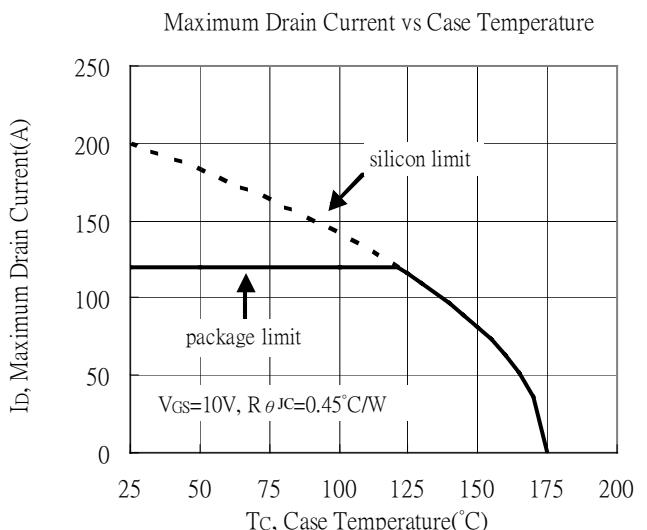
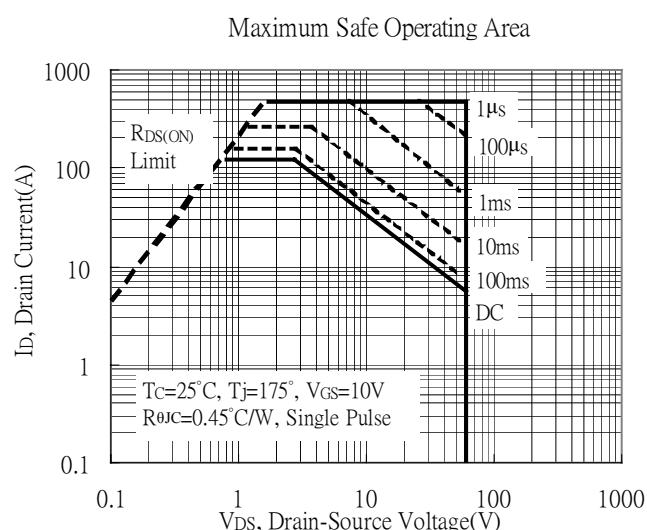
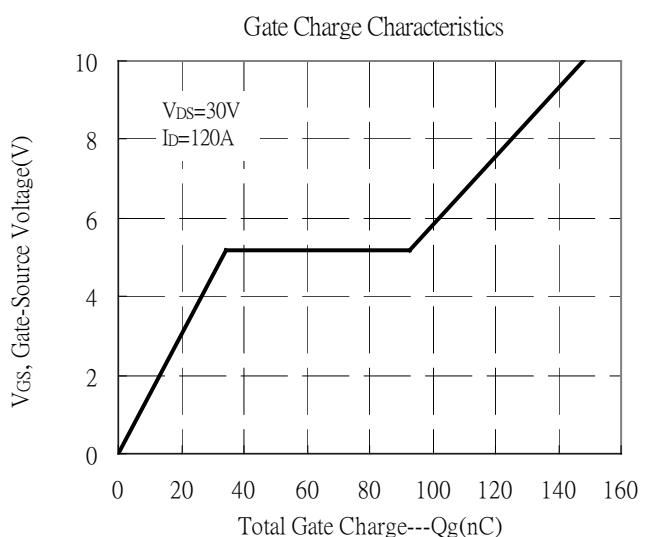
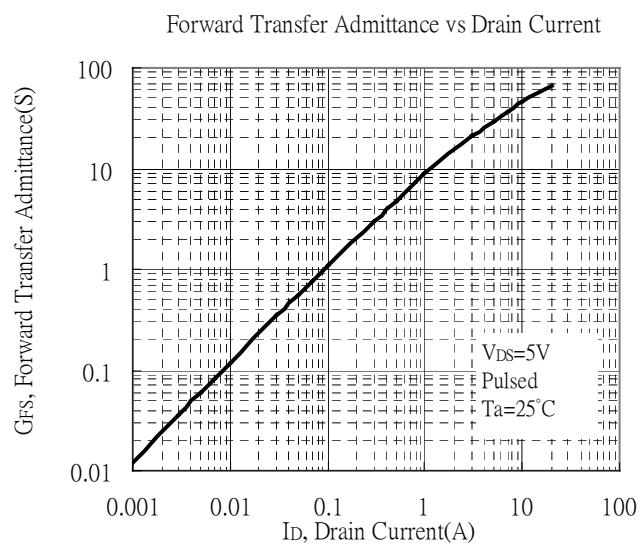
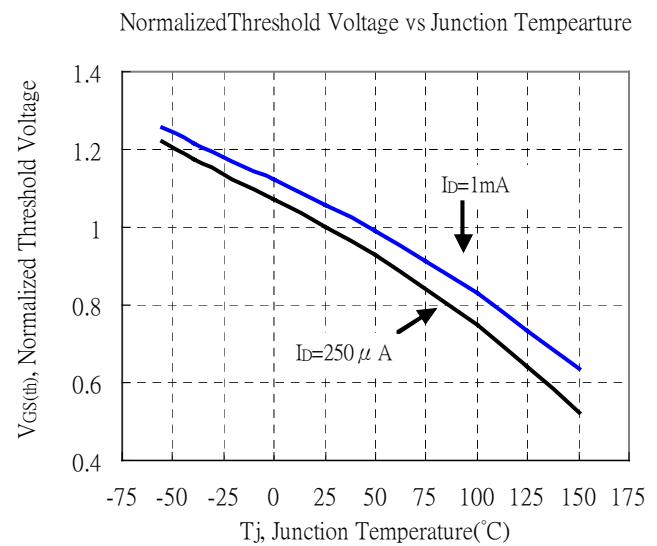
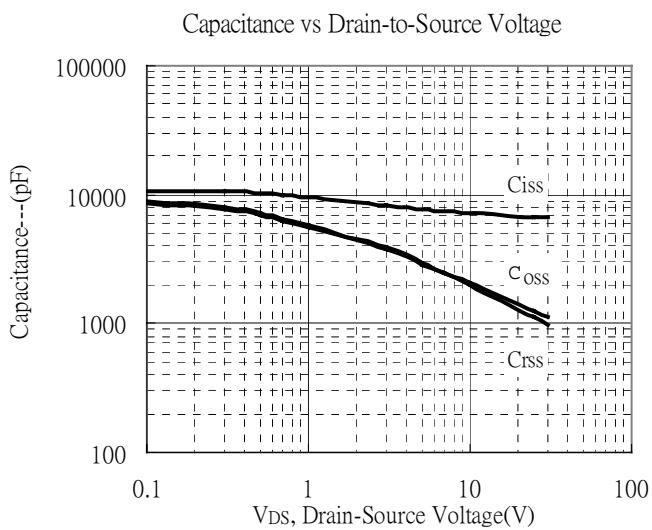
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

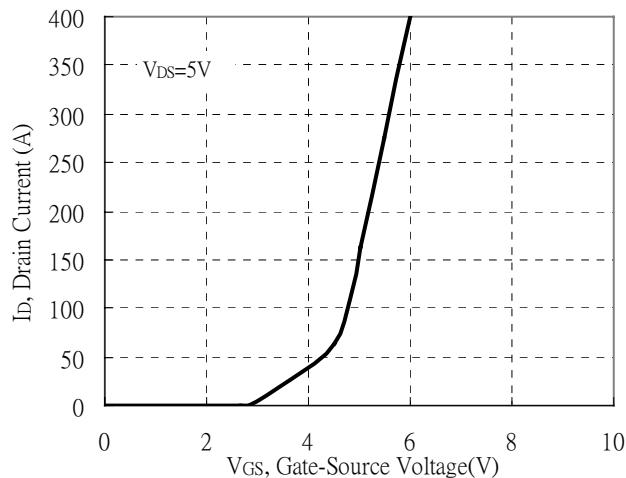


Typical Characteristics(Cont.)

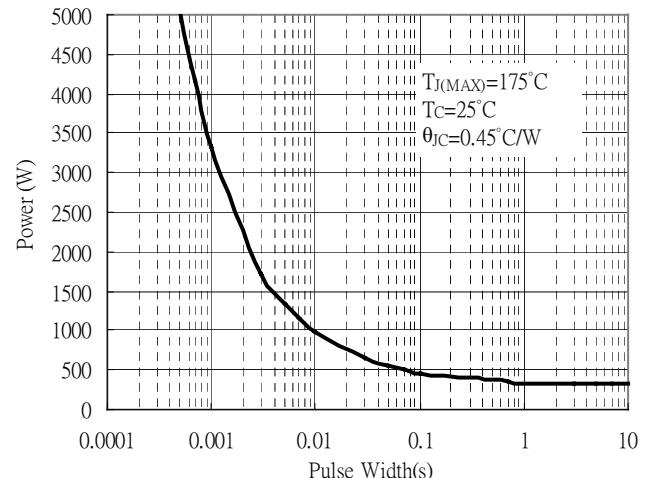


Typical Characteristics(Cont.)

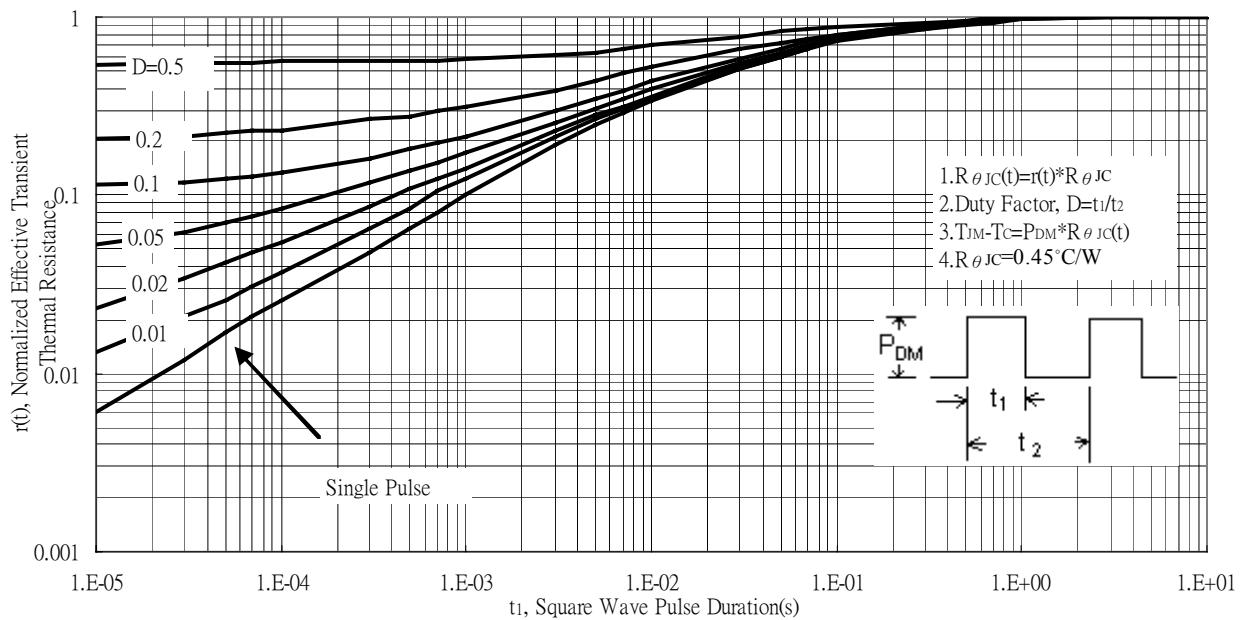
Typical Transfer Characteristics



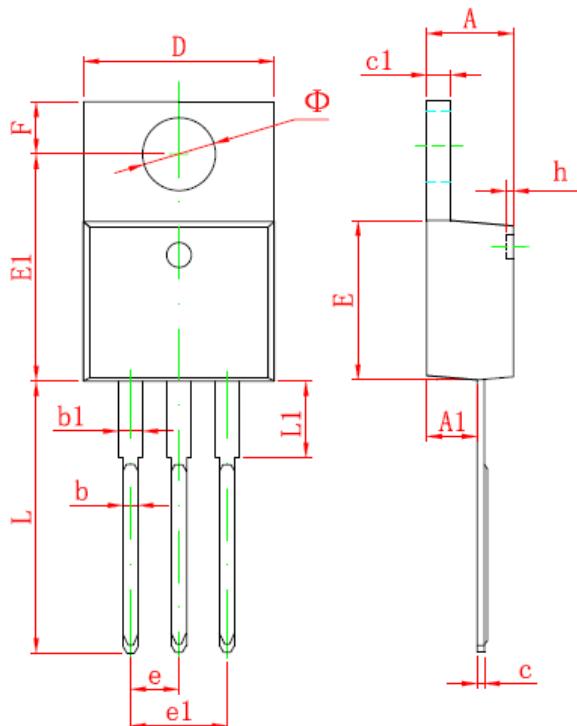
Single Pulse Maximum Power Dissipation



Transient Thermal Response Curves

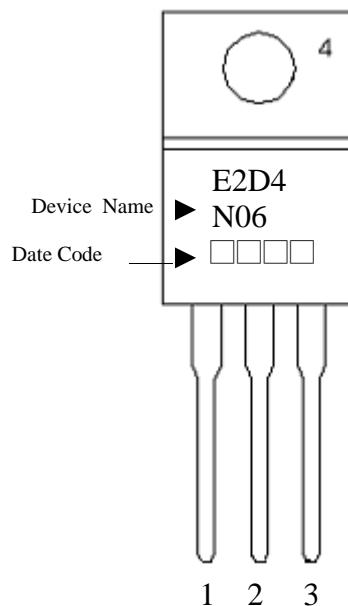


TO-220 Dimension



3-Lead TO-220 Plastic Package

Marking:



Style: Pin 1.Gate 2.Drain 3.Source
 4.Drain

*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184	E1	12.060	12.460	0.475	0.491
A1	2.520	2.820	0.099	0.111	e	2.540*		0.100*	
b	0.710	0.910	0.028	0.036	e1	4.980	5.180	0.196	0.204
b1	1.170	1.370	0.046	0.054	F	2.590	2.890	0.102	0.114
c	0.310	0.530	0.012	0.021	h	0.000	0.300	0.000	0.012
c1	1.170	1.370	0.046	0.054	L	13.400	13.800	0.528	0.543
D	10.010	10.310	0.394	0.406	L1	3.560	3.960	0.140	0.156
E	8.500	8.900	0.335	0.350	Φ	3.735	3.935	0.147	0.155