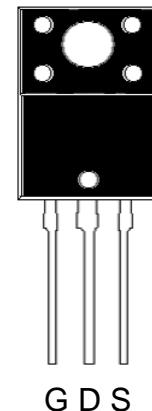


## N-Channel Enhancement Mode Power MOSFET

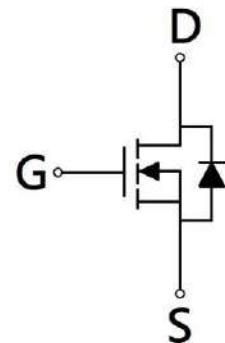
### Features:

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

TO-220FP



BV <sub>DSS</sub>	150V
I <sub>D</sub> @V <sub>GS</sub> =10V, T <sub>C</sub> =25°C	25A
I <sub>D</sub> @V <sub>GS</sub> =10V, T <sub>A</sub> =25°C	10A
R <sub>D(S)</sub> @V <sub>GS</sub> =10V, I <sub>D</sub> =20A	11mΩ



G : Gate S : Source D : Drain

### Ordering Information

Device	Package	Shipping
KE011N15RP	TO-220FP (RoHS compliant)	50 pcs/tube, 20 tubes/box, 5 boxes / carton



## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	150	V
Gate-Source Voltage	V <sub>GS</sub>	±20	
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>C</sub> =25°C	I <sub>D</sub>	25	A
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>C</sub> =100°C		16	
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>A</sub> =25°C		10	
Continuous Drain Current @ V <sub>GS</sub> =10V, T <sub>A</sub> =70°C		8	
Pulsed Drain Current	I <sub>DM</sub>	100	
Continuous Body Diode Forward Current @ T <sub>C</sub> =25°C	I <sub>S</sub>	15	
Avalanche Current @ L=0.1mH	I <sub>AS</sub>	30	
Avalanche Energy @ L=0.5mH	E <sub>AS</sub>	144	mJ
Total Power Dissipation	T <sub>C</sub> =25°C	*a	W
	T <sub>C</sub> =100°C	*a	
	T <sub>A</sub> =25°C	*b	
	T <sub>A</sub> =70°C	*b	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C

## Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-case	R <sub>θJC</sub>	6.5	°C/W
Thermal Resistance, Junction-to-ambient	R <sub>θJA</sub>	40	

Note:

- \*a. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- \*b. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR -4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*c. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.



### Electrical Characteristics ( $T_A=25^\circ C$ , unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	150	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2	-	4		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	29	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =20A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =120V, V <sub>GS</sub> =0V
R <sub>DSS(ON)</sub>	-	11	14.5	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =20A
<b>Dynamic</b>					
C <sub>iss</sub>	-	3765	-	pF	V <sub>DS</sub> =75V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	276	-		
C <sub>rss</sub>	-	33	-		
R <sub>g</sub>	-	1.2	-	Ω	f=1MHz
Q <sub>g</sub> *1, 2	-	62	-	nC	V <sub>DS</sub> =75V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V
Q <sub>gs</sub> *1, 2	-	19	-		
Q <sub>gd</sub> *1, 2	-	18	-		
t <sub>d(ON)</sub> *1, 2	-	33	-	ns	V <sub>DS</sub> =75V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V, R <sub>GS</sub> =1Ω
t <sub>r</sub> *1, 2	-	24	-		
t <sub>d(OFF)</sub> *1, 2	-	59	-		
t <sub>f</sub> *1, 2	-	15	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *1	-	0.83	1.2	V	I <sub>S</sub> =20A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	77	-	ns	I <sub>F</sub> =20A, dI <sub>F</sub> /dt=100A/μs
Q <sub>rr</sub>	-	247	-	nC	

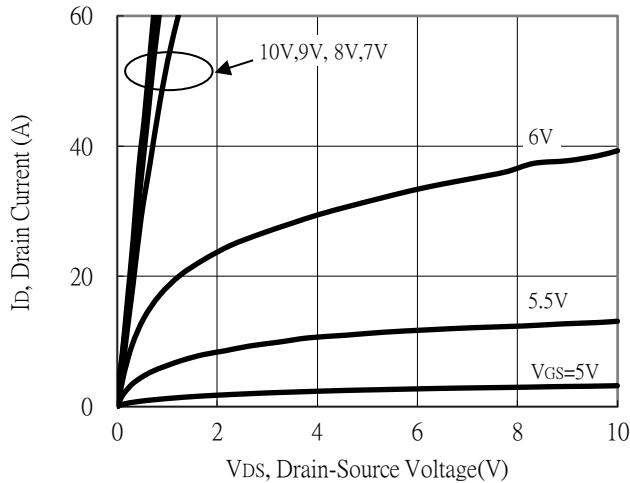
Note:

\*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

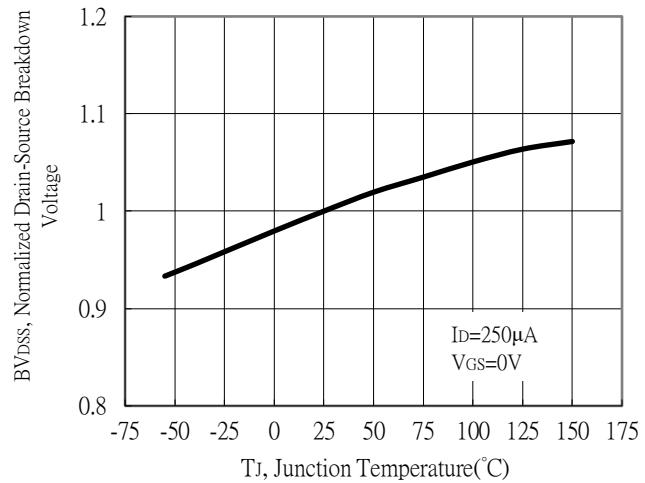
\*2. Independent of operating temperature

## Typical Characteristics

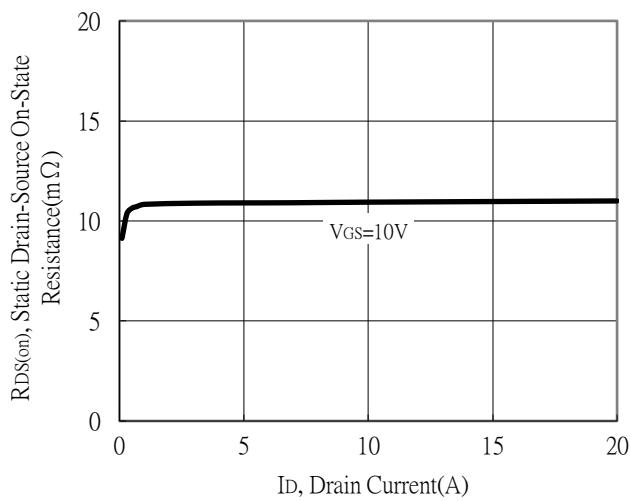
Typical Output Characteristics



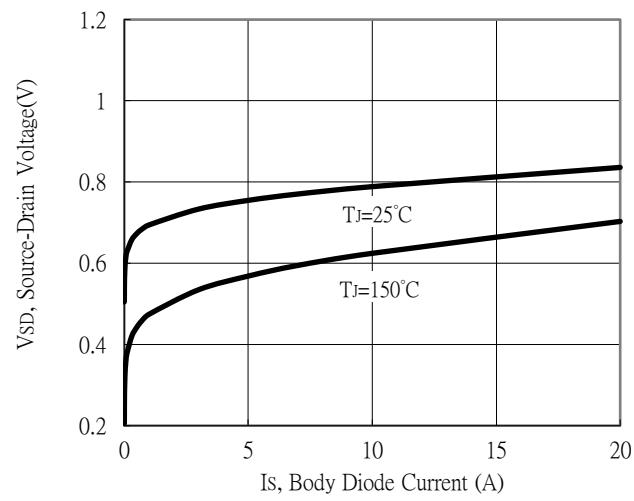
Breakdown Voltage vs Ambient Temperature



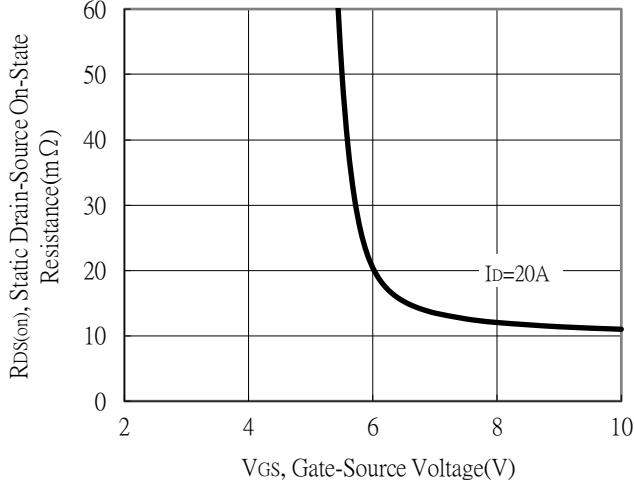
Static Drain-Source On-State resistance vs Drain Current



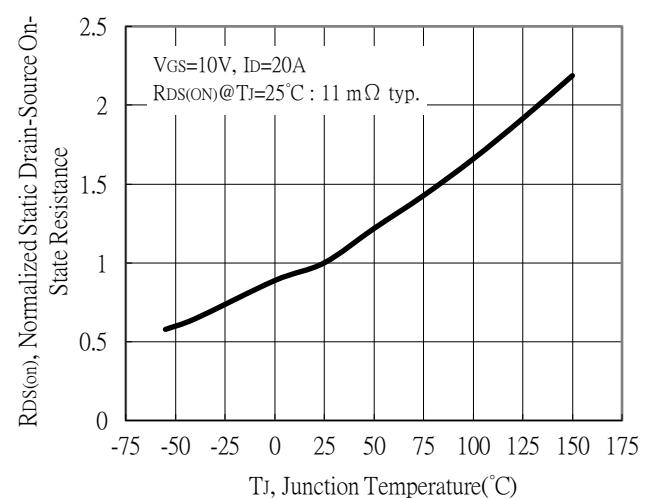
Body Diode Current vs Source-Drain Voltage



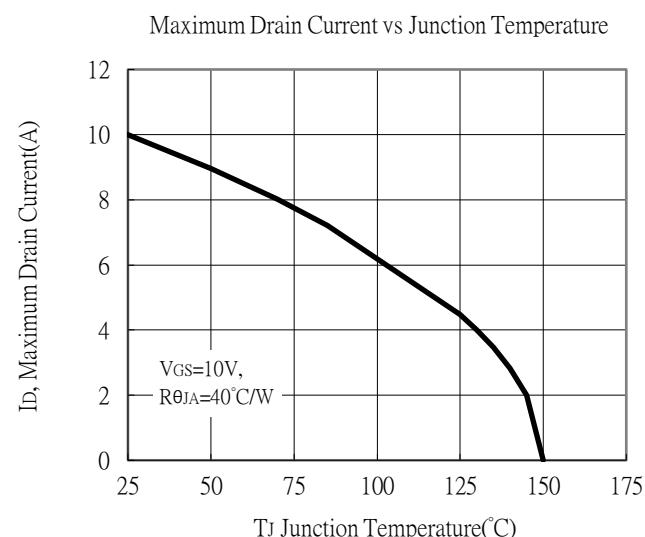
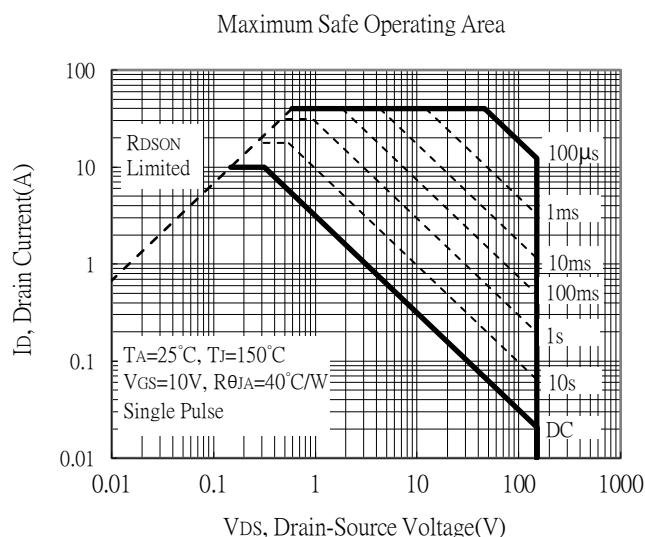
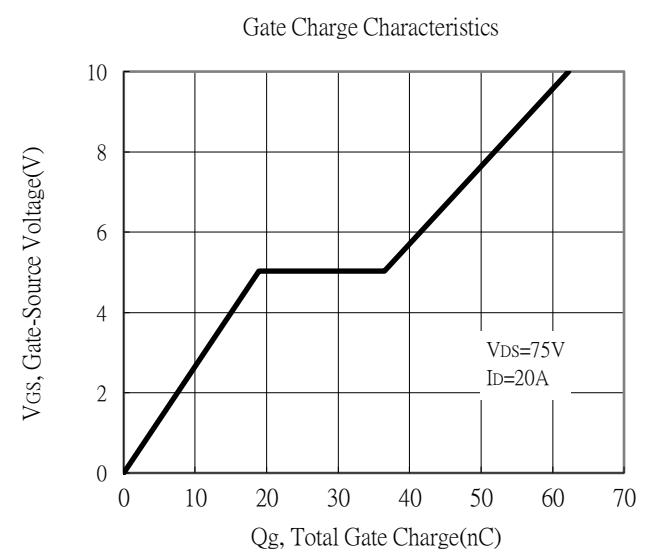
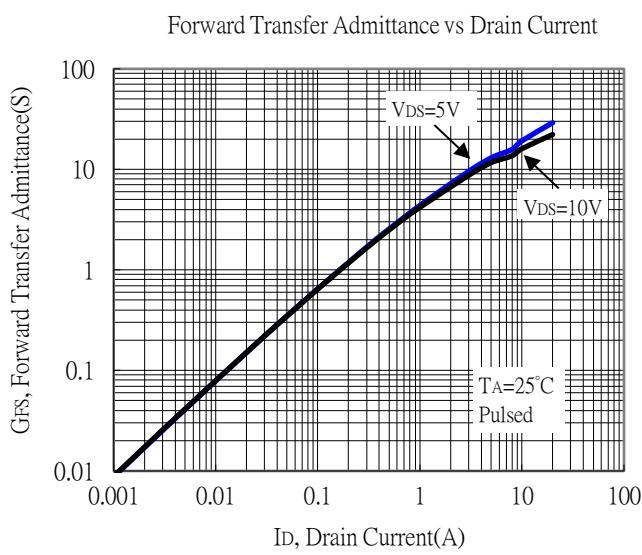
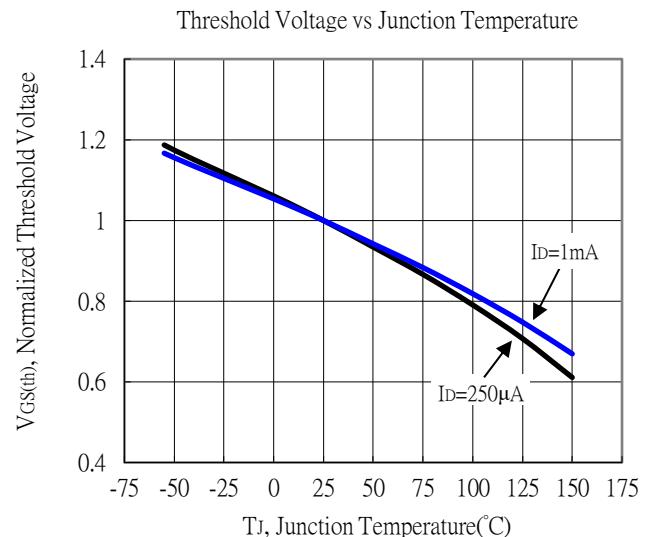
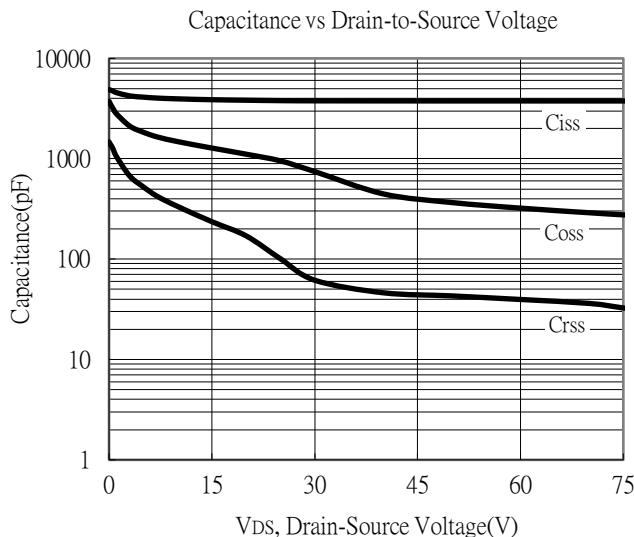
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

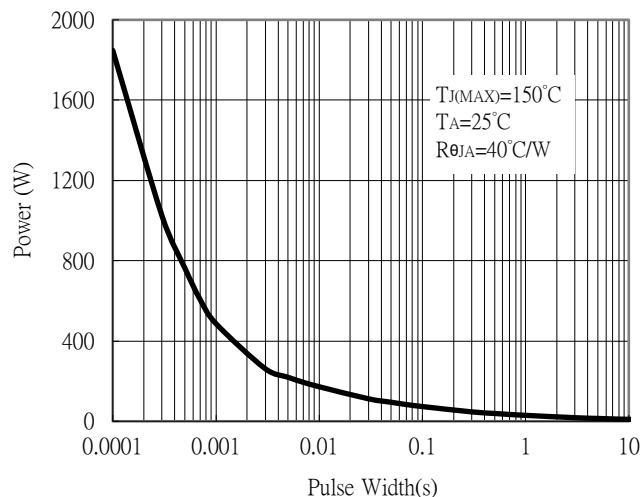


## Typical Characteristics (Cont.)

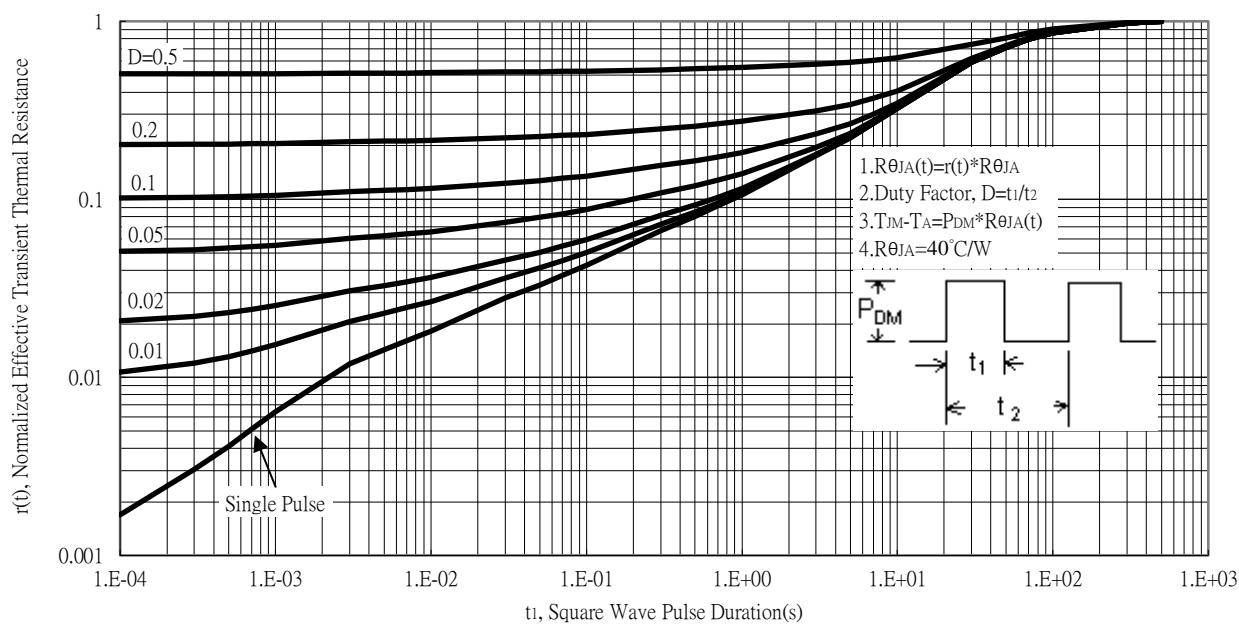


## Typical Characteristics (Cont.)

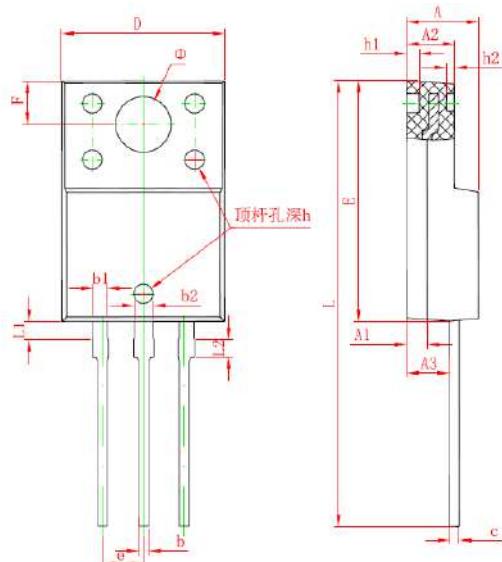
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves

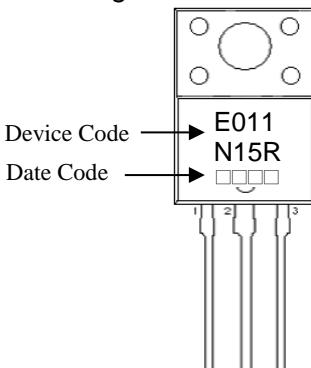


## TO-220FP Dimension



3-Lead TO-220FP Plastic Package

Marking:



Style: Pin 1.Gate 2.Drain 3.Source

Date Code(counting from left to right) :

1<sup>st</sup> code: year code, the last digit of Christian year

2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C, Apr→D

May→E, Jun→F, Jul→G, Aug→H, Sep→J,

Oct→K, Nov→L, Dec→M

3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

\*Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.169	0.185	4.30	4.70	e	0.100 TYP		2.54 TYP	
A1	0.051 REF		1.300 REF		F	0.106 REF		2.70 REF	
A2	0.110	0.126	2.80	3.20	Φ	0.138 REF		3.50 REF	
A3	0.098	0.114	2.50	2.90	h	0.000	0.012	0.00	0.30
b	0.020	0.030	0.50	0.75	h1	0.031 REF		0.80 REF	
b1	0.043	0.053	1.10	1.35	h2	0.020 REF		0.50 REF	
b2	0.059	0.069	1.50	1.75	L	1.102	1.118	28.00	28.40
c	0.020	0.030	0.50	0.75	L1	0.067	0.075	1.70	1.90
D	0.392	0.408	9.96	10.36	L2	0.035	0.043	0.90	1.10
E	0.583	0.598	14.80	15.20					