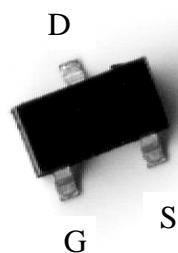


P-Channel Enhancement Mode MOSFET

Outline

SOT-23

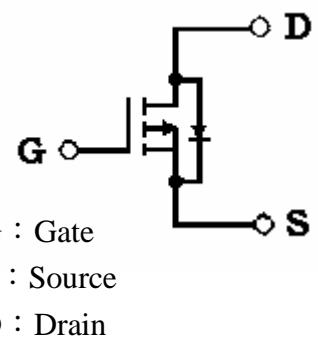


Features:

- Advanced trench process technology
- Super high density cell design for extremely low on resistance
- Reliable and rugged
- Compact and low profile SOT-23 package
- Pb-free lead plating and halogen-free package

Equivalent Circuit

K2305N3



G : Gate

S : Source

D : Drain

BVDSS	-20V
ID	-4.3A
RDS(on)@VGS=-4.5V, ID=-4.2A	52mΩ (typ.)
RDS(on)@VGS=-2.5V, ID=-2A	66mΩ (typ.)
RDS(on)@VGS=-1.8V, ID=-1A	79mΩ (typ.)

Ordering Information

Device	Package	Shipping
K2305N3	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel

Absolute Maximum Ratings ($T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current @ $T_A=25^{\circ}\text{C}$, $V_{GS}=-4.5\text{V}$ (Note 1)	I_D	-4.3	A
Continuous Drain Current @ $T_A=70^{\circ}\text{C}$, $V_{GS}=-4.5\text{V}$ (Note 1)	I_D	-3.4	A
Pulsed Drain Current (Note 2)	I_{DM}	-20	A
Maximum Power Dissipation (Note 1)	P_D	1.38	W
Linear Derating Factor		0.01	W/ $^{\circ}\text{C}$
Operating Junction and Storage Temperature	T_j , T_{stg}	-55~+150	$^{\circ}\text{C}$

Note : 1. Surface mounted on 1 in²copper pad of FR-4 board, $t \leq 5\text{s}$; 270°C/W when mounted on minimum copper pad.

2. Pulse width limited by maximum junction temperature.

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max (Note)	$R_{\theta JA}$	90	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case, max	$R_{\theta JC}$	75	$^{\circ}\text{C}/\text{W}$

Note : Surface mounted on 1 in²copper pad of FR-4 board, $t \leq 5\text{s}$; 270°C/W when mounted on minimum copper pad.

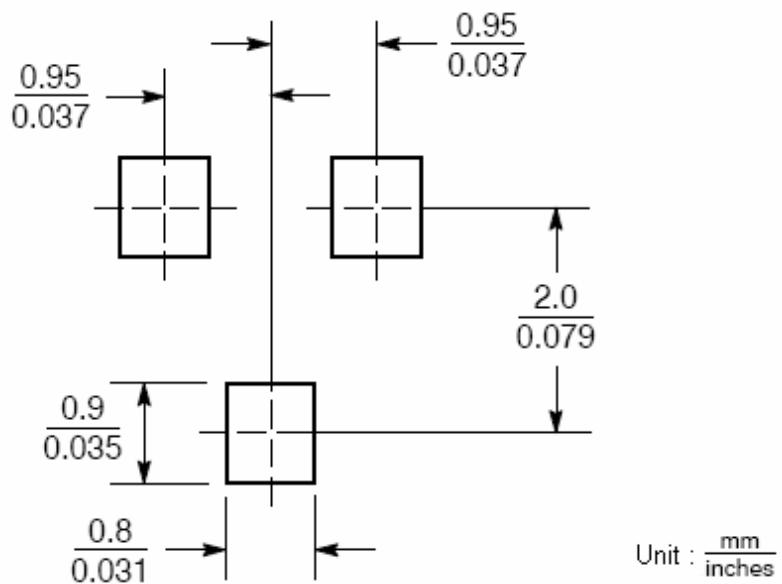
Electrical Characteristics ($T_j=25^{\circ}\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Static						
BV_{DSS}	-20	-	-	V	$V_{GS}=0$, $I_D=-250\mu\text{A}$	
$\Delta BV_{DSS}/\Delta T_j$	-	-0.02	-	V/ $^{\circ}\text{C}$	Reference to 25°C, $I_D=-1\text{mA}$	
$V_{GS(\text{th})}$	-0.5	-0.65	-	V	$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 12\text{V}$, $V_{DS}=0$	
I_{DSS}	-	-	-1	μA	$V_{DS}=-20\text{V}$, $V_{GS}=0$	
	-	-	-10		$V_{DS}=-16\text{V}$, $V_{GS}=0$, $T_j=70^{\circ}\text{C}$	
$*R_{DS(\text{ON})}$	-	44	53	$\text{m} \swarrow$	$I_D=-4.5\text{A}$, $V_{GS}=-10\text{V}$	
	-	52	65		$I_D=-4.2\text{A}$, $V_{GS}=-4.5\text{V}$	
	-	66	85		$I_D=-2.0\text{A}$, $V_{GS}=-2.5\text{V}$	
	-	79	120		$I_D=-1.0\text{A}$, $V_{GS}=-1.8\text{V}$	
$*G_{FS}$	-	8.3	-	S	$V_{DS}=-5\text{V}$, $I_D=-2.8\text{A}$	
Dynamic						
C_{iss}	-	1101	-	pF	$V_{DS}=-15\text{V}$, $V_{GS}=0$, $f=1\text{MHz}$	
C_{oss}	-	69	-			
C_{rss}	-	60	-			
$*t_{d(\text{ON})}$	-	7	-	ns	$V_{DS}=-15\text{V}$, $I_D=-4.3\text{A}$, $V_{GS}=-10\text{V}$, $R_D=3.6\Omega$, $R_G=6\Omega$	
$*t_r$	-	5	-			
$*t_{d(\text{OFF})}$	-	38	-			
$*t_f$	-	9	-			

*Qg	-	10.6	-	nC	V _{DS} =-16V, I _D =-4.3A, V _{GS} =-4.5V,
*Qgs	-	2	-		
*Qgd	-	2.8	-		
Source-Drain Diode					
*V _{SD}	-	-0.78	-1.2	V	V _{GS} =0V, I _{SD} =-1.2A
*t _{rr}	-	28	-	ns	I _S =-4.3A, V _{GS} =0V, dI/dt=100A/μs
*Q _r r	-	22	-	nC	

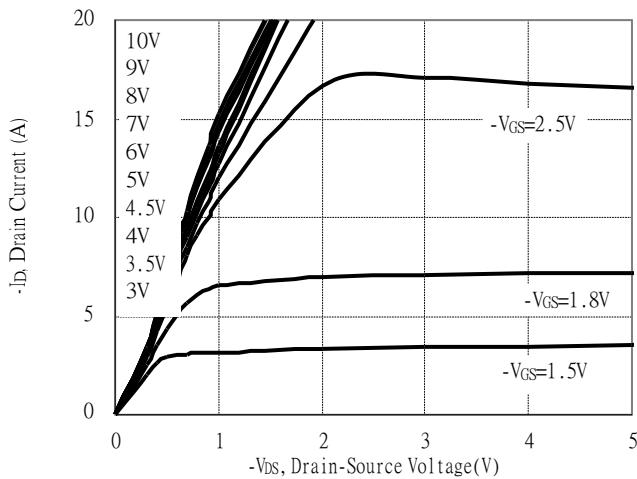
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended Soldering Footprint

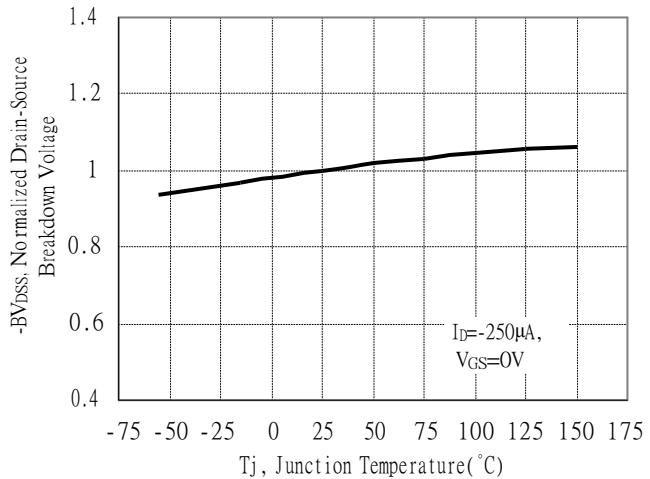


Typical Characteristics

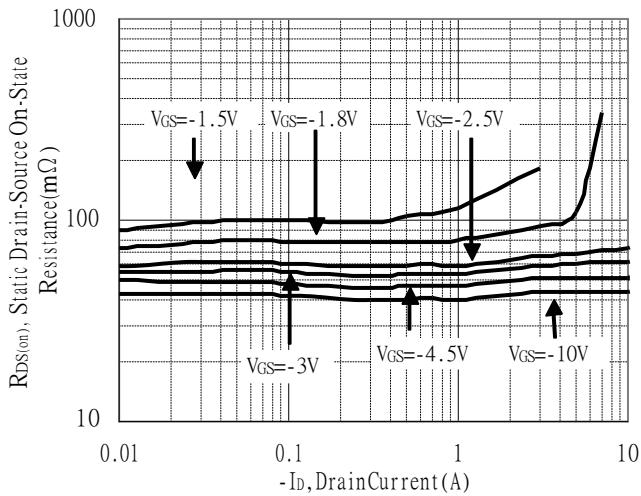
Typical Output Characteristics



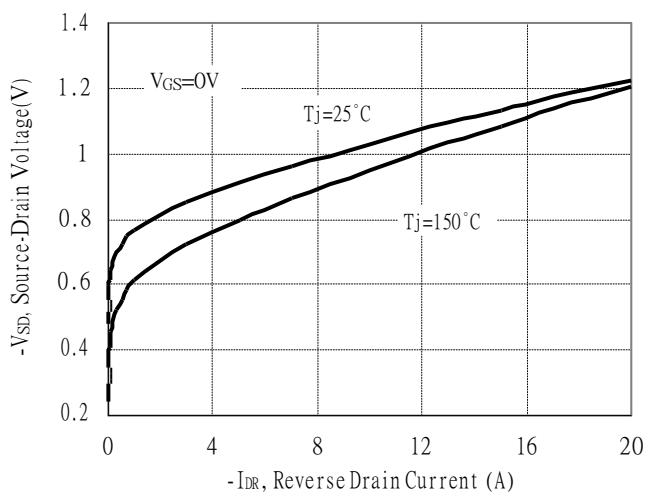
Breakdown Voltage vs Ambient Temperature



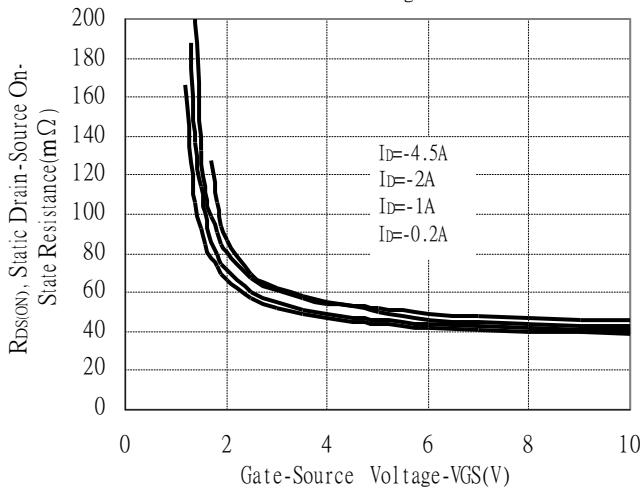
Static Drain-Source On-State resistance vs Drain Current



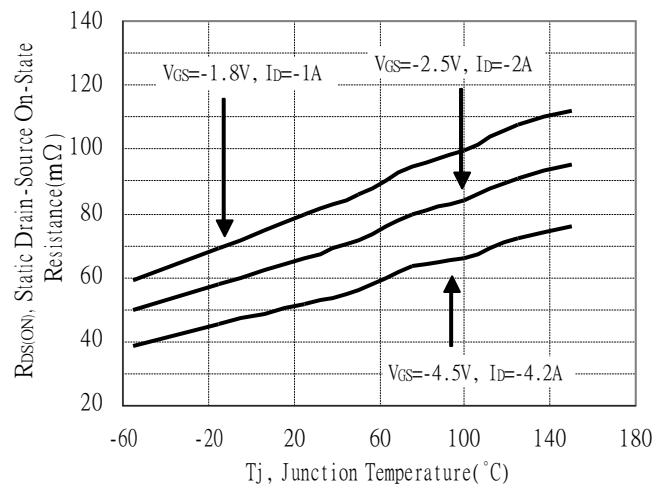
Reverse Drain Current vs Source-Drain Voltage



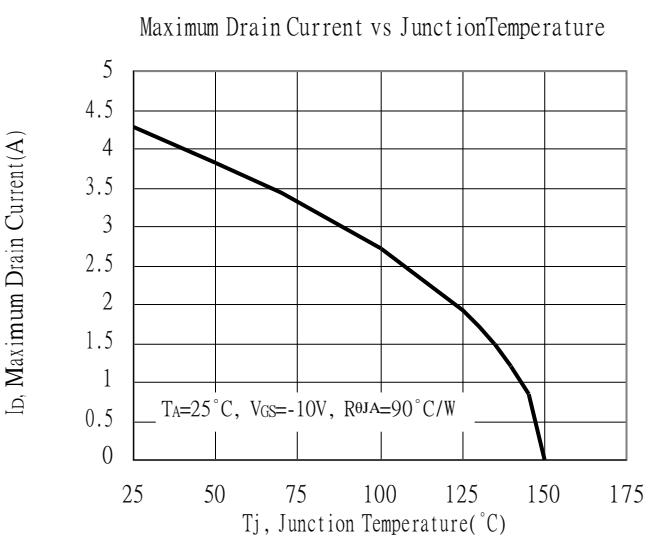
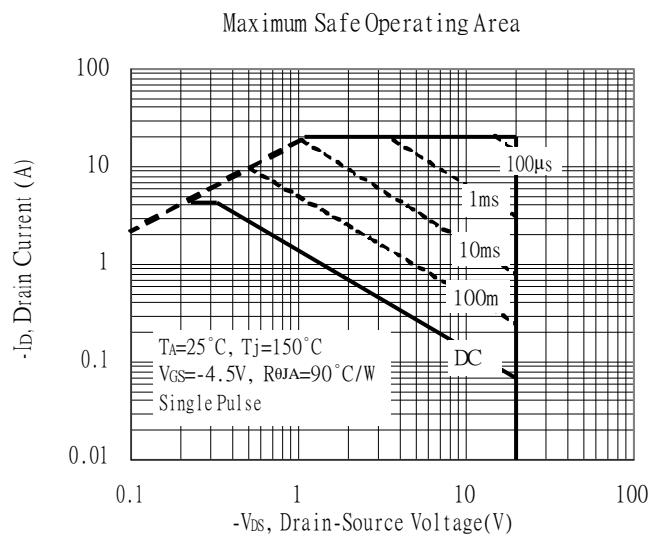
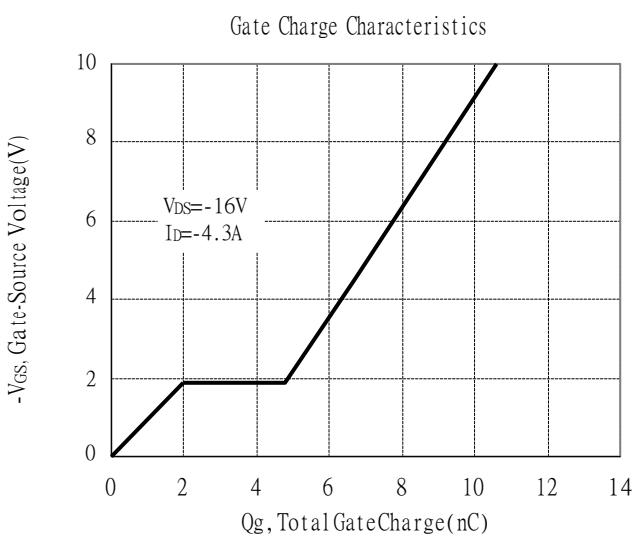
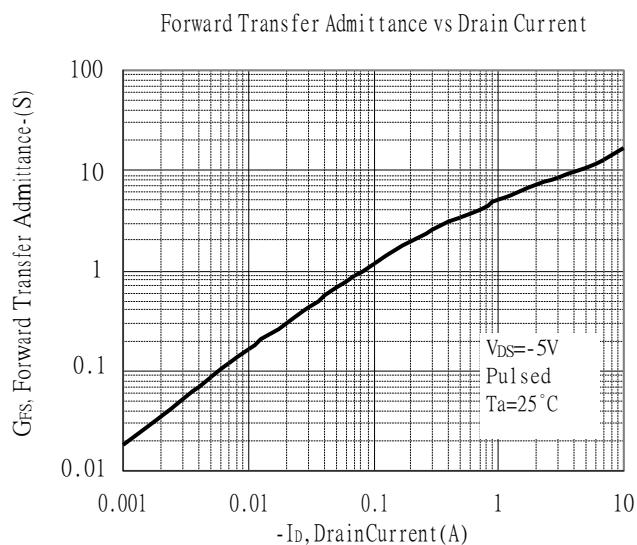
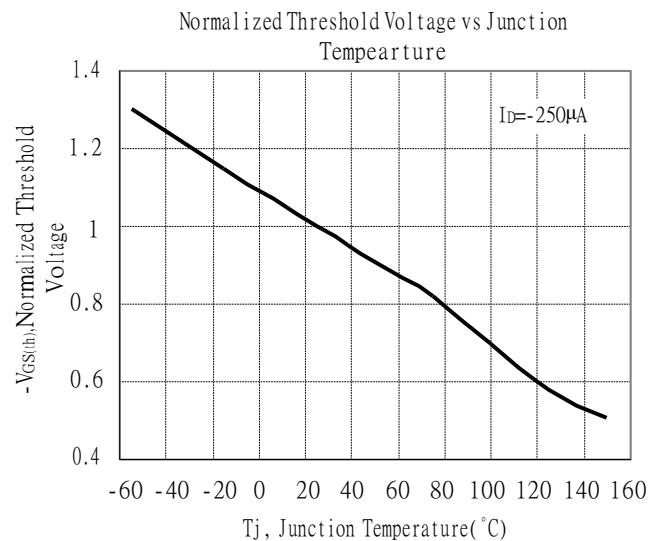
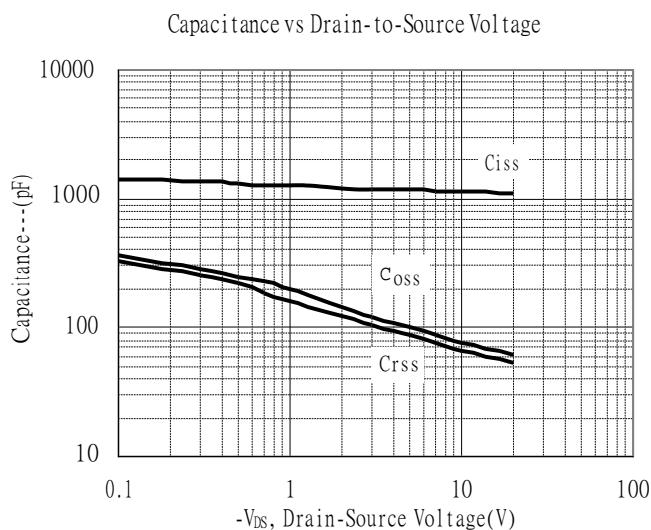
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

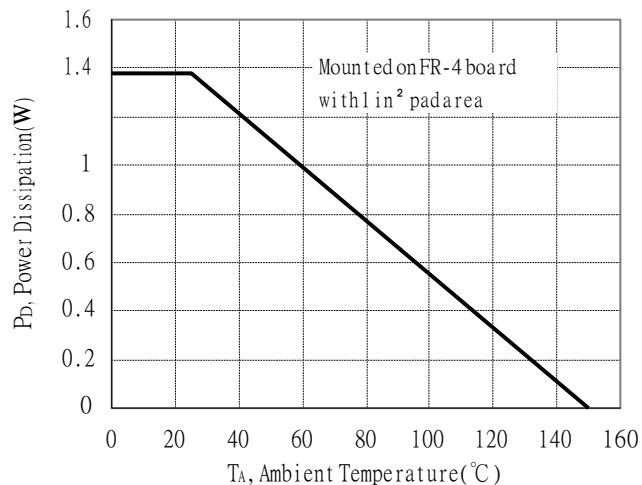


Typical Characteristics(Cont.)

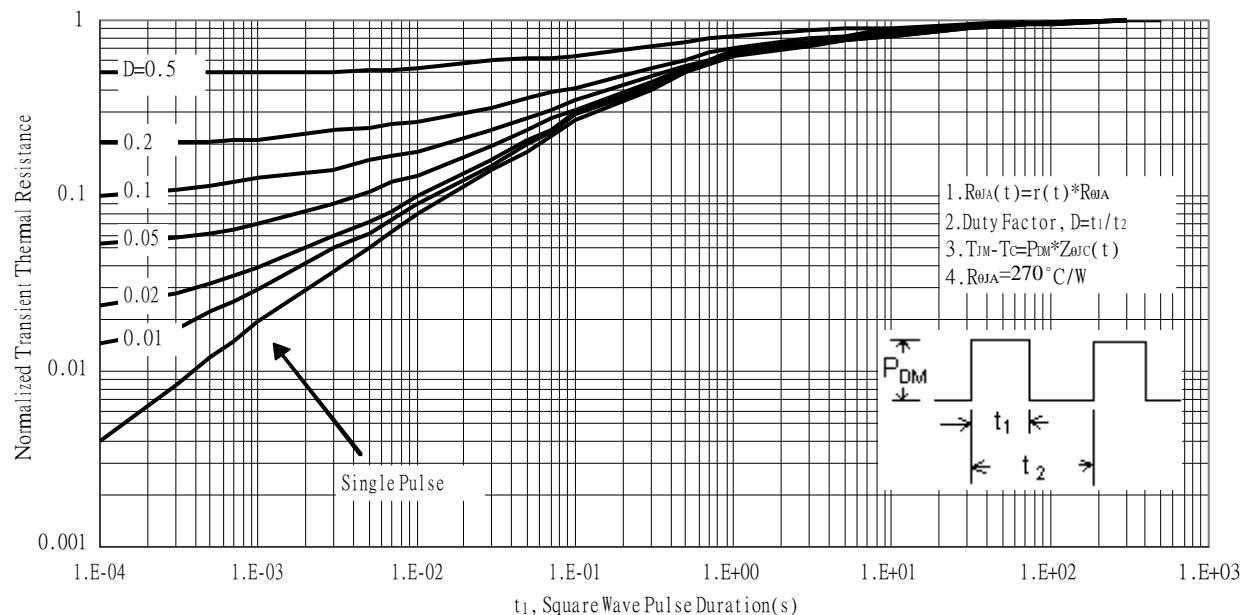


Typical Characteristics(Cont.)

Power Derating Curve

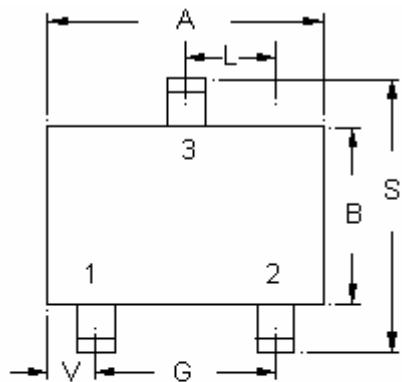
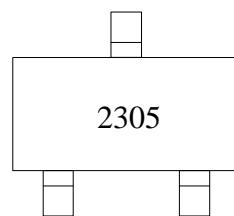


Transient Thermal Response Curves

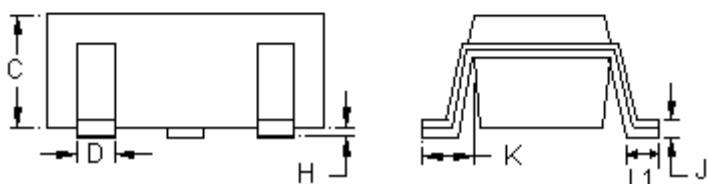


SOT-23 Dimension

Marking:



3-Lead SOT-23 Plastic
Surface Mounted Package
Code: N3



Style: Pin 1.Gate 2.Source 3.Drain

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50